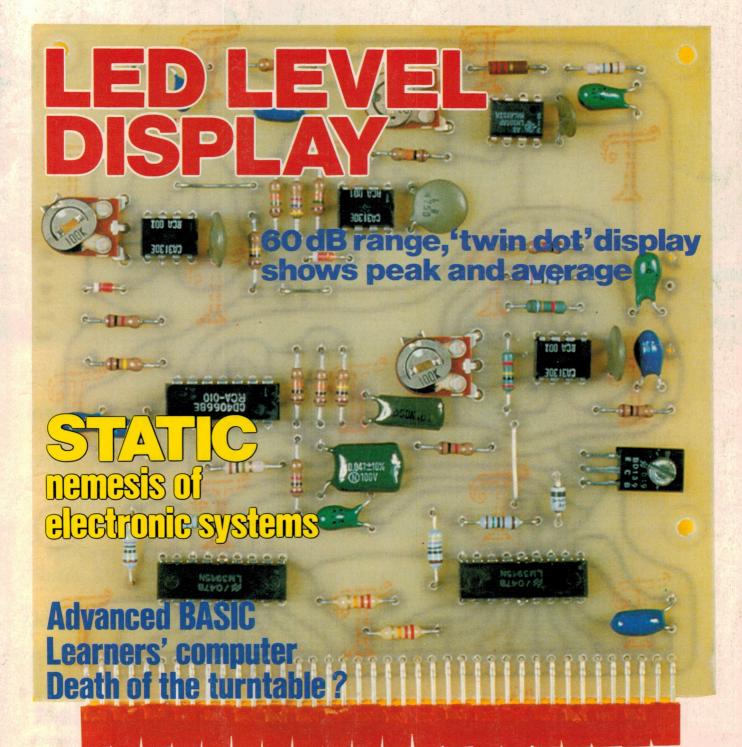
INSIDE — COMPUTING TODAY

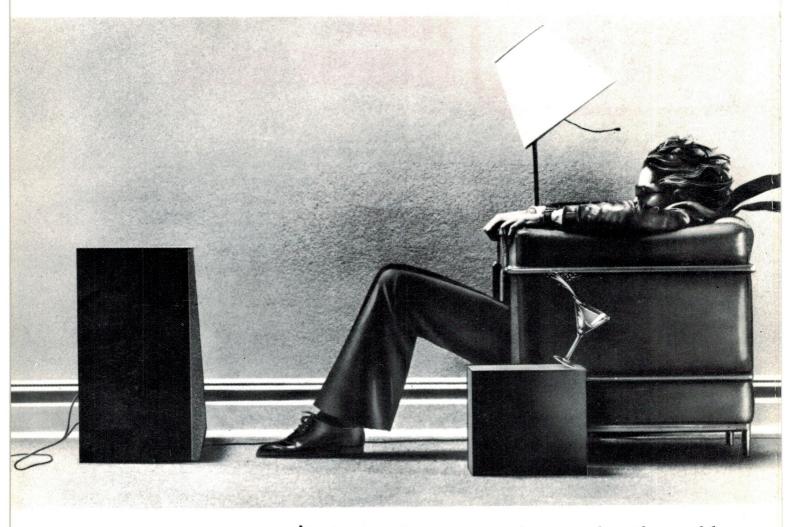
JUNE 1981 \$1.75*NZ \$2



ELECTRONICS TODAY INTERNATIONAL



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If your old favourites don't sound as good as they used to, the problem could be your recording tape.

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What can happen is, the oxide particles that are bound onto tape loosen and fall off, taking some of your music with them.

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HAGEMEYER

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POLICE RADAR — POLICE TRAPPED?

THE NSW POLICE announced that they would be introducing the KR-11 radar speed trap into their Highway Patrol operations just as we went to press this month. In the March issue, our feature article by Jon Brereton examined the operation of the Digidar 1 units then in use and the KR-11, which the NSW Police were then trialling. That article examined the technical operation of both types of radar speed trap and detailed the possible modes of failure of each.

We think the introduction of the KR-11 is a step in the right direction, but not far enough. There is still a problem with the use of the unit in that it can record a 'violating' signal where the vehicle producing it is out of sight of the radar's operator. The KR-11 does a crude spectrum analysis of the returned signals. Jon Brereton says, "Since it searches down in frequency, it will always pick the fastest target whose return is strong enough to satisfy the sensitivity conditions. The long and the short of it all is that the KR-11 picks not the strongest ('nearest') return but that corresponding to the fastest vehicle . . . it has 'thrown away' some information, but the hope is that it has retained a more useful piece of information."

While that seems fairly straightforward, it's not foolproof. In the March feature article, we published a list of the measured radar cross-sections of some 15 vehicles, ranging from a Honda motorbike to a Kenworth truck. The tables showed that there is an enormous variation, even between vehicles of a similar size. Jon Brereton commented:

"For reliable operation the onus is on the policeman to pick out which car is the fastest, which may present difficulties when it is recalled that the difference in distance between vehicles returning signals just strong enough to activate the device may be a factor of around ten.'

This may lead the radar operator to think that the vehicle he sees 100 metres away is speeding, when the radar is actually measuring the return from a speeding vehicle 1 km away!

That's the most drastic situation, admittedly, but it's by no means a remote possibility. Let's look more closely at the police's method of operation with the KR-11. We understand they propose to use KR-11s in the 'mobile operation' mode to measure the speed of vehicles approaching the patrol car on the opposite side of the road. In this mode the KR-11 uses a ground return signal to determine the patrol car's speed and then subtracts this from the target's apparent speed. The clear danger in this method of operation is that, as we have explained, the radar operator has to choose which car in his sight is the speeding vehicle, with the distinct possibility that he may not be able to see the vehicle actually causing the reading on the KR-11. Take the situation where a Peugot 505, the only car in sight, travelling at 100 km/h — within the highway speed limit — is seen by the patrol car approaching at a distance of 300 metres. An Inter Acco truck around 600 metres away travelling at 120 km/h would cause the KR-11 to indicate a speed violation, as the ratio of the approaching radar cross-sections of the two vehicles is 10 (March issue, p.16), giving equal radar returns where the larger vehicle is twice as far away as the smaller. At a distance of 600 m the Inter Acco truck may be easily missed and the patrol car would mistakenly pursue and book the Peugot 505. The NSW Police do not say how they intend to prevent such problems or avoid such situations.

Clearly, it is unfair to the motorist, but we must also point out that the NSW Police Department are being unfair to their members who operate the units — the fallibility of the KR-11 falls back on the police operator.

While the motorist who does not speed, yet is caught by circumstances similar to that described, is the prime loser, so are the police — especially if a number of motorists mount challenges in the courts, successful or not. Such litigation has already reduced public confidence in police radar operations. The KR-11, whilst a step in the right direction, does not remove the onus of proof from the operator when, with appropriately modified electronics, we are certain that it can. We feel that the innocent on both sides of the radar trap need not be trapped by lage Dann the trap itself.

Roger Harrison Editor



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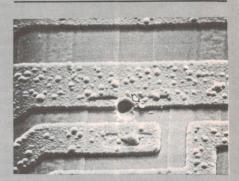
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The ETI-458 LED level meter up close! Photography and cover design by Ivy Hansen.

*Recommended retail price only

features



ELECTROSTATIC DISCHARGE -NEMESIS OF ELECTRONIC SYSTEMS 14

Electrostatic hazards are present during manufacture, assembly and use of electronic equipment, and can cause permanent and drastic damage to components. This article reviews the nature of ESD and how to avoid it.

SEEKING THE GLUON

26 Are gluons the fundamental particles that hold everything together?



LIQUID CRYSTAL DISPLAYS

LCDs are widely used in such things as watches, digital instruments, computer displays, even pocket TVs. This article explains how they work.

news

NEWS DIGEST

'Picture-frame' TV with flat tube?; Discount on 465B oscilloscopes; 200 watt high power bass/P.A. bin kit; Another ETI contest to come; etc.

Vector Graphics' VIP reviewed; Club call; Local micro support products at US prices; Uncovering more of the

COMMUNICATIONS NEWS

Updated Comni VHF receiver; The missing link; New FM transmitter from QEI; and more.

computing

COMPUTING TODAY

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IBM makes experimental dynamic RAM circuits using simple polycide technology; Computerland to sell Commodore business machines.

TALKING COMPUTERS -**NEW HOPE FOR THE BLIND**

A voice generator recently developed by the USA's National Institute of Health should soon be opening up job opportunities for the blind in the computer and allied fields

ADVANCED BASIC — PART 1

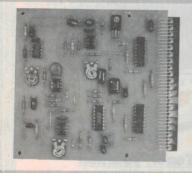
If you followed the previous series 'Back Door into BASIC', or picked up the rudiments of BASIC programming elsewhere, you're probably asking yourself: "Where do I go from here?" Phil Cohen's new series of articles on advanced BASIC will tell you.

projects



156: HI-Z INSTRUMENT PROBE

This probe will allow you to make CRO or frequency meter/timer measurements on high impedance circuits with waveforms having rise times as fast as three or four nanoseconds — and the cost is well below commercial equivalents.



458: LED LEVEL METER

This project is in fact the first part of the construction for the Series 5000 Control Preamp. However, the LED ievel meter described here is ideal for any application requiring a wide dynamic range level display. Naturally, two are required for stereo applications.

155: AUDIO DUMMY LOADS

Apart from a multimeter and perhaps an oscilloscope, a resistive dummy load of 4, 8 or 16 ohms impedance capable of dissipating up to 100 watts is just about the most useful item of test equipment the audio enthusiast could have. Here are several ways to build one.

660: LEARNERS' MICROCOMPUTER 103

This month we go into the 'architecture' of the microprocessor, an RCA CDP1802, detailing the functions of the various portions to gain an understanding of how our computer works.

sight&sound

SIGHT & SOUND NEWS

137

Film and TV School applications now invited; First trade audio exhibition; Selectavision under way, stereo TV to come, says RCA; etc.

OPTICAL DISC RECORDERS

142

The new technology of the digital disc 'played' by a laser pick-up must signal the eventual demise of the conventional record and turntable. Alan Concannon reviews the various applications and advantages of this revolutionary technology.

SANYO PLUS 75 RECEIVER REVIEW 148

Sanyo's new Plus 75 AM/FM stereo receiver is aimed at the higher end of the market, and according to Louis Challis its biggest 'plus' is that you can play it as loudly as if the rock band were in the room with you!



MARANTZ 'ESOTEC' SM1000 STEREO AMP REVIEW

154

Marantz' Esotec range has been designed to be the ultimate in hi-fi quality. The SM1000 amplifier lives up to that high standard, but you have to pay for such excellence!

general

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ELECTRONICS BOOKS FROM ETIBeginners' books, data books, circuit books, etc.

IDEAS FOR EXPERIMENTERS

'Broadcast booster' for AM band DXers; Meter for the ETI-560 mains cable seeker; Modification to ETI-640 VDU cures 'interlace jitter'; etc.

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next month



TECHNICS' SB10 LOUDSPEAKERS

We first introduced readers to Technics' revolutionary 'Honeycomb Disc' drivers, and the SB range of speakers, back in the May '80 issue and we have been hankering for some time to review the SB10s. What surprises will Louis Challis' spectral decay analysis have in store?

'CURRENT TRIP' CAR ALARM

Featuring entry/exit delay, this alarm uses the battery earth strap as a 'trip' sensor! It's easy to build and install, uses commonly available components, and does not suffer from false triggering problems.

LOGIC PULSER PROBE

Featuring five modes of operation — single pulse plus pulse trains of 1, 10, 100 and 1000 Hz — this versatile little test instrument can be fitted in a ball pen and may be used with CMOS or TTL circuitry. Mode selection is made by an ingenious touch switch and a seven-segment display indicates which mode you're in.

SERIES 5000 PREAMP - PART 1

To properly take advantage of the superlative performance of our widely acclaimed Series 5000 MOSFET Power Amplifier, David Tilbrook has designed a preamp/control unit to match. This article explains the philosophy and circuit techniques behind this top-line project. Features include a moving-coil cartridge preamp, two moving-magnet cartridge preamps, accurate RIAA compensation, a versatile tape monitor scheme, and the ETI-458 LED level meter for each channel.

Although these articles are in an advanced state of preparation, circumstances may affect the final content. However, we will make every attempt to include all features mentioned here.

FANTASTIC CROCOMPUTER KIT Microfice Build-it-yourse

*Software compatible with the very popular ZX80 *An ideal introduction to computer technology.

*The world's lowest price computer.

Unique component parts

The MicroAce is not just another personal computer. Quite apart from its exceptionally low price, the MicroAce has two uniquely advanced components: the powerful BASIC interpreter, and the simple teach yourself BASIC manual

The unique versatile BASIC interpreter offers

- remarkable programming advantages:

 UNIQUE 'ONE-TOUCH' KEY WORD

 ENTRY: the MicroAce eliminates a great
 deal of tiresome typing. Key words (RUN PRINT, LIST, etc.) have their own single-key
- UNIQUE SYNTAX CHECK. Only lines with correct syntax are accepted into programs. A cursor identifies errors immediately. This prevents entry of long and complicated programs with faults only discovered when you try to run them.



- EXCELLENT STRING-HANDLING CAPABILITY—takes up to 26 string variables of any length. All strings can undergo all relational tests (e.g. comparison). The MicroAce also has string input-to request a line of text when necessary. Strings do not need to be dimensioned.
- Up to 26 single dimension arrays FOR/NEXT loops nested up 26.
- Variable names of any length.
- BASIC language also handles full Boolean arithmetic, conditional expressions, etc.
- Exceptionally powerful edit facilities, allows modification of existing program lines.

Excellent value

For just \$199.00 you get everything you need to build a personal computer at home...PCB, with IC sockets for all ICs; case; leads for direct connection to a cassette recorder and television (black and white or colour);

everything!
Yet the MicroAce really is a complete, powerful, full-facility computer, matching or surpassing other personal computers at several times the price.

The MicroAce is programmed in BASIC, and you can use it to do quite literally anything, from playing chess to managing a business.

The MicroAce is pleasantly straightforward to assemble, using a fine-tipped soldering iron. It immediately proves what a good job you've done: connect it to your TV . . . plug in a mains adaptor . . . and you're ready to go.

Fewer chips, compact design

The MicroAce owes its remarkable low price to its remarkable design: the whole system is packed on to fewer, newer, more powerful and advanced LSI chips. A single SUPER ROM, for instance, contains the BASIC interpreter, the character set, operating system, and monitor. And the MicroAce 1K byte RAM (EXPANDABLE TO 2K ON BOARD) is roughly equivalent to 4K bytes in a conventional computer—typically storing 100 lines of BASIC. (Key words occupy only a single byte.)

ACCESSORIES TO MAKE YOUR MICROACE EVEN

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Dick Smith Plug Pack. 9V 600mA Cat: M 9560 \$12.95

brand to load and save programs Cat: A 4095 \$79.95

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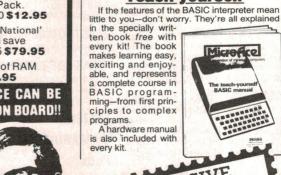
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Expansion kit to 2K of RAM Cat: K 6501 \$9.95

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clean

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Leads and Plugs. (For cassette and TV con-

Free BASIC programming book-course

Coaxial TV antenna connection switch.

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Teach yourself

P.C. Board with IC Sockets for all ICs.

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Recorder.

and power

RAM chips

Room for extra RAM

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Facts from Fluke on lowcost digital multimeters.

People who know electronic test and measurement equipment throughout the world recognize Fluke as a leader in the design and manufacture of precision instrumentation – products that speak for themselves in accuracy, reliability and engineering excellence.

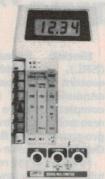
Now users of handheld 3½-digit multimeters are also getting to know us because of the wide acceptance of the 8020A Analyst, the world's best-selling handheld DMM, and the 8022A Troubleshooter, our basic-performance multimeter.

With the addition of the 8024A Investigator to the Fluke low-cost DMM line, we now offer three choices with three distinct levels of performance:

basic voltage/current/resistance functions; the added convenience of conductance for high resistance measurements to 10,000 Megohms; or all these func-tions plus K-type thermocouple compat-ibility, peak hold and a logic level/continuity detector.

As a product family, the Troubleshooter, Analyst and Investigator offer a unique combination of electrical performance, mechanical ruggedness and environmental endurance to users who want = the convenience





The Analyst

- Seven functions dc voltage ac voltage dc current ac current resistance diode test conductance (1/R)
- . 31/2-digit resolution
- 0.1% basic dc accuracy
- LCD display
- Overload protection
- Safety-designed

of a handheld DMM without sacrificing the accuracy and performance of a benchtop instrument.

> Simple, straightforward and easy on the eyes.

We've always thought a handheld DMM should actually work like one that is, the size and shape and placement of controls should allow true one-hand operation. Fluke handheld DMM's are strikingly simple in design with

uncluttered front panels where function and range combinations are clearly defined by color coding. A single row of eight trouble-free pushbuttons replaces the awkward rotary switches still offered on other multimeters.

The crisp, razorsharp 31/2-digit LCD readout in these three instruments features a wide viewing angle that you can see in bright sunshine or low ambient light.

Graduated with honors from the school of hard knocks.

All Fluke handheld DMM's feature tough, lightweight cases that stand up to the abuses of life in the field. Sturdy internal construction surrounded by a high-impact, flame-retardant shell make these units virtually indestructible. And all meet severe shock/vibration tests.

The shocking truth about overloading.

Like all Fluke Multimeters, our handheld DMM's are equipped with extensive internal protection against overloads and operator errors. Don't worry if you accidentally plug the test leads of

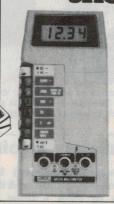
your Fluke DMM into a wall outlet while the instrument is in the resistance function. The same test could destroy a multimeter with lesser defenses. But a Fluke DMM comes through with flying colors. A simple case of survival of the fittest.

Our DMM's can stand up to this kind of punishment because a substantial portion of their components are devoted exclusively to reliability. The 8020A, 8022A and 8024A can withstand 500V on resistance ranges, and 1000V dc and 750 rms ac on all voltage ranges. In addition, the





The 8022A **Trouble** shooter.



Model 8022A: The Troubleshooter

- Six functions ac voltage dc current ac current resistance diode test
- 31/2-digit resolution
- 0.25% basic dc accuracy
- LCD display
- Overload protection
- Safety-designed

Investigator. Medel 8024A

The 8024A

The Investigator



- Nine functions ac voltage dc current ac current diode test logic level and continuity detect temperature (K-type thermocouple)
- Peak hold on voltage and current function
- Selectable audible indicator for continuity or level detection
- 3½-digit resolution
- 0.1% basic dc accuracy
- LCD display
- Overload protection
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El 5 digest

Flat tube is key to "hanging picture" TV

The long-awaited flat tube television which will eventually hang on the wall like a picture is to go into production next year.



Clive Sinclair has finally produced his long-time 'dream', a tiny, portable, go-anywhere-in-the-world TV receiver. It is built around his flat screen cathode ray tube - which has much wider application if development

AWA and Hughes in joint bid

Amalgamated Wireless (Australasia) Ltd and Hughes Aircraft of the USA recently announced that they are completing negotiations to form a team that will submit proposals to the Australian Government for the supply of satellite ground terminals.

AWA, with its Australian resources, would apply Hughes' technology to provide local Australian manufacturer support and engineering services.

Hughes' successful satellite system activity world-wide must place it in a strong position to bid competitively for an Australian system, and AWA, who established commercial beam wireless communications between Australia and Europe in 1927, are now aspiring to a major role in the new satellite system.

Research eastern England, programme on a flat TV tube that is just 19 mm deep.

First use of the revolutionary tube will be in a new pocket TV and FM radio that will sell for around \$100 when it is introduced next year. The 'Microvision' set will be produced in a highly automated plant at the Dundee, Scotland, factory of the Timex watch corporation.

first phase of a The \$10 million four-year capital investment programme will cover establishment of an advanced manufacturing facility at Dundee that will employ 250 people next year and be able to produce a million flat tubes a

By 1985, 1000 people will be working at an expanded plant that will have a capacity to make several million tubes.

Although the final design of the actual set has not been agreed it will measure just 152 x 101 x 25 mm. It will thus be about the size of a typical paperback book and easy to carry in the pocket.

The set, with its 76 mm diameter screen and total weight of little more than 100 grammes, will be able to operate to the transmission standards of most parts of the world. (The exception at present is the French standard).

Mr Clive Sinclair, founder of Sinclair Research, says a number of technical advances forming glassware, a volume personal mobile teletext unit.

Ltd reduction of two and a half times (SRL), of Cambridge in over the size of a conventional has tube, while the tube is brighter announced the successful than normal and needs less completion of a five-year power to operate it. The TV set research and development will run on transistor radio batteries.

> Sinclair Research chose to have the new tube and sets made alongside each other in Scotland after an international search for a manufacturing base. One other location seriously considered was Hong Kong.

> Mr Sinclair says all the development work on the tube, set and automated machines needed to produce them has now been completed and if the target production rates to be introduced in a year's time are met, the number of minutes needed to produce a set will be the fastest yet achieved anywhere. A pilot plant in Cambridge has already been producing flat screen tubes for the past 18 months at the rate of up to 50 a day.

> The Microvision pocket set, however, is just an example of things to come, predicts the Sinclair chief. He says the basic flat tube can easily be modified for projection TV systems and he foresees the day when three small tubes will be used in a 1.27 metre diagonal screen full-colour projection TV that will hang on the wall.

> The optics and electronics for it would fix into a shoebox-size unit that would be mounted above the screen to project the pictures onto it.

The flat tube might also be have been achieved in the linked up to the Sinclair development of flat screen TV personal computer to give it a tubes. These include perfection new visual dimension and toof a new method of vacuum gether they could be used as a



CSIRO help the handicapped

The CSIRO Division of Manufacturing Technology in Adelaide has developed an automated microfiche reader which allows severely immobilised people or those with impaired manual skills to read books by using simple touch controls.

The device is to be launched on the commercial market by R.W. Bowman Manufacturing Pty Ltd.

Some people, because of physical handicaps, cannot use conventional microfiche readers as they require some manual skill to move cards in and out and select a particular page. The automated reader incorporates a microprocessor electronic control and allows such people to use a conventional machine through simple touch controls.

LCD work wins award

UK Professor Cyril Hilsum has received the American 1981 David Sarnoff Award of the US Institute of Electrical and Electronics Engineers for his work in the development of a range of superior liquid crystals for use in digital watches and calculators.

Hilsum ordinated the UK research as LCDs, and he expects further

Prof. Hilsum has a back- both defence and civil purposes.

ground in infrared and has co- semiconductor research as well programme on flat panel elec- research and development to tronic displays, which has given produce new types giving even that country a leading market higher performance for such position in liquid crystal applications as flat-screen displays that will be needed for

Silicon still has it over GaAs ...

That's the consensus of proponents of both who gathered at the first High Speed Digital Technologies Conference in San Diego, California, in January.

Although advances in GaAs the undersea light-wave systechnology are coming quickly, tems development department. advances in silicon technology will continue to keep it ahead, siderable attention to improving delegates agreed.

and TRW, among others, are Electronics Research Centre in developing GaAs technology, Thousand Oaks, California, reboth reported work on silicon ported on its 8-by-8-bit integrated circuits. There were, multiplier that puts more than a of course, advances reported in thousand gates on a chip. GaAs technology from, for International.

Reliability, ease of manufactur- grated circuits. ing and better integration levels

major factor in Bell Labs' choice seconds! of bipolar technology for a oceanic fibre-optic cable to be installed in 1988.

ability level for what we need," as soon as possible. says David Ross, an engineer at

Researchers are giving conthe densities of GaAs ICs, and Although Bell Laboratories Rockwell International Corp's

Meanwhile the hottest news at example, Fujitsu and Rockwell March's International Electron Devices Meeting in Washington, Designers of communi- USA, may well be that silicon cations systems may look can compete very effectively longingly at gallium arsenide, with gallium arsenide in the but they're settling for silicon. processing of high-speed inte-

Researchers at American give it the edge over faster but Telephone & Telegraph Corp's difficult GaAs as the mainstay Bell Laboratories in Murray Hill, material for high-speed digital New Jersey, have succeeded in circuitry for the foreseeable producing a MOS ring oscillator with one micrometre line widths The reliability of silicon was a that will operate at 40 pico-

They have also devised a signal regenerator for a trans- 2.5 GHz divide-by-eight counter.

Plans are to take this technology into the large-scale and "No one knows anything very large-scale integration about gallium arsenide's reli- arenas for memories and logic

... But GaAs device price to drop?

Scientists at the Massachusetts Institute of Technology's Lincoln Laboratory in Lexington, Mass., have developed a technique that may greatly reduce the cost of gallium arsenide semiconductor material.

It might also make possible substrate, creating a thin slab of new sorts of integrated circuits high-quality crystal. with thin, single-crystal films on The same substrate is used insulating substrates, or in repeatedly to grow slabs 5 to multiple layers, according to its 10 µm thick and comparable in developers.

Called Cleft (cleavage of with melt-grown GaAs. lateral epitaxial films for transphase epitaxy and then cleaved drastically cutting cost. parallel to the plane of the

their electrical characteristics

With Cleft, the usual GaAs fer), the approach uses — and substrate, typically 250 to reuses — a GaAs substrate that 400 µm thick, is unnecessary, is bonded to glass. Single-saving all but a few per cent of crystal GaAs is grown on this the GaAs normally needed for a substrate through vapour-solar cell or IC and therefore

MEWS digest



Another great contest coming!

Thousands of readers so enjoyed the contests we have run over the past year that we have received many requests for more and next issue your wishes will be granted!

Radofin Electronics (Aust.) have offered one of their Adam 180 Teletext adaptors, which we ran a news item about on these pages in the April issue, as a contest prize.

To refresh your memory, the Adam 180 adaptor is an add-on unit that can be used with any colour or B&W TV set and months' warranty.



Teletext broadcasts contain features both VHF and UHF information on weather, traffic, input, Teletext at the touch of a TAB results, share prices, button, Teletext superimposed currency rates, food prices, over normal TV reception, theatre, travel etc. The retail double height characters, clock value of the Adam 180 is \$575. and controller, subtitle and It's going to be a great contest, newsflash capability, handheld and remember - you've got to remote control unit and 12 be in it to win it! Don't miss the July issue of ETI.

Lightweight 50 MHz oscilloscope

The Iwatsu SS3510 Service Oscilloscope has recently become available from Tech-Rentals Pty Ltd.

The SS3510 is a compact, leased their would not be practicable.

hood, leatherette cover and a full operating and service

Tech-Rentals also recently re- St, Crows Nest NSW 2065.

1981 lightweight, full specification catalogue, which lists over 300 50 MHz oscilloscope with dual different types of equipment trace and delayed sweep. It is plus specifications from the suitable for use with advanced world's leading manufacturers. electronic equipment, yet its size This equipment is available for and portability are said to make short- or long-term rental with it ideal for situations such as purchase option. Equipment field service and commission-listed includes oscilloscopes, ing, where a large oscilloscope logic analysers, digital multimeters, spectrum analysers, The Iwatsu SS3510 is priced chart recorders, data loggers, at \$1795 plus tax and comes signal generators, sound level complete with probes, viewing meters and computer terminals.

Tech-Rentals Ptv Ltd can be manual. It is also available for contacted at 83-87 Wellington rental or on a rental/purchase St, Windsor Vic. 3181, (03) 51-1303, and at 68 Alexander

Static protection questions and answers

The Charleswater organisation, which specialises in static protection of electronic devices, has prepared a four page publication on the subject, in question and answer form, available from their Australian distributor, Royston

about? And why has it become damage. of such great interest in the last two years?

as CMOS and MOS integrated fifty to sixty per cent per year. circuits, from burn out or this issue.

MOS technology. While the use boards. of MOS technology results in a

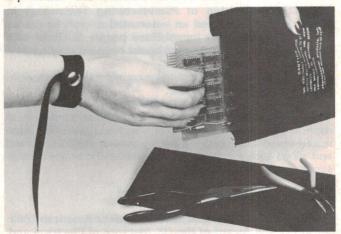
unprotected and therefore is 709-5293.

What is static protection all sensitive to static discharge

The great interest generated in recent years is due to the Static Protection refers to pro- rapidly expanding use of such tection of discrete devices, such devices, growing at the rate of

An example from the wide degradation due to static elec-range of Charleswater static tricity - see the feature article in protection products, marketed the brand under Static protection has become STATFREE, is the CP302 conof great interest because of the ductive nylon bag. This is increasing need for higher cir- designed for packaging static cuit densities in microelectronic sensitive materials such as components, and the use of CMOS, MOS and printed circuit

Information on this and other device which has greater circuit static protection products, and density and lower costs per unit, copies of the publication, are the penalty is increased sensi- available from Royston Electivity to electrostatic discharge. tronics, 27 Normanby Road, Even with the addition of Notting Hill, Vic 3149, (03)543buffer circuits on the input of 5122; and 15/59 Moxon Road, MOS devices the output is left Punchbowl, NSW 2196. (02)

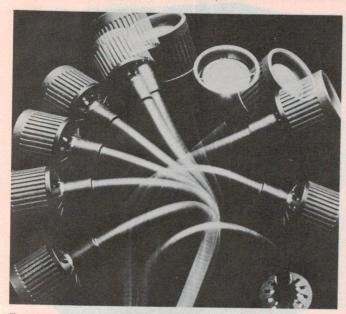


New shapes in LEDs

Four new shapes of Philips' LEDs are now available: plus, minus, triangle and square.

Each shape can be obtained have a body diameter of 5 mm in three colours — 'super-red', and incorporate a lens. green and yellow, designated 1 mcd at 10 mA, and the LEDs 427-0888.

For further details contact (super-red), Philips Electronic Components CQX65-68 (green) and CQX75- and Materials, 67 Mars Rd, 78 (yellow). Luminosity is Lane Cove NSW 2066. (02)



Low-voltage work lamp

The halogen bulb of this 12 V lamp only draws 20 W, yet has an output of 4400 lux at 40 cm, compared to only 850 lux from a standard 80 W bulb at that distance, making it ideal for work which requires intense light.

The rubber-sleeved flexible arm can be adjusted through 360° vertically or horizontally, and the lamp housing is small so as not to obstruct the work area. The lamp base has a switch and can be supplied in various mounting forms: screw on, C-clamp, magnetic, table base.

Contact Consolidated Ultrasonics (A'asia) Ptv Ltd. 77 Allen St, Leichhardt NSW 2040, (02) 569-5022, for catalogue and further information.



Headhunting among the kit managers



Mr. Denis Fanna has been appointed manager of Bill Edge's Electronic Agencies' newly-formed kit depart-

Denis has diplomas in Radio and TV servicing and comes to Electronic Agencies from the Dick Smith stable where he spent two years in the kit section.

Bill Edge boasts, "With the expertise of Denis and some great ideas we have, our kits will soon be the best in Australia."

Electronics Today Limited

It has been brought to our attention that a company in Britain has been trading under the title 'Electronics Today

'Electronics Today Limited' has not now, nor has ever had, any connection of any sort with this magazine, nor with any other edition of Electronics Today International. If any Australian readers or companies have had dealings with Electronics Today Limited they may find it to their advantage to telephone ETI's Managing Editor (Collyn Rivers) on (02)33-4282.

New displays now available

Following the recent association between C & K of Australia and The Staver Co. Inc. of America, a range of numeric/ alphanumeric displays is available under the American trade name "Signalex".

The displays, predominantly seven-segment numeric, range in size from 40 mm (11/2") high to a giant 610 mm (24") high. They offer bistable electromagnetic operation and draw no power except when changing.

Seven display sizes form the range, offering white vanes on matt black background as standard with some "Dayglow" colours as optional. Operating electronic scales, petrol pumps, voltage of 12 volts dc is traffic controls, clocks, etc. standard, although other voltages can be made to order.

tion in time and temperature 101, Merrylands NSW 2160. signs, price signs, scoreboards, (02)682-3144.



For further information please contact C & K Elec-These devices find applica-tronics (Aust.) Pty Ltd, P.O. Box

C & K switch catalogue

C & K of America have just released a new, greatly expanded switch catalogue, outlining the total range of electro-mechanical switching devices they manufacture.

The range of more than throughout the world. 1 000 000 switches has been even further expanded, and pages and is available free of C & K factories are now charge from C & K Electronics operating in Hong Kong and (Aust.) Pty Ltd, P.O. Box 101, England in order to maintain Merrylands NSW 2160. (02) the same level of service 682-3144.

The catalogue contains 71

Discount on 465B oscilloscopes

Tektronix recently announced a major discount on the range of 465B, 100 MHz oscilloscopes, claimed to be one of the most popular 100 MHz scopes in Australia with close to 1000 units currently in use.

The discount is due to over- • 100 MHz at 5 mV/div. stocking, and Tektronix want to • 2 ns/div. sweep rate with x10 clear their supply before the end sweep magnifier of the financial year.

The selling price has been re- • Versatile trigger selection duced by \$400 to \$3150 plus • Alternate sweep sales tax, until current stocks are cleared.

Features of the 465B are:

- Trigger view

For further information contact your local Tektronix sales office.

MEMS digest

200 watt 'bin' kit

In response to a strong demand, Jaycar has produced a high power bass/P.A. bin suitable for pop groups, discos and other applications requiring high power sound

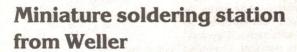
The bass-reflex enclosure Canon a massive 18" (457 mm) diameter bass driver necessary at a small extra cost. conservatively rated at 200 watts RMS. This unit is including tax. This price repreknown Celestion organisation. commercial built-up units. The handled by two Motorola piezo grille removed for clarity.

To keep the cost down the illustration) includes speakers, pre-cut cabinetry, 264-6688.

connector, measuring 590 mm wide x pieces, heavy duty feet, grille, 390 mm deep x 730 mm high, screws etc. The handles and carpet covering are available if

The cost of the kit is \$495 made in England by the well-sents a considerable saving on The top end of the system is illustration shows black mesh

For further information consystem is available in kit form. tact Gary Johnston or Lyndsay The kit, (shown built-up in the Clout at Jaycar, 380 Sussex St, all Sydney 2000 NSW. (02)



The WMCP-EC miniature soldering station was recently released by the Cooper Tool Group under the Weller brand of soldering equipment.

requiring soldering.

mits the actual tip temperature 21-6766.

Eight miniature soldering tips to an evaluator. The working are available, and the station is temperature is continuously said to be ideally suited to variable between 40° and microcircuit technicians, watch- 450°, with the set temperature and other users automatically maintained within precise micro- a tolerance range of ±2°C.

For further information con-The WMCP-EC features a tact a Weller distributor or completely new concept in the Albury Sales Office of the temperature control. A sensor Cooper Tool Group, P.O. Box acting as a transducer trans- 366, Albury NSW 2640. (060)

Bang, bang, you're soldered?

No, it's not the new slimline western six-shooter and holster - Scope Laboratories of Melbourne recently released a new version of their 60 W rechargeable portable soldering iron.

A green LED indicator at the rear of the tool warns when the unit needs recharging, and internal circuitry changes have reversed polarity for greater safety when recharging from dc. in a vehicle.

The standard leather holster has also been upgraded to provide greater protection from obtained from Scope Laboraresidual heat and the relatively tories, P.O. Box 63, Niddrie Vic. sharp profile of some tips.



Further information can be 3042. (03)338-1566.



New NiCad battery charger

The PS519 Universal Nickel-Cadmium Battery Charger from A & R Electronics, marketed under the Arlec brand, incorporates a number of novel features.

It will accept a total of five power is from 240 V 50 Hz ac above each battery position is a pack ac adaptor. LED which lights during charging, providing an instant check battery testing facility. Charging (03)89-0661.

batteries of varying sizes, and mains through an Arlec plug

Further details are available that the battery is correctly from A & R Electronics Pty Ltd. connected. There is also a 30 Lexton Rd, Box Hill Vic. 3128.

High-voltage electrolytics

Philips has developed several new series of electrolytics for use in high-voltage circuits such as in televisions, monitors,

mentary series, the 041 being general purpose types with a capacitance range from 1 uF (385 V) to 22 uF (160 V), and (385 V) to 100 uF (160 V).

The 052 series is an ex- 427-0888.

041/042/043 are comple- tension of the 050 series and includes long-life types ranging from 47 uF (385 V) to 1000 uF (250 V).

For more information contact the 042 and 043 being long-life Philips Electronic Components types ranging from 10 uF and Materials, 67 Mars Rd, Lane Cove NSW 2066. (02)

Short-form catalogue from Swann

Swann Electronics' detailed short-form catalogue has recently been released to assist all manufacturers with their switch and neon indicator design and applications.

Swann manufacture the largest range of appliance and lighting switches in the Southern Hemisphere. If you would like a copy of this catalogue, contact Swann Electronics Pty Ltd, P.O. Box 350, Mt. Waverley Vic. 3148.

Audio enthusiasts — you have not been forgotten. We now have a fantastic range of audio kits for most applications.

Here is a slection of the fine range of kits available from us:-

Amplifiers:

ETI 480/100 (See ETI Dec. 1976)

Complete 100W RMS Amp including deluxe anodised cabinet, handles, connectors, etc. Built for heavy road use. Very popular as slave or main amp. \$130.00

ETI 480/100 Preamp

All componentry to build unit for 2 x Guitar or mic. inputs. Has separate level controls, Bass, Treble and Master Vol. Control. Can be built into ETI 480/100 case above. \$30.00

Both of the above make one of the most popular road amps in use today.

ETI 480/50 watt module \$23.00 ETI 480/100 watt module \$27.00

(Please do not compare these modules with other inferior modules. Ours have special heavy gauge pre-punched heatsink brackets — essential for high power use.)

ETI 466 300W AMP MODULE (ETI Feb. 1980) Rugged 300W RMS Amplifier module which has become the "Industry Standard". The Jaycar module has been especially reconfigured so that it will fit vertically into a standard 51/4" rack box. Don't be misled by others, theirs is too wide for vertical insertion into a standard rack box. The Jaycar module has not been altered electrically in any way from the original design. \$70.00

JAYCAR 3002 (2 x 300 watt RMS Amp)

This magnificent kit features 2 x 466 modules in a specially built 19" rack case, Dual power supplies and Philips 65D heatsinks (the best). The number of 3002's now built & giving reliable service on the road is growing daily. Around half the price of built-up commercial units. \$489.00 Complete down to the last nut and bolt.

KEYBOARD INSTRUMENTS

Jaycar 88-note touch sensitive Piano. Complete kit including professional 71/4 octave keyboard, built-up cabinet, etc., only \$589.00 plus \$25.00 sent anywhere in Australia.

Jaycar String Synthesiser, four octave C to C (49 note) split keyboard, transposable pitches. C-B-Bb-Eb. Complete Kit contains all cabinetry, components, etc. \$445.00 plus \$20.00 freight.

Keyboard Special! Kimber-Allen

61-note keyboards. Can be used in the E.A. 760 Electronic Organ (Ref. E.A. June '76). Professional English made keyboards at a crazy price, only \$99.00 each plus \$15.00

Ideal for organs, synthesisers, or any electronic keyboard experimental work.

Shop Hours: Mon - Sun -9AM - 5.30PM



380 Sussex St Sydney 2000. Ph 264 6688

\$20.00 freight.

plus

E. A. Graphic Analyser Kit (E. A. March '81)

Complete kit of parts including transformer, modulator, Xtal front panel, etc. Colour Option \$89.00 \$20.00

Horwood Box to suit E.T.I. 477 Mosfet Amp Module (E.T.I. Jan-March '81)

Complete kit including high voltage
Hitachi Mosfets, fibreglass P.C.B.,
pre wound coil, etc.
Cast front panel \$59.00 \$39.50 Power Transformer \$34 50

Other components for 5000 amp available 'Le Gong' Kit (Refer E. A. March '81) \$13.95

'P. C. Birdies" Kit (Refer E. A. May '81) only

\$14.95 Contains all parts, including Jiffy Box, I.C., P.C.B.

2 Channel Infrared Remote Control Kit (Refer E. A. May '81) Complete kit of parts \$65.00

Sound Level Meter Kit (Refer E. A. May '81) Complete kit of parts for this project including high quality electret mic. insert. \$39 50

P.C.B.'s for many other current projects available.

UHF Masthead Amp Kit (Refer E.T.I. April '81) Complete kit of parts including fibreglass boards OM350, power supply, etc. ...only \$35.00

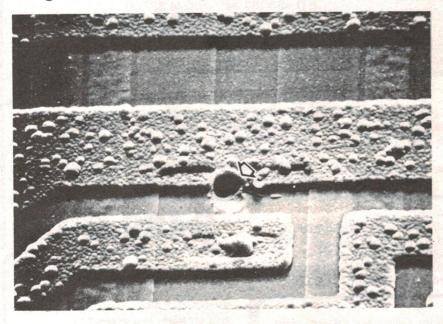
P&P CHARGES:

ORDER VALUE CHARGES S 5 S9 99 S 10 S24 99 S 25 S49 99 S 50 S99 99 \$1.00 \$2.00

Electrostatic discharge — nemesis of electronic systems

D.E. Frank

Electrostatic hazards to electronic equipment are known to be present at manufacturing facilities during the assembly and testing of electronic systems as well as during shipment and handling at the user's facilities. In order to protect these systems, it is necessary to understand the nature of electrostatic discharge (ESD), how it is generated, and how it is transmitted to electrostatic discharge sensitive (ESDS) components. Additionally, it is necessary to understand which parts are ESDS, how they are damaged, and the subsequent effect on the respective system.



Close up view (x1000) of electrostatic damage in an op-amp. (See the series of pictures on pages 16-17).

What is ESD?

OUR MOST common conception of electrostatic discharge is the miniature lightning bolt or shock we receive in periods of low humidity when we walk across a carpet or slide across vinyl seatcovers and then discharge to a door knob or door handle. In essence, however, most static electricity is subliminal or occurs at values well below our perception level of 1500 to 2000 volts.

Static electricity is generated whenever two substances are rubbed together, separated, or flow relative to one another (such as a gas or liquid over a solid). This static electricity (electrical charge at rest) is stored on nonconductive materials and tends to remain in the localised area of contact

This article is reprinted with permission from Douglas Service magazine, Volume XXXVII, July/ August 1980. awaiting the opportunity to discharge to the first available ground source. In the case of conductive materials, the charge is rapidly distributed over the entire surface and the surface of other conductors which come in contact.

The Triboelectric Series (Table 1) shows the charge relationships of many materials. Note that cotton is identified as a reference material, being at midpoint of Table 1. It tends to absorb moisture, thereby rendering it somewhat conductive. However, when cotton is rubbed against another material, it has the ability to produce a static charge.

Materials listed above cotton tend to assume a positive charge by giving off electrons in a friction separation situation, while those listed below cotton become negatively charged by acquiring electrons. When any two materials experience separation or rubbing, the material listed highest on the table will become positively charged, and the material listed lower will accept the negative charge.

For the sake of simplicity, let us define the cause of common static as the flow of materials and people within an environment. Materials include all components, packaging, and other raw materials which make up our finished product. People carry and generate charges, and it all takes place within a defined environment made up of facilities and equipment. The environment is not limited to a plant, but can be defined as a package, or many plants as in a transport situation.

Static, as it manifests itself in our environment, is actually a symptom. If we can impose control on the elements which create static as an end result, we can control the generation of a myriad of problems caused by static.

It is the author's intent to present an understanding of ESD, to explain how various materials and situations generate ESD, and to discuss ESD sensitive devices in depth. In the discussion of ESD sensitive devices, an effort has been made to present a thorough "physics of failure" analysis to provide insight into the design and structure of ESD sensitive devices as well as the failure modes and effects. Those individuals not desiring an indepth technical treatise may review Tables 2 and 3, and also the discussion in paragraphs entitled "Detecting ESD Damage" and "Protecting Equipment".

ESDS devices

Typical ESD voltages are shown in Table 2, and the ranges of susceptibility of ESDS devices are shown in Table 3. Parts of devices can be destroyed (hard failures) or simply degraded or made intermittent (soft or upset failures) due to exposure to electrostatic discharge. Parts are susceptible to damage when an ESD occurs across their terminals. ESDS parts can be destroyed by an ESD where one pin is connected to a high voltage source and other pins are ungrounded. In other words, a hard ground connection is not required to destroy an ESDS part.

MOS large scale integration devices in hermetic packages with nonconductive lids could be damaged by

A CONTRACTOR OF THE PARTY OF TH	
MATERIALS AIR HUMAN HANDS ASBESTOS	
RABBIT FUR GLASS	tive
MICA HUMAN HAIR NYLON	ncreasingly Positive
WOOL FUR	ingly
LEAD SILK	creas
ALUMINUM PAPER	Lever Az Consultation
COTTON STEEL	
WOOD AMBER	
SEALING WAX HARD RUBBER	9
NICKEL COPPER BRASS SILVER	ncreasingly Negative
GOLD PLATINUM SULFUR	N VIE
ACETATE RAYON POLYESTER	asing
CELLULOID ORLON	Incre
SARAN POLYURETHANE	your sense
POLYETHYLENE POLYPROPYLENE	HE PAR NAME TO THE
PVC (vinyl) KEL-F (CTFE)	* * * * * * * * * * * * * * * * * * * *
SILICON TEFLON	

Table 1. Triboelectric series

Means of Electrostatic Voltages	
10 to 20%	65 to 90%
Relative Humidity	Relative Humidity
35 000	1 500
12 000	250
6 000	100
7 000	600
20 000	1 200
18 000	1 500
	10 to 20% Relative Humidity 35 000 12 000 6 000 7 000 20 000

Table 2. Typical electrostatic voltages

spraying the lids with canned coolant, despite there being no ground path connected to the part.

ESDS parts installed in assemblies normally have their leads connected to a sufficient mass of conductive material, such as printed circuit board (pcb) runs and wiring, which may provide the required ground to result in damage from an ESD. In such cases, however, the voltages required are normally higher than those needed when one or more pins or the part case is grounded.

Assemblies and equipment containing ESDS parts are often as sensitive as the most sensitive ESDS part which they contain. Incorporation of protective circuitry in these assemblies and equipment can provide varying degrees of protection from ESD applied to their terminals. Such assemblies and equipment, however, can still be vulnerable from induced ESD caused by strong electrostatic fields or by contact of pcb electrical connections or paths with a charged object.

Intermittent or upset failures can occur on certain types of parts, such as LSI memories and chips, either prior to or after lidding and sealing. Such failures can also occur when equipment is in operation, characterised by a loss of information or temporary distortion of its functions. No apparent hardware damage occurs and proper operation resumes automatically after the ESD exposure or, in the case of some digital equipment, after re-entry of the information by resequencing the equipment.

Upset can also be the result of an ESD spark in the vicinity of the equipment. The electromagnetic pulse generated by the spark causes erroneous signals to be picked up by the equipment circuitry. Upset can also occur by the capacitive or inductive coupling of an ESD pulse or by the direct discharge of an ESD through a signal path providing an erroneous signal.

While upset failures occur when the equipment is operating, catastrophic failures can occur any time. Cata-

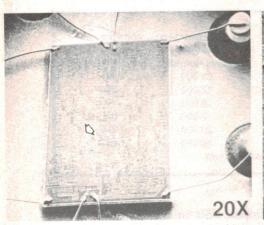
strophic ESD failures can be the result of electrical overstress of electronic parts caused by an ESD, such as: a discharge from a person or object, an electrostatic field, or a high voltage spark discharge (see Figure 1).

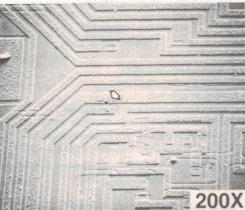
Some catastrophic failures may not occur until some time after exposure to an ESD, as in the case of marginally damaged ESDS parts, which require operating stress and time to cause further degradation and ultimate catastrophic failure. Only certain part types seem to be susceptible to this latent failure process.

There are some types of catastrophic ESD failures which could be mistaken for upset failures. For example, an ESD could result in aluminium shorting through a Si02 dielectric layer. Subsequent high currents flowing through the short, however, could vaporise the aluminium and open the short. This failure may be confused with upset failure if it occurs during equipment operation, but the damage due to the ESD would cause a latent defect that will probably reduce the operating life of the part.

Parts that are very susceptible to ESD upset are any within logic families that require small energies to switch states or small changes of voltage in high impedance lines. Examples of families that are sensitive would be NMOS, PMOS, CMOS, and low power TTL. Linear circuits with high impedance and high gain inputs would also be highly susceptible, along with RF amplifiers and other RF parts at the equipment level; however, design for RFI immunity can protect these parts from damage due to ESD high voltage spark discharge.

To protect parts sensitive to ESD high spark discharge at the equipment level requires: good radio frequency interference (RFI)/electromagnetic compatibility (EMC) design, buffering of busses, proper termination of busses, shielding of buss conductors, and the avoidance of penetrations of the equipment enclosure that lead to sensitive parts.





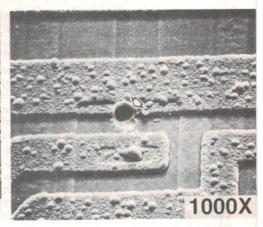


Figure 1. Static discharge damage in an op-amp integrated circuit (arrows show location of damage).

Failure mechanisms

Typical ESD failure mechanisms are divided into the two following categories. Those in the first category, thermal secondary breakdown, metallisation melt and bulk breakdown, are power dependent. Those in the second category, dielectric breakdown, gaseous arc discharge, and surface breakdown, are all voltage dependent. All of the above are applicable to microelectronic and semiconductor devices. Metallisation melt and gaseous arc discharge are evident in film resistors, and bulk breakdown is typical of piezoelectric crystals.

Besides these catastrophic failure mechanisms, un-encapsulated chips and LSI MOS integrated circuits have exhibited temporary failure due to gaseous arc discharge from positive charges deposited on the chip as a byproduct of gaseous arc discharges within the package between the lid and the substrate.

Thermal secondary breakdown is also known as avalanche degradation. Since thermal time constraints of semiconductor materials are generally large compared with transient times associated with ESD pulse, there is little diffusion of heat from the areas of power dissipation, and large temperature gradients can form in the parts. Localised junction temperatures can approach material melt temperatures, usually resulting in development of hot spots and subsequent junction shorts due to melting.

For junction melting to occur in bipolar (P-N) junctions, sufficient power must be dissipated in the junction. In the reverse bias condition, most of the applied power is absorbed in the immediate junction area with minimal power loss in the body of the part. In the forward bias condition, the junction in-

CLASS 1: SENSITIVITY RANGE 0 TO < 1000 VOLTS

Metal Oxide Semiconductor (MOS) devices including C (Complementary), D (Double-Diffused), N (N-Channel), P (P-Channel), V (V-Groove) and other MOS technology without protective circuitry, or protective circuitry having Class 1 sensitivity

Surface Acoustic Wave (SAW) devices

Operational Amplifiers (OP AMP) with unprotected MOS capacitors

Junction Field Effect Transistors (JFETs) (Ref.: Similarity to MIL-STD-701: Junction field effect, transistors and junction field effect transistors, dual unitized)

Silicon Controlled Rectifiers (SCRs) with Io < 0.175 amperes at 100° Celsius (°C) ambient temperature (Ref.: Similarity to MIL-STD-701: Thyristors [silicon controlled rectifiers])

Precision Voltage Regulator Microcircuits: Line or Load Voltage Regulation <0.5 percent

Microwave and Ultra-High Frequency Semiconductors and Microcircuits: Frequency >1 gigahertz

Thin Film Resistors (Type RN) with tolerance of < 0.1 percent; power > 0.05 watt

Thin Film Resistors (Type RN) with tolerance of >0.1 percent; power ≤0.05 watt

Large Scale Integrated (LSI) Microcircuits including microprocessors and memories without protective circuitry, or protective circuitry having Class 1 sensitivity (Note: LSI devices usually have two to three layers of circuitry with metallization crossovers and small geometry active elements.)

Hybrids utilizing Class 1 parts

CLASS 2: SENSITIVITY RANGE >1000 TO ≤4000 VOLTS

MOS devices or devices containing MOS constituents including C, D, N, P, V, or other MOS technology with protective circuitry having Class 2 sensitivity

Schottky diodes (Ref.: Similarity to MIL-STD-701: Silicon switching diodes [listed in order of increasing trr])

Precision Resistor Networks (Type R2)

High Speed Emitter Coupled Logic (ECL) Microcircuits with propagation delay ≤1 nanosecond

Transistor-Transistor Logic (TTL) Microcircuits (Schottky, low power, high speed, and standard)

OP AMPs with MOS capacitors with protective circuitry having Class 2 sensitivity

LSI with input protection having Class 2 sensitivity

Hybrids utilizing Class 2 parts

CLASS 3: SENSITIVITY RANGE >4000 TO ≤15,000 VOLTS

Lower Power Chopper Resistors (Ref.: Similarity to MIL-STD-701: Silicon Low Power Chopper Transistors)

Resistor Chips

Small Signal Diodes with power ≤1 watt excluding Zeners (Ref: Similarity to MIL-STD-701 Silicon Switching Diodes [listed in order of increasing trr])

General Purpose Silicon Rectifier Diodes and Fast Recovery Diodes (Ref: Similarity to MIL-STD-701: Silicon Axial Lead Power Rectifiers, Silicon Power Diodes [listed in order of maximum DC output current], Fast Recovery Diodes [listed in order of trr])

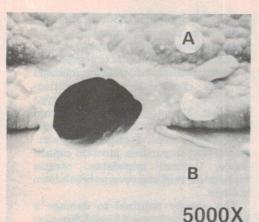
Low Power Silicon Transistors with power ≤ 5 watts at 25°C (Ref: Similarity to MIL-STD-701: Silicon Switching Diodes [listed in order of increasing trr], Thyristors [bi-directional triodes], Silicon PNP Low-Power Transistors [Pc ≤ 5 watts @TA = 25°C], Silicon RF Transistors)

All other Microcircuits not included in Class 1 or Class 2

Piezoelectric Crystals

Hybrids utilizing Class 3 parts

Table 3. List of ESDS devices by part type



Detailed view of the 6-micron (0.0002") diameter hole created in aluminium metallisation (A) and silicon dioxide substrate (B) by static discharge.

hibits lower resistance. Even though a greater current flows, a greater percentage of the power is dissipated in the body of the part. Thus, more power is generally required for junction failure in the forward bias condition.

For most transistors, the emitterbase junction degrades with lower current values than the collector-base junction. This is because the emitterbase junction normally has smaller dimensions than any of the other junctions in the circuit. For reversed polarity signals, only a very small microampere current flows until the voltage exceeds the breakdown voltage of the junction. At breakdown, the current increases and results in junction heating, due to the nucleation of hot spots and current concentrations. At the point of second breakdown, the current increases rapidly due to a decrease in resistivity and a melt channel forms that destroys the junction. This junction failure mode is a power-dependent process.

Metallisation melt failures can occur when ESD transients increase part temperature sufficiently to melt metal or fuse-bond wires. Theoretical models exist which allow computation of currents that can cause failure for various materials as a function of area and current duration. Such models are based on the assumption of uniform area of the interconnection material. In practice, it is difficult to maintain a uniform area, and the resultant nonuniform area can result in localised current crowding and subsequent hot spots in the metallisation. This type of failure could occur where the metal strips have reduced cross-sections as they cross oxide steps. Normally due to shunting of the currents by the junction, this failure requires a larger power level at higher frequencies than is required for junction damage at lower frequencies. Below 200 to 500 MHz, the

junction capacitance still presents a high impedance to currents, thereby shunting them around the junction.

Bulk breakdown results from changes in junction parameters due to high local temperatures within the junction area. Such high temperatures result in metallisation alloying or impurity diffusion, resulting in drastic changes in junction parameters. The usual result is the formation of a resistance path across the junction. This effect is usually preceded by thermal secondary breakdown.

Dielectric breakdown occurs when a potential difference is applied across a dielectric region in excess of the region's inherent breakdown characteristics, and a puncture of the dielectric occurs. This form of failure is due to voltage rather than power and could result in either total or limited degradation of the part, depending on the pulse energy. For example, the part may heal from a voltage puncture if the energy in the pulse is insufficient to cause fusing of the electrode material in the puncture. It will, however, usually exhibit lower breakdown voltage or increased leakage current after such an event, but it will not exhibit catastrophic part

This type of failure could result in a latent defect resulting in catastrophic failure with continued use. The breakdown voltage of an insulating layer is a function of the pulse rise time, since time is required for avalanching of the insulating material.

Gaseous arc discharge occurs in parts with closely spaced, unpassivated, thin electrodes. Gaseous arc discharge can cause degraded performance. The arc discharge condition causes vaporisation and metal movement, which is generally away from the space between the electrodes. The melting and fusing do not move the thin metal into the interelectrode regions. In melting and fusing, the metal flows together and flows or opens along the electrode lines. There can be fine metal globules in the gap region, but not in sufficient numbers to cause bridging. Shorting is not considered a major problem with passivated thin metal electrodes.

On a SAW band pass filter device with thin metal of approximately 0.4 micron and 3.0 microns (1 micron = $1 \times 10^{-6} \text{ metres}$) electrode spacing, operational degradation was experienced from ESD. When employing thicker metallisation such as 1.35 microns, this gaseous arc discharge in an arc gap at typically 50 microns can be used for protection to dissipate incoming high voltage spikes.

For LSI and memory ICs with passivation/active junction interfaces

susceptible to inversion, gaseous arc discharge from inside the package can cause positive ions to be deposited on the chip and cause failure from surface inversion. This has been reported to occur especially on parts with nonconducting lids. A special case of this is UV-EPROMs with quartz lids, where failures can be annealed by neutralising the positive charge with ultraviolet light through the quartz lid.

Surface breakdown occurs at perpendicular junctions, and is explained as a localised avalanche multiplication process caused by narrowing of the junction space charge layer at the surface. Since surface breakdown depends on numerous variables, such as geometry, doping level, lattice discontinuities, and unclean gradients, the transient power which can be dissipated during surface breakdown is generally unpredictable.

The destruction mechanism of surface breakdown results in a high leakage path around the junction, thus nullifying the junction action. This effect, as well as most voltage sensitive effects like dielectric breakdown, is dependent upon the rise time of the pulse and usually occurs when the voltage threshold for surface breakdown is exceeded before thermal failure can occur

Another mode of surface failure is the occurrence of an arc around the insulating material. This failure is similar to metallisation gaseous discharge except that discharge is between metallisation and semiconductor.

Specific effects on circuits

Now, having identified the causes of ESD and the major types of failure mechanisms, it is important to assess how these failures manifest themselves in systems. Typical devices and their degradation thresholds are summarised in Table 4. A more detailed analysis of the physical mechanisms follows.

MOS structures are a conductor and semiconductor substrate separated by a thin dielectric. This family includes MOS field effect transistors (FETs), MOS ICs, bipolar, hybrid, linear and digital ICs and MOS capacitors. Or more basically, the family includes any dual dielectric system or semiconductor with metallisation crossovers. The newer devices in this area - the VMOS (vertical groove MOS), the HMOS (high density MOS), the HEX MOS, and some of the prototype GaAs MESFETS (gallium arsenide metal semiconductor field effect transistor) - approach 1 micron or less compared with today's chip geometries of 4 to 5 microns (see Figure 2). Needless to say, as these

	Т	EST RESULTS
DEVICE	THRESH- OLD, ¹ VOLTS	DEGRADATION CRITERIA ²
Diodes 1N459	> 3000	50% drop in V_R at $I_R = 5 \mu A$
1N916	3000	ικ σμα
T1551	450	OF THE PROPERTY OF
1N4151	> 3000	
Zener Diodes	b- waren	50% drop in V _R at
LVA356	> 3000	$I_R = 5 \mu A$
Transistors		50% drop in V(BR) CBO at
2N2222	1000	$I_R = 5 \mu A$
2N2369A	460	1000 1000 1000 1000 1000 1000 1000 100
2N2432A	620	
2N2540	1450	And the second
2N2907	1200	The second of the second
2N3117	1000	The same and the
2N3570	380	A CONTRACTOR OF THE PARTY OF TH
2N4251	460	
2N4872	1200	K The State of the
2N5154	> 3000	Amount of the Amount
Junction	Service of	50% drop in V(BR) GSS a
Field-Effect	150 00 000	$I_G = 5 \mu A$
Transistors		The state of the s
2N2608	320	The second second second second
2N3112	530	The state of the s
2N3971	160	
2N4118A	140	
Metal-Oxide	- 1	$I_G > 5 \mu A$ at $V_{GS} = 22 V$
Semiconductor	and the second	$V_{GS} = 22 V$
Transistors	Parks when	and the state of
GI MEM 520c	The state of	at his
(chip)	58	
Complementary	ALL FOR	> 0.5 μ A input at
Metal-Oxide	the market	10 V or
Semiconductor	1	> 10% decrease in
Integrated	100	output voltage
Circuits	1-53	across 100-KΩ load
RCA CD4001	250	Contract program
Silicon-	100	50% increase in Icco
Controlled	1	
Rectifiers		10 · N · N · N · N · N · N · N · N · N ·
2N886A	680	A CONTRACTOR OF THE PARTY OF TH
2N3030	1000	I do to the second

NOTES:

1. Reverse-breakdown polarity.

2. Where V_R is the reverse voltage, I_R the reverse current, V_{(BR)CBO} the collector/base breakdown voltage, I_B the base current, V_{(BR)GSS} the gate/source breakdown, I_G the gate current, V_{GS} the gate/source voltage, and I_{CGO} the gate leakage current.

Table 4. Typical device degradation threshold

smaller geometries blend with higher purity processing, the device susceptibilities will rise and ESD transients of 20 volts will become lethal.

Differences in susceptibilities of these MOS technologies are dependent upon the gate dielectric strength and the oxide thickness. In the past, gate dielectric thickness has typically been around 0.11 micron with dielectric strengths ranging around 1×10^6 to 1×10^7 V/cm, with breakdown between 80 and 120 volts. Researchers today, however, are creating functional devices with dielectric thicknesses in the 0.06 to 0.08 micron range and breakdowns at 20 and 25 volts.

Many monolithic ICs have metallisation runs which cross over active semiconductor regions with field oxide between them serving as the insulator. These are called parasitic MOS transistors. Normally, these break down around 100 volts due to field intensification at the corners of the metallisation and weak dielectric strength of the oxide barrier. Breakdown of the oxide insulator is permanent, as opposed to breakdown of a semiconductor, which is reversible.

If very short-term overvoltages occur, a subsequent breakdown or avalanche occurs at a lower value than normal. As the punch-through short occurs, the metallisation will flow through the dielectric to create a low resistance short. However, in some instances where there is a particularly thin metallisation, such as 0.4 micron, or there is sufficient energy passed through the short, the metal will be vaporised and the short will clear but leave a cratered hole in the dielectric. Degraded performance may result but

not a catastrophic failure. There is conjecture that the short in some circumstances might reappear or performance might continue to degrade.

Semiconductor junctions included in this classification are positive-negative (PN) junctions, P-type intrinsic N-type (PIN) junctions, and Schottky barrier junctions. Their sensitivity to ESD depends on geometry, size, resistivity, impurities, junction capacitance, thermal impedance, reverse leakage current and reverse breakdown voltage.

The energy required to damage a junction in the forward biased direction is generally ten times that required in the reverse biased direction. Emitterbase junctions in bipolar transistors, whether integrated circuit or a discrete transistor, are usually more susceptible to ESD damage than collector-base or collector-emitter junctions. This is primarily due to size and geometry, where the emitter-side wall experiences large energy-densities during reverse biased ESD. Because of larger areas, the collector-base and collector-emitter do not experience the same energy densities, although with the collector-base and collector-emitter it is possible to laterally forward bias the base-emitter. In this case, a current crowding at the emitter side will occur.

Junction field-effect transistors which have high impedance gates are particularly sensitive to ESD. They have extremely low gate-to-drain and gate-to-source leakage in the order of less than 1 nanoampere, and relatively high breakdown voltage of greater than 50 volts. Therefore, the gate-to-drain and gate-to-source are usually the most sensitive ESD paths. Figure 3 is a

TYPICAL CELL GEOMETRIES

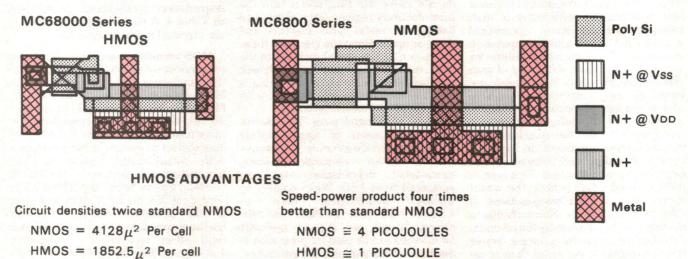


Figure 2. Comparison of HMOS and NMOS technologies.

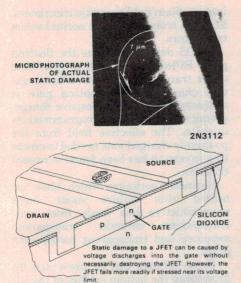


Figure 3. Example of nondestructive ESD damage to a JFET device.

classic example of non-destructive ESD damage to a JFET device. The device shown continued to function normally in the circuit. However, it experienced a dramatic decrease in its reverse breakdown voltage.

Schottky barrier junctions, such as the 1N57111 diode and TTL Schottky integrated circuits, are particularly sensitive to ESD because they have very thin junctions and the presence of metal increases the probability of ESD being carried through the junction.

Semiconductor junctions as sensitive ESD constituents are found not only in diodes, transistors, and bipolar integrated circuits, but also in MOS as parasitic diodes and input protection clamps. Although the input port junctions are meant to provide protection from ESD damage, the size of the protective junctions is limited due to cost and performance tradeoffs. Thus, ESD pulses of sufficient energy can damage the input protection junctions.

The temperature coefficient of extrinsic semiconductors is positive. That is, the higher the temperature, the higher the resistance. This feature prevents current crowding and hot spots from forming at low temperatures. However, in the reverse biased mode all the energy is being dissipated by the relatively large voltage drop across the relatively narrow depletion width of the junction. Due to geometrical effects, local resistance variations, and crystal defects, perfectly uniform current distribution does not occur across the junction. As an ESD occurs across the junction, the temperature at the depletion region increases quickly, and the extrinsic semiconducting material becomes an intrinsic semiconducting material, causing a sharp decrease in resistance which results in thermal

secondary breakdown. The more rapid the discharge, the more uniform is the increase in temperature and therefore the current across the junction. This means that for short duration discharges of less than 10 nanoseconds, the resultant filament short is wide compared to longer duration discharges.

It is possible for spots to develop but not grow completely across the junction such that at low bias voltages they do not cause a failure condition. However, during operation at certain bias conditions, locally high current densities may exist with a corresponding highly localised large increase in temperature at the previously formed hot spot locations, such that continued growth of a filament short may occur or silicon and metallisation may diffuse through the junction via the electromigration process at temperatures greater than 200°C. Low-leakage, high-breakdown JFET and Schottky barrier junctions seem to be particularly susceptible to this failure process.

It is this same failure process that requires the breakdown test of JFETs be performed as a leakage test rather than puting the junction into breakdown. With low-leakage junctions, highly localised currents can occur during junction reverse breakdown.

With the Schottky barrier junction, metallisation is immediately available to migrate through the junction at localised hot spots. As the current filament develops across a semiconductor junction, it is analogous to putting a parallel resistor across the junction of the same value as the short. However, in some marginally formed hot spots, it may be similar to putting a zener diode and a resistor in parallel with the junction. Failure indication of filament short from a high resistance short is high leakage.

Film Resistors: Resistor material adhering to an insulating substrate comes under the ESDS constituent classification of film resistor. The degree of sensitivity will depend on the ingredients and formulation of the resistor material and size-power considerations.

Hybrid microcircuits frequently contain either thin film resistors or thick film resistors. Hybrid designs which cannot tolerate large changes in resistance, such as precision voltage regulators, are sensitive to ESD.

Thick film resistors consist of a conductive metal oxide as the resistive element, a metal additive to improve electrical performance, and a glass frit to provide a support matrix, adhesion to the substrate, and resistivity control. Such parts are particularly sensitive to

ESD. Since the charge is almost always negative for thick films, electrical discharge has been considered as a possible trimming method when conventional trimming overshoots the desired resistance tolerance. It has also been found that the thick film resistance changes are heavily dependent on voltage rather than energy.

Thin film resistors, on the other hand, are more energy dependent and do not have changes greater than 5% in resistance until the energy of discharge is sufficient to cause film rupture.

In addition to hybrid microcircuits, some monolithic integrated circuits may also contain encapsulated thin film resistors, such as polysilicon resistors, as part of an input protection circuit. Discrete encapsulated resistors which contain the film resistor structure are also sensitive to ESD.

Carbon film, metal oxide, and metal film resistors are somewhat sensitive to ESD, especially at low tolerance and low wattage ratings. A frequently recurring ESD problem with resistors is with the 0.05 W metal film, part RNC50, specified at 0.1% tolerance. Putting these parts in a polyethylene bag and rubbing them on another bag is sufficient to shift the tolerance of these resistors.

ESD failure mechanisms of film resistors are not well defined. This is partly the result of not knowing the ingredients and formulations of the resistor material, which are often held proprietary by the manufacturer.

For thick film resistors, the failure mechanism has been modelled as the creation of new shunt paths in a matrix of series-parallel resistors and infinitesimal capacitors isolating metallic islands. With the application of high electric fields, the dielectric breakdown of the glass frit or other isolating dielectric material is exceeded and the rupture welds metallic ensuing particles together in a conducting path known as metallisation melt. Since this model involves a dielectric breakdown process, it is mostly voltage dependent. >

WATCH THAT SOLDER SUCKER

Removing integrated circuits soldered directly to a printed circuit board usually requires sucking away the solder from a reheated pad or plated through hole. That is fine for bipolar circuitry, but it can be extremely dangerous for MOS devices. Dan Anderson, president of Anderson Effects, points out that standard plastic solder suckers have been found to produce a static surge of 5000 to 10 000 V at the tip. This tip is invariably in direct contact with a device's lead when the surge occurs, resulting in a damaged or destroyed device. Anderson Effects and other firms now offer static-free metalised plastic models that produce no static charge. For more information, contact Anderson Effects Inc., P.O. Box 657 Mentone, California 92359 USA.

It appears that the ESD behaviour of resistive materials is very much a function of the number of parallel current paths or the number of capacitive couplings between parallel paths in the film structure. The nature of the glass used in the material also appears to be quite important, both because it influences the distribution of the resistive elements and because it can act as a resistive element itself. Thus, the behaviour of different thick film resistor paths to ESD can vary greatly. ESD sensitivity testing, therefore, should be specified for critical tolerance thick film resistors.

For thin film resistors and encapsulated metal film, metal oxide, and carbon film resistors, the failure mechanism is primarily a thermal, energy-dependent process modelled as the destruction of minute shunt paths. This mechanism is associated with an increasing resistance shift on the thin film and metal film type resistor which appears to be voltage dependent. This negative shift is usually not more than 5% and is typically less than 1% before changing to positive shifts as ESD voltage increases.

Some thin film resistors, such as deposited tantalum nitride on $Si0_2$ substrates, may be so small and power-limited that ESD voltages greater than 5000 volts from a person can melt the resistor open. For most cases, however, a shift in resistance will be the failure indicator.

Thus, for circuit designs tolerant of large resistance changes, the failure may not be critical. Generally, after exposure to an ESD, the stability of the resistor is reduced and the degree of instability is directly related to the level of ESD. Temperature coefficient changes have been known to result from such ESD exposure.

For thick film resistors, the resistance shift is negative. The resistance change can easily exceed 50% with some thick film pastes. Some exceptions to this may occur, especially at low resistance values. For thin film, metal film, metal oxide and carbon film at lower ESD levels, small negative resistance shifts of less than 5% can be experienced. At higher ESD levels, large positive shifts greater than 10% can be experienced, depending on the power rating.

Metallisation Strips. Relatively narrow, thin metallisation strips on a substrate such as SiO₂, which carry current between terminals without any other energy-absorbing element in the path, are susceptible to ESD. These metallisations may consist primarily of aluminium or gold, but can also be multi-layered. The failure mechanism

is burnout from joule heating. This type of constituent is often used in monolithic integrated circuits, hybrid microcircuits and multiple finger overlay transistor construction found in switching and high frequency transistors.

Joule heating is most likely to occur when: (1) the ESD source has very low contact resistance, resulting in high currents over short time constants, and (2) a low resistance large area diode is connected by the metallisation path between the two terminals, resulting in large currents due to the low voltage drop in the diode forward biased direction.

Increasing the width or thickness of the strip will decrease ESD sensitivity. The use of glassivation and thinner Si0₂ between the strip and the silicon also reduces ESDS. The failure indicator from this failure mode is open.

Passivated field-effect structures with nonconductive lids. Various NMOS and PMOS integrated circuit designs have been found to fail from very localised high concentrations of positively charged ions on the outer passivated surface of the die.

NMOS designs fail from excessive leakage currents as a result of field inversion between N+ junctions, such as thick field parasitic transistors, intermediate field parasitic transistors, EPROM transistors, and normal select transistors.

PMOS designs, such as the floating gate, EPROM or depletion type field effect transistors, fail when the negative charge on the floating gate is overcompensated by a positive charge, giving an erroneous unprogrammed indication. The effective field from the positively charged ions needed to create this inversion has been found to exceed 85 volts.

Hermetic packages which have recorded this failure mode have nonconductive lids made from nontransparent ceramic, transparent sapphire and transparent borosilicate glass.

These failures can be prevented by grounding the bottom surface of the lid over the die or by initiating preventive measures to avoid electrostatic charging of the nonconductive lid. This failure mechanism is most common with NMOS and PMOS UV-EPROMS having transparent lids. NMOS static random access memory (RAMs) in a ceramic package, however, have also been reported to fail from the ESD failure mechanism. Unless testing shows otherwise, any LSI integrated circuit with nonconductive lids could conceivably have field effect structures which are susceptible to failure from

CONDUCTIVE WRIST STRAP PROTECTS MICROCIRCUITS.

Royston Electronics has recently introduced a conductive wrist strap that meets the latest military specifications for quick release and resistance to line voltages from accidental contact.

Military users have specified these new requirements for wrist straps to prevent static electricity damage to microcircuits while radar, avionics, computer and other equipment is undergoing repair, maintenance or inspection in base stations or the field.

The CP401A grounding strap has a wrist attachment of "Velcro" hook-and-loop tape that separates with a slight pull for safety and to prevent a worker from breaking the grounding wire by inadvertently leaving the work area while still wearing the strap.

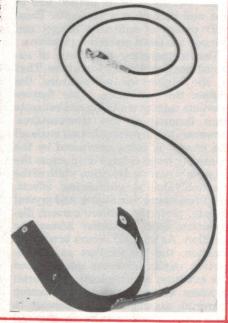
The wrist strap is made of conductive polyester ribbon for permanent conductivity, but with built-in resistance to protect the wearer against possible line voltages.

An alligator clip at the other end of the four-foot long grounding wire can be attached to any convenient ground, draining static electricity before it can build up to levels that are harmful to microcircuits.

The wrist strap and ground wire are joined by a standard snap fastener. An extra snap fastener provides a convenient connection for grounding electric tools, bench covers or other items in the work

area that must be grounded to prevent static electricity build-up.

Information on this, and the comprehensive range of other anti-static devices, is available from Royston Electronics, Melbourne (03)543-5122 or Sydney (02)709-5293.



undesirable field inversion or gate

threshold voltage shifting.

Failure mechanism involves positively charged ion clusters deposited on the die as a result of air breakdown in the air gap between the die surface and the bottom of the package lid. Charging of the bottom of the lid can be induced by several means, one of which is by freeze spraying the package with canned coolant. The positive charging rate of the freeze spray impinging on the top of the lid depends on the flow rate of the coolant from the can. At low flow rates, the charging is negative and does not induce failure; at high flow rates, sufficient positive charging can occur and induce failure. The localised air breakdown in the air gap of the package causes ionised streamers to form from the die to the lid. The positive charge on the bottom of the lid drives the positive charge in the streamer toward the die surface and attracts the negative charge toward the lid. This results in very localised clusters of positive ions on the die surface. Because of the nature of the air breakdown for certain package ambients, this charge is probably identical in type to the very large ions that can be experimentally created by positive corona discharge in the air.

These localised positive charges also cause the formation of inversion layer leakage paths between N+ diffusions and shift the gate threshold voltage on PMOS depletion type transistors. The formation of leakage paths and the gate

threshold shifts give rise to isolated circuit failures. This failure mechanism is recoverable by neutralising the positive charge on the outer surface of the die.

On UV-EPROMs with transparent lids, recovery is nondestructive when $2737\text{\AA}\ (2.737 \times 10^{-7}\ \text{metres})$ ultraviolet light with a minimum photon energy of $4.3\,\text{eV}$ is applied to the chip for as short as three to five seconds.

Failure indicators for this failure mode come under the general classificaof operational degradation. Operational degradation will take the form of a functional failure. In the case of NMOS UV-EPROMs, certain programmed bits appear unprogrammed and certain unprogrammed bits appear programmed. In one group of failure indicators, bit failures have been organised in columns where programmed bits appear unprogrammed. In another group of failure indicators, bit failures were organised on rows where unprogrammed bits appeared programmed.

The failure indicators for PMOS UV-EPROMs are random single-bit failures throughout the memory which would read as programmed but appear as unprogrammed. Failure indicators for NMOS static RAM have been reported as random hits stuck in "1" or "0" logic state and the adjacent cell also stuck but in the opposite logic state.

Piezoelectric crystal devices, such as quartz crystal oscillators and SAW devices, can fail from ESD, resulting in operational degradation. Electrical parameters of piezoelectric crystals contained within these parts are damaged by excessive driving current. Also, the piezoelectric effect from high voltages causes mechanical stress and movement to be generated in the crystal plate. When the voltage is too great, mechanical forces cause motion in excess of the elastic limits of the crystal and crystal fracture occurs. Fracture may occur as a lifted platelet, as has been experienced in lithium niobate SAW delay lines. Such fractures, when occurring in sufficient number, will cause enough change in the operating electrical characteristics to cause failure.

Closely spaced electrodes. When employing thick metallisation, such as 1.35 microns, gaseous arc discharge in an arc gap 50 microns wide can be used as a protection device to dissipate incoming high voltage spikes. In devices with closely spaced, unpassivated, thin electrodes, however, gaseous arc discharge can cause degraded performance.

Devices that employ thin, closely spaced electrodes include SAW devices. Other parts, such as high-frequency, multiple-finger transistors, and new technology, such as very large scale integration (VLSI) and very high speed integration (VHSI), could also be degraded to failure from arc discharge between metallisation runs. Arc discharge causes vaporisation and metal movement generally away from the space between the electrodes. Melting and fusing do not move the thin metal into the interelectrode regions, but the metal pulls together and flows or opens along the electrode lines. There can be fine metal globules in the gap region, but not in sufficient numbers to cause bridging. Shorting is not considered a major problem with unpassivated thin metal electrodes.

ESD failures have been experienced on SAW band pass filters with thin metal of 0.4 micron and electrode spacing of 3.0 microns.

NEW MATERIAL WILL OVERCOME SPACECRAFT LOSSES

A new composite material just successfully tested in Britain will overcome a problem in space that has led to the loss of at least two satellites.

Orbiting spacecraft are bombarded by high-energy electrons that cause electrostatic charges of up to 20 000 volts to build up on the surface of the craft. As a result a spontaneous electrical discharge can occur through the outer thermal protective material.

As well as damaging the covering of the satellite, the discharge can cause false electronic signals to disrupt the operation of the craft. If this should happen while it is being manoeuvred in space, the satellite could be lost.

British Aerospace says that these dangers will be eliminated by encasing the satellite in a new composite sandwich material which dissipates the surface charge the moment it hits the spacecraft. The material, which has been patented, also eliminates electrostatic-induced interference, minimises contamination and will prolong the operational life of satellites by maintaining the thermo-optical protection.

In tests, samples of the multi-layer thermal-insulating material have successfully withstood electron energy levels of 30 000 volts at intensities up to 30 times greater than those anticipated in space.

The material has shown that the electrostatic surface potential cannot build up to operationally dangerous levels even at temperatures as low as minus 170° Celsius, where the probability of a discharge is much greater.

British Aerospace says the new material is made up of one of two types of material already used for thermal insulation. However, in this new material it is arranged in composite sandwich form along with two conductive layers of aluminium or carbon, which are earthed to the satellite's main structure.

Electrons penetrating the outer skin are captured by the first conducting layer, while the second aluminium layer on the inner side of the material captures the more highly charged particles that may have penetrated further into the surface of the spacecraft. The inner conductive layer can also act as a radio frequency shield.

The multilayer technique may find further use in protecting spacecraft equipment such as thermal control mirrors, solar arrays and the back of antenna dishes, and British Aerospace say there may be other applications on the ground.

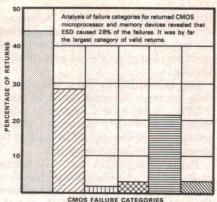
Detecting ESD damage

By this time, hopefully, the reader has developed an acute sensitivity to the nature of ESD and the insidious nature of ESD damage to the function of an electronic device or a black box. Although there are many thousands of users worldwide, very few have the capability—that is, trained people and facilities—to perform the failure analysis that would lead to the recognition of ESD as the culprit in numerous equipment failures.

Even with trained people and the proper tools, identifying ESD damage can be difficult. Phil Kohlhaas of 3M Static Control Systems reported at a recent seminar (hosted by Warren Yates of Electronic Products magazine) that 3M sent 100 deliberately static-damaged devices to a testing laboratory. The lab performed a 100% failure analysis — SEM (scanning electron microscope), glass removal, metal removal, the works — and in 60% of the cases, could not identify ESD-related damage that had occurred.

ESD-induced failures are often mistaken for other types of failures. This is particularly true, according to Roy Walker of IITRI/RAC, when it comes to steady-state electrical overstress failures. Agreeing with Walker, Hewlett-Packard's Kim Gray said he encountered a latch-problem in a CMOS device that appeared to result from steady-state-overstress failure; it turned out to be an ESD failure.

A lack of ESD awareness causes many people to limit ESD protection to only the most widely used susceptible devices — FETs without protection, and CMOS with double diode protection. But don't be lulled into a false sense of security if you're using bipolar devices. It's just more difficult to discern the ESD mechanism in a bipolar device than in a MOS device. Walker and others contend that there are many more ESD related problems in bipolar devices than we actually know about



	CMOS FAILURE CATEGORIES
	CUSTOMER INDUCED PROBLEMS: 73.5% INVALID RETURNS. 44% ELECTROSTATIC DAMAGE 28% EOS, VCC SPIKES. 1.5%
	OBSERVED FAILURE MODES: 26.5% ASSEMBLY 2.5% PROCESSING FLAWS 21% TEST ESCAPES: 3%
	RETURNED UNITS EQUAL 0.2% OF TOTAL PARTS SHIPPED
PATE	SUMMARY BASED ON RESULTS OBTAINED DURING THE INTERVAL AUGUST 1977 TO MARCH 1978 Courtesy of Harris Semiconductors

Figure 4. Typical device degradation threshold.

because of limitations of ESD failure analysis.

The ESD problem is big — make no mistake about that — even though it's really not possible to put a precise handle on just how big. For example, Gene Freeman presented some failure analysis data compiled by Harris Semiconductor on devices returned to them

(see Figure 4). Note that ESD comprises the largest single failure mode.

Steve Halperin of Analytical Chemical Labs reported on his company's observations of equipment manufacturers. Where large boards of critical design are involved, he has seen up to a third of all boards started during a day enter a "repair and refurbish" function at some period during handling in the manufacturing facility. Cost of manufacturing failures can be prohibitive, but at least these types of problems are caught at the factory. But what about devices that are degraded by ESD but don't fail until later, out in the field? Halperin quoted figures from some computer manufacturers indicating that 70% of their field service calls were static related.

Degraded devices can become much more than just an expensive field service problem. We cannot ignore the possible substantial costs of product liability, as a failure in a critical end item system might mean substantial property losses or loss of life.

An effective plan to combat ESD requires a strong static awareness on the part of all concerned — factory assembly and test personnel, engineering, maintenance, and field service. But most of all, it requires a strong commitment on the part of top management.

Protecting your equipment

Once it is recognised that static discharge can degrade equipment per-

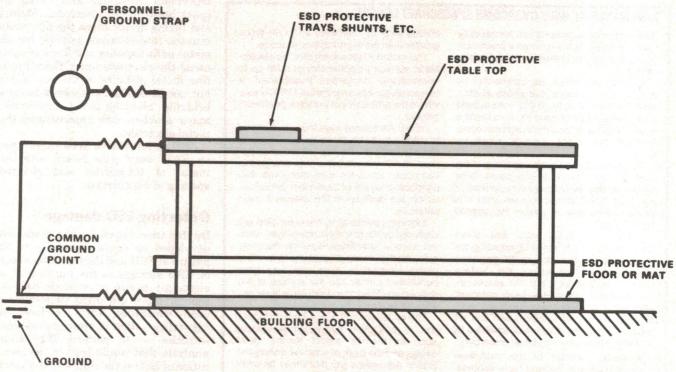


Figure 5. Typical ESD grounded work bench.

formance, and that in reality only the 'tip of the iceberg' can be identified as ESD, it is evident that ESD can be combatted only through protective measures. The first and most obvious key, as just stated, is top management's absolute commitment toward a total ESD program.

If management is astute enough to make this commitment, a total awareness and educational programme reaching to all individuals interfacing with the equipment is essential. After awareness is implementation, preparation of specifications and requirements to control work environments, identification and labelling of ESDS hardware, acquisition of antistatic handling equipment and work stations, and coordination of ESD programmes with both suppliers and users.

YOUR CHECK-LIST

- Identify static-sensitive parts. Manufacturers should be required to clearly mark all parts that are suspected to be sensitive to static charge. Markings should read "static-sensitive devices".
- 2. Provide procedural guidelines to all personnel involved in handling, packaging, testing, assembling, and reworking "static-sensitive devices".
- Maintain good grounding techniques by keeping equipment and personnel at the same potential. Use conductive countertops, floor mattings, wrist straps, or arm sleeves, and make proper connections to a grounding source. (See Figure 5.)
- 4. Use conductive carriers for transporting, storing, and shipping static-sensitive parts.
- Use neutralisers to neutralise charge on personnel, handling tools, and work surfaces.
- 6. Use a noncontacting static voltmeter to regularly monitor static charge in assembly area and on working personnel. This offers control and also keeps personnel static-conscious
- Failed parts should be treated with the same precautions; otherwise, the cause of the original failure may never be determined.
- Keep all LRU assemblies stored away from radar, laser, X-ray).
- Keep connector caps on LRUs at all times whenever they are not installed. (Conductive caps are preferred.)
- 10. Never open an LRU on or remove an SRU unless at a properly equipped work station.

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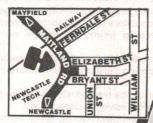
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Seeking the gluon

Brian Dance

Are gluons the fundamental particles that hold everything together?

IT'S A LONG TIME since Thomson discovered the electron (from which the name of our hobby is derived), but modern electronic equipment has been used to search for more and more particles, the latest being the 'gluon'. The name gluon obviously comes from the fact that this particle is postulated as acting as a glue which holds something together, but the story is a little more involved than this.

In the 1960s the hypothesis that all nuclear particles consist of still smaller particles known as 'quarks' was tentatively proposed and this idea has grown in importance with time so that it is now widely accepted. Evidence has been accumulated for the existence of five different quarks and physicists are now seeking a sixth type which is believed to exist for reasons of symmetry. According to the current theory, quarks combine in groups of three to form protons and neutrons which are held together by the 'strong' nuclear force. The latter is one of the four fundamental forces of nature, the others — in order of diminishing strength — being the electromagnetic force which controls all chemical change, the 'weak' force responsible for certain radioactive changes (beta radioactivity) and the gravitational

Each of these forces is associated with an intermediary particle, at least in theory. The best known of these intermediary particles is the photon, which gives rise to the electromagnetic forces which manifest themselves as electrostatic forces between electrical charges, the magnetic forces in electric motors, etc. The intermediary particle connected with the strong force is the gluon, which is said to travel very rapidly between the quarks which it is holding together.

Experimenters have thought there would be little hope of detecting the gluon to confirm the theoretical work, since it is associated with the strongest of the four forces known to exist in nature. However, recent work has

provided quite strong evidence that this particle exists. Nobel prize winner Professor Abdus Salam predicted in his concluding talk at the Geneva high energy physics conference in 1979 that the gluon is likely to be discovered before the long-awaited intermediary particle of the weak interaction, the intermediate vector boson which has been sought in neutrino experiments, and it certainly appears that his forecast will be correct.

A team of people working with the PETRA accelerator (near Hamburg, West Germany) announced details of the evidence they had obtained for the existence of gluons at an international conference held at the Fermi National Accelerator Laboratory in Batavia, Illinois, USA, and astonished the particle physicists working in this field.

The PETRA work employed beams of high energy electrons and of positrons. (The positron is the antiparticle of the electron and is similar to it, except that it has a positive charge.) At moderate energies the colliding beams of particles and their antiparticles result in the disintegration of the components into pairs of quarks and anti-quarks which move off very rapidly in opposite directions before they are transformed into the types of particle with which we are much more familiar.

The PETRA workers employed particle energies of about 30 GeV (30 000 million electron volts). They found that, instead of two particles moving in opposite directions from the point of collision, three jets of particles were formed. Two of these jets were narrow, but the third was broad and it

was this latter beam that contained the evidence for the existence of gluons.

Theoretical particle physicists have been gradually formulating a theory of quantum chromodynamics (QCD) to describe the forces that operate deep inside protons, neutrons and similar particles. Gluons are postulated as the carriers of the 'colour' force acting between quarks and are thus ultimately responsible for all of the strong force phenomena. Somewhat delicate effects found in neutrino experiments have given considerable support to the QCD theory, but these effects can easily be masked and are difficult to measure. The work at the PETRA accelerator has provided a new and very effective way of testing the QCD theory.

Under the high energy conditions, one particle of each quark-antiquark pair is believed to have produced a high speed gluon. This process, when continuously repeated, knocked the main beam of particles slightly to one side and thus created a stream of gluons which quickly transformed into quarks and then into other particles. The gluons and the quarks have such a short lifetime that their existence has to be inferred from their effects in the same way that the existence of an animal is often inferred from its foot-prints. The detection of the three beams by the PETRA workers provided very strong evidence of the existence of gluons. The hard gluon emission has been called 'Glühstrahlung' (German for 'glue radiation') by some workers.

This new work at PETRA may well be of great importance for the future in our understanding of the relationship of the

FORCE	APPROXIMATE RELATIVE STRENGTH
Nuclear or strong	1
Electro-magnetic	10-2
Weak	10-12
Gravitational	10-39

Table 1. The forces of nature.

four forces of nature — which are of vital importance in modern physics. The director of the FermiLab, Leo Lederman, has said that physicists are absolutely delighted with the new results and that the discovery of the gluon is of vital importance for our understanding of the theory of the strong force. Physicists are already convinced that the electro-magnetic and weak forces are basically different views of the same thing and it seems likely that the QCD theory will bring the strong nuclear force into the same general system.

We have known about the photon since early this century. If the existence of the gluon is confirmed, it will be only the second intermediary particle to be found. These particles are vital to our understanding of the basic particle interactions and such a discovery should encourage work on further searches for the intermediate vector boson; if the latter has a relatively high mass, a more powerful and more expensive accelerator may be required before it can be produced. The fourth intermediary, the graviton, has also been proposed, but owing to the very weak nature of the gravitation force, it may be a very long time before it is discovered — assuming it can exist.

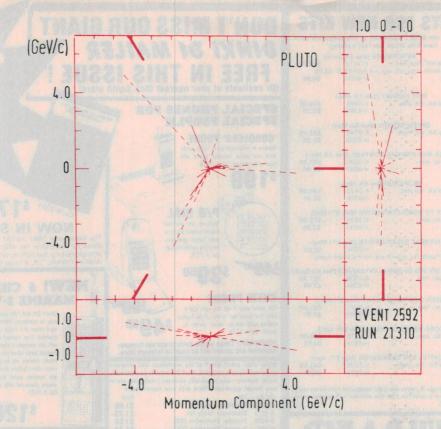


Figure 1. Moments of particles produced at 31.6 GeV at PETRA. Solid and dotted lines correspond to charged and neutral particles respectively. The thick bands show the directions of the jet axis. The three drawings cover the three dimensions. (CERN COURIER.)



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Dick Smith and Staff

High impedance instrument probe features 100 MHz bandwidth

This probe will allow you to make CRO or frequency meter/timer measurements on high impedance circuits with waveforms having rise times as fast as three or four nanoseconds. Cost is well below commercial equivalents.

Jonathan Scott

MOST READERS would be aware that, when taking a measurement on electronic circuitry, the input impedance of the measuring instrument must be much greater than the impedance of the circuit to which it is attached, otherwise the accuracy of the measurement suffers. The input impedance of the majority of oscilloscopes is generally 1M with a parallel capacitance of between 20 pF and 40 pF. For a wide variety of applications this is perfectly adequate and will suffice for measurements of frequencies up to 5 MHz or so. The input impedance of the CRO falls with increasing frequency owing to the falling reactance of the input capacitance. For example, a capacitance of 30 pF - which may be made up of direct input capacitance plus cable capacitance - has a reactance of only 500 ohms at 10 MHz. The input capacitance also affects the rise time of the input - that is, the speed at which a 'step' input will rise from the 10% amplitude value to the 90% amplitude value.

The input impedance of an oscilloscope can be effectively raised, and the capacitance decreased, by using a 'stepdown' probe. For example, a 'x10' probe will generally have an input impedance of 10M and a parallel capacitance of between 5 pF and 15 pF. While this improves the input impedance there are two trade-offs. Firstly, unless elaborate (and expensive) compensation is employed, the rise time is degraded, and secondly, maximum sensitivity is decreased by a factor of ten. As Murphy's law would have it, your CRO will run out of grunt just when you need it most.

Taking the situation with digital counter/timers, we find similar problems. Those that operate beyond 30 MHz or 50 MHz generally employ a prescaler with an input impedance of 50 ohms — which is perfectly all right if you're working on low impedance circuits and/or with high signal levels. But there are those occasions when you need



a high impedance input and a fast (high frequency) rise time. As with the CRO, this is where your counter/timer runs out of grunt.

It's times like these you need . . . the ETI-156 instrument probe. This project is a x1 active instrument probe using a special buffer IC with an input impedance of typically 100 000 megohms! that's 1011 ohms — a very low input capacitance of around four to five picofarads, a fast rise time (around three nanoseconds) and a bandwidth of 100 MHz. Output impedance is around 50 ohms and the device is capable of driving capacitive loads up to several thousand picofarads. Thus it is eminently suited for use with high speed, wide bandwidth oscilloscopes and digital frequency meter/timers at frequencies up to 100 MHz. Output impedance is close to 50 ohms and it is thus suited to drive both high impedance instrument inputs and low impedance inputs (which are generally 50 ohms).

It's all done inside a special IC - an LH0033CG from National Semiconductors. This is described as a 'fast buffer amplifier'. (It has a companion designated LH0063, described as a 'damn fast buffer amplifier'!). The LH0033 is a direct-coupled FET-input voltage follower/buffer (gain ≈ 1) designed to provide high current drive at frequencies from dc to over 100 MHz, It will provide ±10 mA into 1k loads (±100 mA peak) at slew rates up to $1500 \text{ V/}\mu\text{s}$, and the chip exhibits excellent phase linearity up to 20 MHz. No offset voltage adjustment is required as the unit is constructed using specially selected FETs and is lasertrimmed during construction. Input is directly to the gate of a junction FET, operated as a source follower, driving a complementary output pair of bipolar transistors.

Regulated plus and minus supplies of 15 V each provide power to the IC. Lowpower three-terminal regulators are

SPECIFICATIONS ETI-156 HIGH IMPEDANCE INSTRUMENT PROBE

Input impedance Input capacitance Maximum permissible input voltage *Hi-z load ±15 V *50 Ω load

Output impedance Bandwidth

Rise time Gain

*Hi-z load *50 Ω load 109 to 1011 ohms (depends on construction)

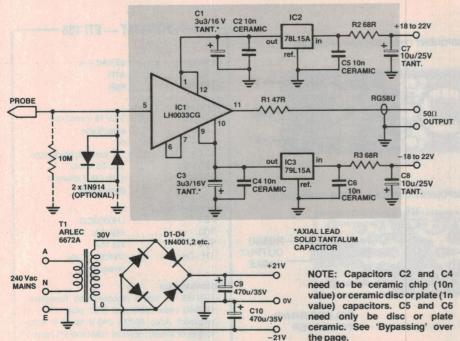
about 5 pF

(depends on construction)

±10 V 50 to 55 Ω 100 MHz better than 3.5 ns

0.98

hi-z instrument probe



used to keep the unit compact. An external unregulated supply of between 18 and 22 volts at around 50 mA is required to power the probe.

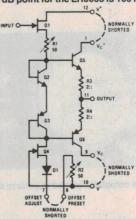
The supply pins on the IC need to be well bypassed over a wide frequency range so that the IC can maintain its characteristics, and the construction has been specially arranged to achieve this. Axial lead solid tantalum capacitors are used to bypass the IC's supply pins at the lower frequencies, while low inductance ceramic capacitors are employed as bypasses for the higher frequencies. A double-sided fibreglass pc board is used to preserve the high frequency response and the high input impedance, and the layout is arranged

- SPECIFICATIONS LH0033 LH0033 Output Voltage vs Absolute maximum ratings Supply Voltage Supply voltage ±40 V $R_i = 1 k\Omega$ Max. power dissipation 15W 16 $R_S = 100 \text{ k}\Omega$ $T_C = +25^{\circ}\text{C}$ Input voltage same as supplies (+ N) 14 Continuous output current ±100 mA VOLTAGE Peak output current +250 mA 10 dc characteristics DUTPUT (LH0033C/LH0033CG) — typical 12 mV Output offset voltage Voltage gain 0.98 Input impedance 1011 ohms 20 6 ohms Output impedance 10 15 SUPPLY VOLTAGE (±V) Output voltage swing ±13 V LH0033 Frequency Response $(V_S = \pm 5 V)$ (6 V p-p) Supply current 21 mA Vs = ±15V 35 $(V_S = \pm 15 V)$ Power consumption 630 mW 30 PHASE LAG (DEGREES) 10 15 10 VIN = 1.0V rms $(V_S = \pm 15 V)$ (8/8) ac characteristics (LH0033C/LH0033CG) — typical VOLTAGE 0.6 Slew rate $(V_{in} = \pm 10 \text{ V})$ 1400 V/us 0.4 Bandwidth (Vin = 1 Vrms) 100 MHz 20 Phase non-linearity (1 - 20 MHz) Rise time ($\triangle V_{in} = 0.5 \text{ V}$) 100 Propagation delay (△Vin = 0.5 V) 15 ns 10.0 20.0 FREQUENCY (MHz) Harmonic distortion (f>1kHz) <0.1% NOTE: Unless otherwise specified, these figures apply for +15 V applied to pins 1 and 12, -15 V to pins 9 and 10, and pin 6 shorted to pin 7. Specifications apply over temperature range between -25° C and $+85^{\circ}$ C; typical values shown are for a temperature of 25 °C.

- HOW IT WORKS ETI-156

This instrument probe employs a wideband hybrid voltage follower/buffer IC, the LH0033, with very close to unity gain, that features a very high input impedance and a low output impedance. It requires regulated, well-bypassed supply rails. Two three-terminal low power regulators provide plus-and-minus 15 V supplies from an unregulated input.

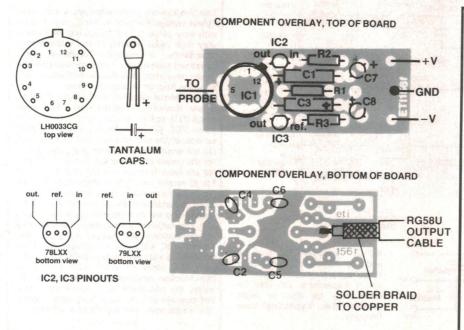
The internal circuit of the LH0033 is shown below. Basically, it consists of a FET input stage (Q1), operated as a source follower. The other FET, Q4, provides a constant current source for the source bias of Q1, while Q2 and Q3 are connected as diodes and provide bias for the bases of Q5 and Q6. Resistors R1 and R2 are laser trimmed in manufacture so that the IC meets the offset voltage specification. As Q1 has a constant current source load, the input impedance at the gate of Q1 is very high indeed and the distortion of the stage is very low. The output of the source follower drives a complementary pair output stage, Q5-Q6. Thus the IC will have a very high input impedance, a very low output impedance and a gain very close to unity. With appropriate construction employed for the internal devices, the bandwidth over which the device will operate can be made very wide indeed. The -3 dB point for the LH0033 is 100 MHz.

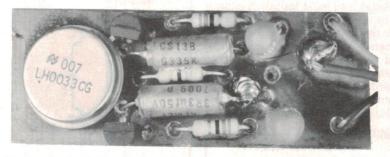


As the device is direct-coupled, dc levels will be maintained between input and output. Bypassing requirements for the IC's supply

leads are explained elsewhere in the article. To provide regulated plus-and-minus 15 V rails for the IC, two three-terminal regulators are employed, a 78L15A for the positive rail and a 79L15A for the negative rail. These can supply up to 100 mA and have a very low output impedance up to several hundred kilohertz, which is exploited for low frequency bypassing. Each supply rail requires an unregulated input of between 18 V and 22 V. Decoupling of the supply leads is provided by R2/C7 on the positive rail and R3/C8 on the negative rail. The input terminal of each regulator is bypassed to prevent instability.

As the input voltage is limited to a maximum equal to the supply rails (high impedance load), input protection may be added in applications where only low level signals are being examined. As shown in the main circuit, this protection consists of two 1N914 diodes connected back-to-back in parallel with a 10M resistor across the input. Signals above 1 V peak-to-peak will be clipped, preventing any damage to the IC. If very fast rise time signals are to be examined then better protection for the IC can be obtained by using hot-carrier diodes such as the HP 5082-2800 instead of





The completed pc board, prior to assembly in the probe housing.

to permit direct connection to the probe tip and provide low input capacitance.

However, the presence of the pc board substrate will degrade the input impedance, surprisingly enough, and you can drill out the area of board immediately beneath pin 5 of the IC and solder the pin directly to the probe tip. For those who wish to go 'all the way' (as Frank Sinatra sings), the plastic insulation of the probe tip can be replaced with a similar piece of Teflon — if you can afford it and have access to a lathe.

The maximum input voltage permissible, when driving a high impedance load, is plus or minus 15 volts. When driving a 50 ohm load, maximum input voltage permissible is only plus or minus 10 volts (limited by maximum output current). No input protection has been included. However, if you are only working with circuits where voltages are no greater than about 1 V peak-to-peak, protection can be added by putting two diodes back-to-

back in parallel with the input, along with a 10M resistor. The maximum input voltage figures include any dc voltages present, *plus* the superimposed signal voltage.

At this stage it is only fair to tell you that the LH0033CG is an expensive device (by comparison) at around \$30 or so apiece over the counter. But — compare the total cost of this probe to a similar commercially-made type and you won't catch your breath a second time!

Construction

The project is constructed on a small double-sided fibreglass pc board with components mounted on both sides of the board. Commence by soldering in place the components that go on the top side of the board, leaving IC1 until last. Note that the positive leads of both C3 and C8 are soldered to the groundplane areas on both the top and the bottom sides of the board. Take care with the orientation of the tantalum capacitors, as well as IC2 and IC3. Having done that,

-PARTS LIST — ETI 156 -

Resistors	all 1/2W, 5%
R1	. 47R
B2. B3	68B

Capacitors

01,00	Sust to v solid tarit.
	axial leads,
	or
C2, 4, 5, 6	10n ceramic block caps.
C7, C8	10u/25 V tant.
C9, C10	470u/35 V electros
	(if required)

3113/16 V solid tant

Semiconductors

IC1	LH0033CG
	78L15A
	79L15A
D1 - D4	1N4001, 2, etc
	(if required)

Miscellaneous

ETI-156 pc board (double-sided fibreglass); RG58U coax cable and BNC plug; T1 — (if required) Arlec 6672A 240 V to 30 V transformer or similar; optional 10M/½W 5% resistor and 2 x 1N914 diodes; wire; probe housing — Jabel type PH3T or similar.

Price estimate

We estimate the cost of purchasing all the components for this project will be in the range:

\$48 - \$55

Note that this is an estimate only and not a recommended price. A variety of factors may affect the price of a project, such as — quality of components purchased, type of pc board (fibreglass or phenolic base), type of front panel supplied (if used), etc — whether bought as separate components or made up as a kit.

solder C2, C4, C5 and C6 to the bottom side of the board. Now you can install IC1. Watch the orientation — the tag on the can points toward the 'out' pin of IC2. You will have to juggle the legs a little. Push the can as far down on the board as you're able; its base should sit no more than 3 mm from the board.

Now that you have everything in place, *check it all*. It seems pretty simple, but Murphy's law will ensure that the simplest things have the highest stuff-up rates!

All's well? — now you attach the output coax cable to the underside of the board, plus the dc input and ground (0 V) wires. But—before you do, slip the output end piece of the probe case over the cable and supply wires, push it down about 150 mm or so and then slip the case of the probe case down the wires. This saves slipping them over the other end of the whole business and sliding them all the way to the probe.

The probe tip can be attached and

hi-z instrument probe

soldered in place last of all. Now you can screw it all together and attach the appropriate plugs to the other end of the cable and supply wires.

With the construction completed, you can power up and try it out. Note that the transformer suggested in our power supply is but one of many suitable types. Any transformer that will deliver at least 26 Vac at a load of about 50 mA

will suffice. Alternatively, any dualpolarity dc supply having an output between 18 and 22 volts at 250 mA will power the probe.

Notes

When using the probe to drive a 50 ohm load, the pulse response can be improved if you wish by a simple modification. Apply a fast rise time

square wave to the input and observe the output on a wideband (50 MHz to 100 MHz) CRO. The rise time can be optimised by paralleling small-value ceramic capacitors across R1 — tack them in place on the underside of the board.

Always take care that you don't exceed the input voltage limitation; LH0033s are expensive.

BYPASSING

SUPPLY LEAD BYPASSING is important in order that the LH0033 can operate correctly over the full bandwidth from dc to 100 MHz. To ensure this, the bypassing has been specially arranged and the techniques employed are probably unfamiliar to many readers.

The output circuit signal return path for the IC is via the ground and the two supply rails. Any significant impedance in series with this path (or paths) will subtract signal from the output load. Thus, the supply rail bypassing has to present an impedance which is a *fraction* (like one-tenth or better) that of the minimum output load impedance. Here, the minimum output load is about 100 ohms (R1 + 50 ohms instrument input impedance) and the supply bypassing impedance should ideally be less than 10 ohms across the frequency range.

The bypassing on each supply rail to the IC leads here takes advantage of the characteristics of three separate components to cover three sections of the frequency range.

From dc to around 100 kHz, each threeterminal regulator (IC2, IC3) has an output impedance well below one ohm, rising to four or five ohms at 1 MHz, as shown in Figure 1. The two tantalum capacitors, C1 and C3, then take over.

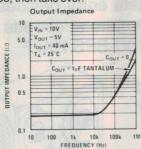


Figure 1. Output impedance characteristic of a three-terminal regulator.

Solid tantalum capacitors have a characteristic impedance that falls with frequency according to its value, which then 'flattens out' in the region around 500 kHz — 1 MHz, rising to a few ohms around 10 MHz, as can be seen in Figure 2. Thus, C1 and C3 serve as effective bypasses across the range from around 100 kHz to around 10 MHz. Axial lead tantalum capacitors were chosen as their construction exhibits the slowest impedance rise following the minimum impedance value.

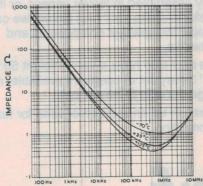


Figure 2. Impedance characteristic of axial lead solid tantalum capacitors.

To provide bypassing over the decade from 10 MHz to 100 MHz, capacitors C2

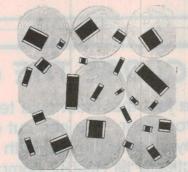


Figure 3. Ceramic chip capacitors — shown about actual size. They have no leads, just plated end pads for connections.

and C4 have been specially chosen and positioned on the pc board. For the prototype, 'chip' ceramic capacitors were used. These tiny, 'naked' chips of ceramic with a capacitor embedded in them are probably the most effective bypass capacitors made. The leads and physical construction of all capacitors form an inductance which is effectively in series with the capacitance of the component. The combined effect forms a series resonant circuit, the frequency of which (that is, the self-resonant frequency of the component) is mainly dependent on the length of the connecting leads, the particular construction of the capacitor and the way in which it is mounted. Ceramic chip capacitors, being a tiny block with connecting pads or surfaces on each end, have extremely low values of series inductance and thus very high self-resonant frequencies - see Figure 4. Now, any value of chip capacitor between 1n and 10n can be used for C2 and C4. The self-resonant frequency of a 1n chip capacitor is somewhat above 100 MHz (as per Figure 4), but that of a 10n chip is between 40 MHz and 50 MHz. Now, this isn't a problem, for the chip's impedance falls with frequency as usual until near the self-resonant frequency where it falls rapidly, reaching a minimum at the self-resonant frequency. Above that frequency its impedance rises again, but is still low enough for effective bypassing.

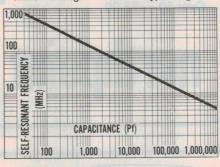


Figure 4. The self-resonant frequency versus capacitance of a typical ceramic chip capacitor.

Ordinary ceramic disc and plate capacitors behave in much the same way. The self-resonant frequency of a typical 5 mm diameter disc or 5 mm square plate capacitor depends on the lead length, as shown in Figure 5. Thus, you could use 470 pF or 1000 pF (1n) capacitors of this type for C2 and C4, provided you installed them on the underside of the board with absolute minimum lead length. More information on this subject can be obtained from "Self Resonance in Capacitors" by Roger Harrison, ETI March 1978, page 80.

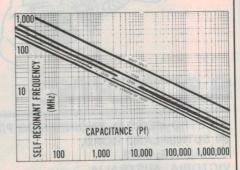


Figure 5. The self-resonant frequency versus capacitance of a typical 5 mm disc or plate ceramic capacitor with differing lead lengths (from lower curve, up — 25 mm lead length, 22 mm, 13 mm, 6 mm and none).



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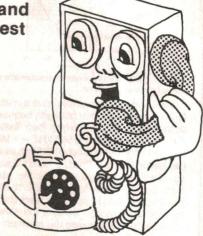
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Liquid crystal displays

Liquid crystal displays, often referred to as LCDs, are widely used in such things as watches, digital instruments, computer displays — and even pocket TV sets, these days! Here's how they work.

Robin C.H. Moorshead B.Sc.

JUST AS DAY follows night, there are certain patterns of change in the physical world which we hold to be always true. Perhaps one of the earliest that we learn is that matter exists in three states, solid (crystalline), liquid or gas. The particular state a substance exists in depends on temperature. At low temperatures substances tend to be solid, at higher temperatures liquid, and yet higher, gaseous. Furthermore, the transition between the states is clear and precise; for example, ice changes to water at 0°C — there is no gradual transition.

This pattern of change is explained by the 'kinetic theory'. This theory is based on several assumptions: that matter consists of minute, more or less spherical particles which are held together by 'cohesive forces' which are spread evenly over their surface. In the solid (crystalline) state the particles are tightly bound by the cohesive forces and are perfectly ordered like bricks in a wall. As the temperature increases, the particles begin to vibrate and the cohesive forces weaken so the particles can move about but are still attached to

one another (see Figure 1).



Figure 1.

At higher temperatures the cohesive forces are vanishingly small and the (gas) particles fly about at random (see Figure 2).



Figure 2.

Simple materials which fit into this description have another property, that their physical characteristics are the same from whichever direction they are approached. This is termed 'isotropic'. Examples of isotropic materials are glass, steel or water. Their electrical resistance, refractive index and strength are the same from whichever direction we measure them.

Against the grain

However, by no means all materials are isotropic; wood for example is much stronger across the grain than with the grain, graphite has a higher electrical resistance when measured through its 'plate' structure than when it is measured along the plates. Such materials as these are termed 'anisotropic' (see Figure 3).

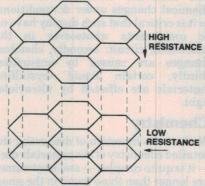


Figure 3. The resistance of graphite along and through its plates.

It would be surprising if wood and graphite were isotropic, since they are constructed of rods (cellulose fibres) and plates (the graphite). In the same way we would not expect roof slates to fall into a box in a random arrangement; they will have a strong tendency to fall flat and so order themselves into an anisotropic arrangement.

Rods and plates

Many of the large molecules found in organic chemistry have exactly the same kind of rod- or plate-like shapes and have anisotropic crystal structures. The tendency towards ordered arrangements in these substances is so great that when they melt they retain a

degree of order until the temperature is considerably increased. As a result the liquid has anisotropic properties, some flowing in a gliding stepwise fashion or interfering with the passage of light. When this happens the substance is said to possess a liquid crystalline phase (sometimes termed a mesomorphic or paracrystalline state).

So we have:

For an Increasing temperature isotropic material: solid | liquid | gas

For an anisotropic solid liquid isotropic material: isotropic liquid gas

It is of interest to note that this property has been well known since 1890, and some 0.6% (15 000-20 000) of organic chemicals show this behaviour.

Nematic and smectic

Liquid crystals fall into two main categories: nematic (from the Greek for thread) and smectic (from the Greek for soap).

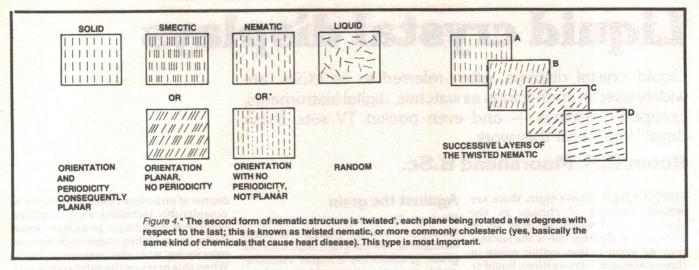
Smectic liquid crystals have many interesting properties but have found little practical application, so this article will not discuss them further.

The nematic liquid crystals have many applications and form the substance of this article. There are several types of nematic materials. The differences between these types is shown in Figure 4.

Some nematic liquid crystals possess properties which cause them to interfere with the passage of light in an applied electric field, or with changing temperature. They are of great interest in modern electronic displays for several reasons:



This 32-character liquid crystal display can produce numbers and letters on a 5×7 dot matrix in any of the 32 individual character cells. The device is made by General Electric and is model 95E from their GE-LXD product line.



1. The power consumption of such displays is extremely small, between 2 μ A and 0.2 μ A per segment of a seven-segment display, about 10 μ W per cm² of display, whereas a similar LED display consumes 500 mW.

2. They are made of the commonest elements (carbon, hydrogen, oxygen, nitrogen), rather than the more expensive elements such as gallium, germanium, etc.

3. Since they do not emit light themselves, but interfere with the passage of incident light, they cannot be 'washed out' by strong incident light.

4. They are compatible with PMOS circuits.

There are, needless to say, disadvantages as well:

1. Since they are passive, i.e. they do not emit light, they cannot be read in the dark; however, this can be overcome by providing background illumination. This increases power consumption; the power consumed does not however have to pass through the addressing circuit, as it does in LED displays.

2. Since they are operating in a phase between solid and liquid their temperature range is limited, at a maximum between -20°C and 100°C, but more typically 0°C to 60°C.

Below this temperature the display freezes; above the maximum the liquid is isotropic and no display is visible. Furthermore, the response time near the freezing point is rather slow, in the order of 0.2-second rise time and 0.6-second fall time. Freezing or liquefying the display does no permanent damage, but temperatures in excess of 150°C may cause irreversible damage. There is no doubt that future development will broaden this temperature range considerably.

3. The lifetime is still limited, but provided conditions are ideal it is now well in excess of 40 000 hours. Future development of materials with higher

purity and chemical stability will improve this a great deal.

Stability may be affected by several factors. Firstly, certain liquid crystalline materials undergo irreversible chemical changes under dc conditions, so it is critical that such display have no dc components whatsoever in the addressing circuit; secondly, chemical changes are caused by impurities; thirdly, certain liquid crystalline materials are effected by ultraviolet light.

Chemistry

We have no intention of discussing the detailed chemistry of the materials used — it is quite complex — and most names are longer than those found in the small print on toothpaste tubes. However, an outline of the structure of a typical nematic and a typical cholesteric material are included for comparison (see Figures 5 and 6).

Figure 5. A 'Shiffs' base. This has a fairly straight structure about seven times as long as it is broad.

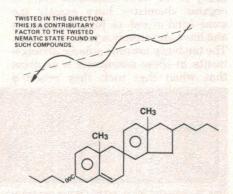


Figure 6. A cholesterol ester. The molecule is about eight to ten times longer than it is broad.

The actual material used in a display is not usually pure; it is more frequently a mixture of two or more nematics. This has the advantage of increasing the liquid range by the creation of a 'eutectic' mixture (see Figure 7).

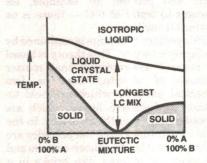


Figure 7.

The anisotropic properties that materials suitable for display purposes must include are:

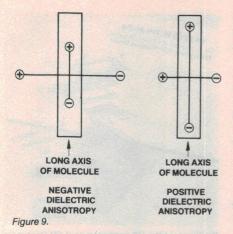
1. The refractive index is different as the material is viewed from different aspects, i.e. the light is bent more as it passes through the material in one direction than another.

2. The molecule must posess a dipole. This is an uneven distribution of change on the molecule, which causes it to align in an electric field (see Figure 8).

Figure 8.

A large proportion of organic molecules possess such dipoles. The dipole on the materials used in liquid crystalline displays have two components, one along the long axis (EII) and one perpendicular (tel) to it.

If the dipole along the long axis A is



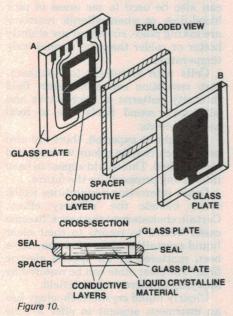
greater than the dipole perpendicular to it, it is said to possess positive dielectric anisotropy. If the dipole is greater on the perpendicular axis it is said to possess negative dielectric anisotropy (see Figure 9).

3. The material must also possess anisotropic conductivity (as graphite does). The conductivity in nematic liquid crystals is greater along the long axis than perpendicular to it.

4. The material should have a resistivity of the order of $10^9 \Omega$ cm.

Display construction

The displays work in two different ways, but the construction of the cells is similar; the differences are mainly in the filters on the back and faces of the display and in the type of background.



The cell consists of a very thin layer (about $12 \mu m$) of the liquid crystalline material between two sheets of glass, which have a conductive coating on their inside (see Figure 10). One glass plate (A) has the actual seven-segment display etched on it. The other plate (B) has a common electrode etched on it.

The conductive coating is either tin oxide or a mixture of tin and indium oxides. This provides an electrode with about 90% transmission of light.

This conductive coat is further treated so that the molecules align themselves with the surface while an electric field is not applied.

This provides a more or less translucent display. When an electric field is applied, the molecules move so as to align their dipoles with the electric field. This causes changes in the optical properties of the liquid crystal material which appears as the display.

There are two principle techniques used here, dynamic scattering and polarisation modes.

Dynamic scattering

(see In this mode the liquid crystalline material is chosen such that it has negative dielectric anisotropy, with the greater electrical conductivity along its long axis. The molecules are normally perpendicular to the surface, and when an ac field is applied the molecules, in clusters, move to re-align their dipoles with the field. The re-alignment of the dipole is in opposition to the conductivity and the liquid becomes turbulent. This turbulence is seen as milkiness in the display (see Figure 11).

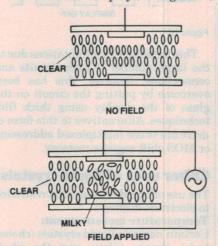


Figure 11.

Since there is no light emitted the display must be used to modify the passage of incident light. This may be done either by passing light through the display, or more usually by reflecting light from a mirror behind the display (see Figures 12 and 13).

The transmissive cell will appear to glow and the reflective cell will appear misty where the segments are switched on. These displays have the shortcoming of a rather low 'contrast ratio'. That is, the apparent difference between the switched on and switched off display is not very great.

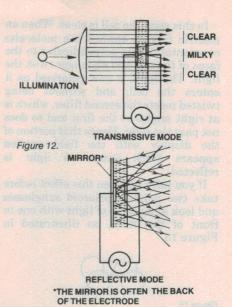


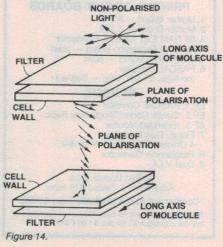
Figure 13.

Polarisation modes

The display is constructed in basically the same way as the dynamic scattering cell. The difference lies in the type of liquid crystalline material. The material used is one which assumes a twisted nematic structure and has positive dielectric anisotropy (the major component of its dipole along its long axis).

In this case the inside faces of the cell are coated so that the molecules are parallel to them and aligned in a particular direction when no electric field is applied.

The cell thickness is designed so that there is a complete 90° turn of molecules between the top and bottom faces. The twisted nematic has the property that it twists light that passes through it. Polaroid filters are fitted above and below the cell so that light is polarised as it enters, and is twisted through 90°, exiting through a filter opposed at 90° to the first. The light is then reflected off a mirror and returns via the same pathway (see Figure 14).



In this state the cell is clear. When an electric field is applied the molecules re-orientate to lie perpendicular to the faces of the cell and no longer twist the light. The light is now polarised as it enters the cell, and without being twisted meets the second filter, which is at right angles to the first and so does not pass the light. Hence that portion of the display with the field applied appears black (since no light is reflected).

If you have not seen this effect before take two pairs of polaroid sunglasses and look at a source of light with one in front of the other, as illustrated in Figure 15.



Figure 15.

Held in this way light, although polarised, is free to pass through the second filter, since the plane of polarisation is the same for both lenses. If one



Figure 16.



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lens is now rotated through 90°, as in Figure 16, no light passes, since the light polarised by the first lens will not pass through the second.

The effect of having the 'crossed polaroids' in the cell causes almost total extinction of reflected light and consequently a high contrast ratio, an almost completely black and white display. This is many times better than the dynamic scattering cells.

Addressing technique

The cells are normally operated under ac conditions (although some cholesteric cells may operate under dc).

The technique commonly used is to have dc pulses of identical amplitude, one applied to the back, the other to the display segment via an exclusive OR gate. In the off state the two signals are in phase, in the on state they are out of phase (see Figure 17).

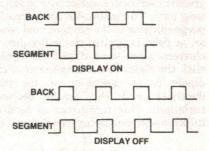


Figure 17.

This technique has limitations due to the large number of both circuits and connections; however, this has been overcome by putting the circuit on the glass of the display using thick film techniques. Alternatives to this form of drive are to use multiplexed addressing or MOS shift register memory.

Other uses of liquid crystals

The use of liquid crystal is not restricted to electrical displays.

Temperature measurement:

Certain nematic liquid crystals (cholesteric) change colour over the whole range of the spectrum (red to violet) as their temperature changes. Furthermore, the colour change is over a very narrow temperature range, usually 2 or 3°C. The temperature at which this happens, and the range over which the change takes place, can be adjusted by use of mixtures of different cholesterics.

A set of ten to twelve of such cells in a row, the following one starting to show colour at 2°C higher temperature than the previous one, forms a useful thermometer working over a fairly restricted range. It has found application as living room and refrigerator thermometers.



Beckman of the US produced an 'LCD Designers Kit, model 750-1' a few years back that was enormously successful. This has been superseded by their model 750-2 designers kit, which includes a 6 mm, four-digit LCD display (the 741-4), plus polariser, connectors, board and instruction manual. Beckman products are distributed in Australia by Warburton Franki.

Perhaps a more important application is using liquid crystals which have a very narrow range over which they change colour (0.5°C). They have found application in medicine, since they can resolve differences of 0.05°C.

Assuming the liquid crystal is set to show colour at normal skin temperature, any local deviation from the correct temperature will show as a different colour. This has applications in detecting cancers, since they tend to be hotter than normal body heat. They can also be used to see areas of poor blood-flow, or where allergic reactions are taking place, since they are slightly hotter or colder than the normal body temperature.

Cells with extremely low temperature resolution can even detect field intensity patterns of microwaves and ultrasonic sound fields due to local heating effects.

As might be expected, there are also cells which change colour with applied electric field. This would appear to have interesting prospects for the future.

Other interesting possibilities which occur include the 'memory effect'. Certain cholesterics take hours, in some cases weeks, to return to their clear liquid crystalline state after they have been scattered by an applied electric field. The clear state can be restored by applying a different electric field.

Clearly liquid crystal technology has an enormous amount to offer a wide variety of fields - electronics, medicine and others. We are likely to see further interesting developments in the next few years as this technology takes over and improves on existing display techniques. How about an alphanumeric display with independently variable colour segments?

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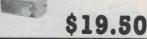


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ETI458 LED level meter for new ETI5000 preamp. Kit includes deluxe fibreglass PCB and all components to build one meter (mono). Case & power supply not included as it's intended to be part of the new preamp

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The AT609 is a lead wire consisting of 14 twisted strands. Each strand is 99.99 percent pure silver and 0.12mm in thickness and insulated with polyurethane. The whole is then covered with Teflon. Transmission loss is minimised by avoiding coloration, which, if used, impairs the properties of the insulators. The gold-plated tips at both ends of the AT609 are attached to the lead wire with high quality silver contained solder.

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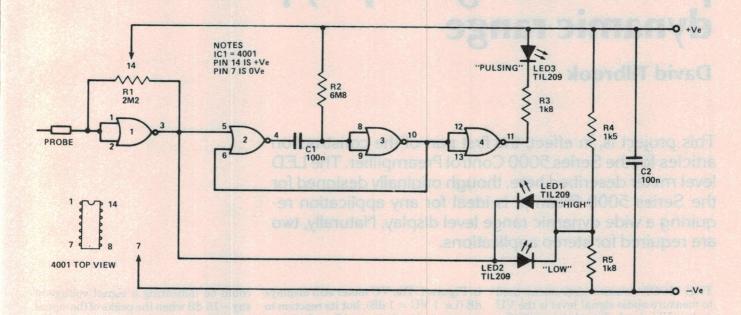
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CMOS logic probe

A LOGIC PROBE is a device which is used when testing digital circuits, and it shows the logic state at the selected test point. In common with most designs, this one can indicate four input states, as follows:

- 1. Input high (logic 1).
- 2. Input low (logic 0).
- 3. Input pulsing.
- 4. Input floating.

The circuit uses the four two-input NOR gates contained within the 4001 CMOS device and is primarily intended for testing CMOS circuits. The probe derives its power from the supply of the circuit being tested. The first gate has its inputs tied together so that it operates as an inverter, and it is biased by R1 so that roughly half the supply potential appears at its output. A similar voltage appears at the junction of R4 and R5, and so no significant voltage

will be developed across D1 and D2, which are connected between this junction and gate 1 output. Thus under quiescent conditions, or if the probe is connected to a floating test point, neither D1a or D2 will light up. If the input is taken to a high logic point, gate 1 output will go low and switch on D1, giving a 'high' indication. If the input is taken to a low test point, gate 1 output will go high and D2 will be switched on to indicate the 'low' input state.

A pulsed input will contain both logic states, causing both D1 and D2 to switch on alternately. However, if the mark to space ratio of the input signal is very high, this may result in one indicator lighting up very brightly while the other does not visibly glow at all. In order to give a more reliable indication of a pulsed input, gates 2 to 4 are connected as a buffered output monostable multivibrator. The purpose of

this circuit is to produce an output pulse of predetermined length (about half a second in this case) whenever it receives a positive-going input pulse.

The length of the input pulse has no significant effect on the output pulse. D3 is connected at the output of the monostable, and is switched on for about half a second whenever the monostable is triggered, regardless of how brief the triggering input pulse happens to be. Therefore a pulsing input will be clearly indicated by D3 switching on.

The various outputs will be:

Floating input — all LEDs off.
Logic 0 input — D2 switched on (D3 will briefly flash on).

Logic 1 input — D1 switched on.

Pulsing input — D3 switched on, or pulsing in the case of a low frequency input signal (one or both of the other indicators will switch on, showing if one input state predominates).

LED level meter features simultaneous peak & average display plus 60 dB dynamic range

David Tilbrook

This project is, in effect, the first part of the construction articles for the Series 5000 Control Preamplifier. The LED level meter described here, though originally designed for the Series 5000 Preamp, is ideal for any application requiring a wide dynamic range level display. Naturally, two are required for stereo applications.

THE MOST common instrument used to measure audio signal level is the VU meter (VU stands for volume unit). Before the introduction of the VU standard however, ordinary meter movements were used. A full-wave rectifier converted the applied audio signal to dc suitable for driving a voltmeter, usually fitted with a dB scale. Although this is completely suitable for steady sinewave measurement it is entirely unsatisfactory for measurement of constantly changing voltages such as audio signal level. The biggest problem is overshoot of the meter movement. If a 1 kHz sinewave, for example, is applied to this type of meter, the movement can overshoot the correct reading by nearly 80%, indicating a transient that is in fact not present. The VU standard was introduced to overcome these problems. It does this by defining the 'ballistics' of any meter movement to be used in audio signal level measurement. A comparison of VU and ordinary meter movement ballistics is shown in Figure 1. The amount of overshoot of the VU meter is specifically defined by the standard to be not less than 1% and not greater than 1.5%. This characteristic is achieved by carefully modifying the shape of the meter pole pieces and counterweighting the pointer. These techniques ensure that the movement stabilises in the shortest possible time, around 0.3 s (300 ms) for the case shown

in Figure 1. The VU meter still displays dB (i.e: 1~VU=1~dB), but its reaction to transient signals is significantly better than the ordinary meter movement.

could be indicating a signal voltage of say -15 dB when the peaks of the signal are actually overloading an amplifier. Another disadvantage of most VU

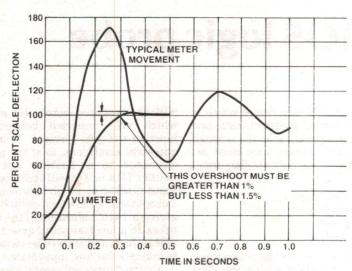


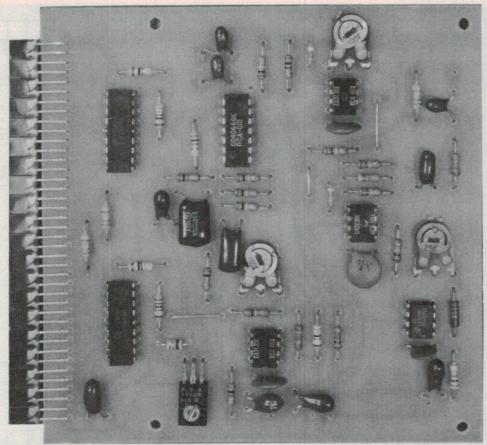
Figure 1. 'Ballistics' of a VU meter compared to conventional moving-coil meter.

Nevertheless, the VU meter is still very slow. It indicates something between the average and the real peak of the signal voltage depending on the complexity and transient nature of the particular input signal. The 0.3 s rise time of the meter will hide all but the most repetitive peaks, so a VU meter

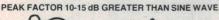
meters is their limited dynamic range. Usually they display only the 'top' 23 dB of the total range (i.e: -20 to +3 dB) and with the ever increasing dynamic range of modern recording techniques this is not sufficient.

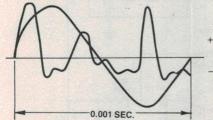
The ETI-458 overcomes these problems by replacing the meter movement

pk/av LED level meter



Full-size reproduction of the completed project. Note the components are laid flat to permit close stacking of two boards for a stereo display.





A typical 'music' signal may have a completely different peak-to-average ratio compared to a sinewave, and the peaks are often not symmetrical in amplitude about the zero axis. The duration of peaks may be as short as 50 microseconds.

with a row of light emitting diodes driven by a pair of dB LED display drivers. Twenty LEDs are used, with 3 dB between each LED, so the total dynamic range displayed is 60 dB. The circuit monitors both the true peak and the average signal level and displays both simultaneously. The difference between the peak and the average voltages of a sinewave is around 3 dB, so with a sinewave applied consecutive LEDs will light. With music applied however, the difference between the two LEDs will be substantially greater, depending on the transient nature of the signal applied.

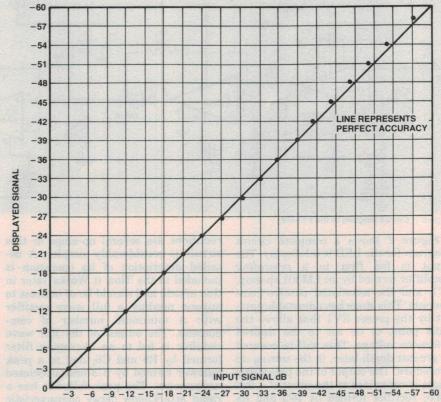


Figure 4. Accuracy of the ETI-458 LED level meter display (dots) compared to 'perfect accuracy' (line).

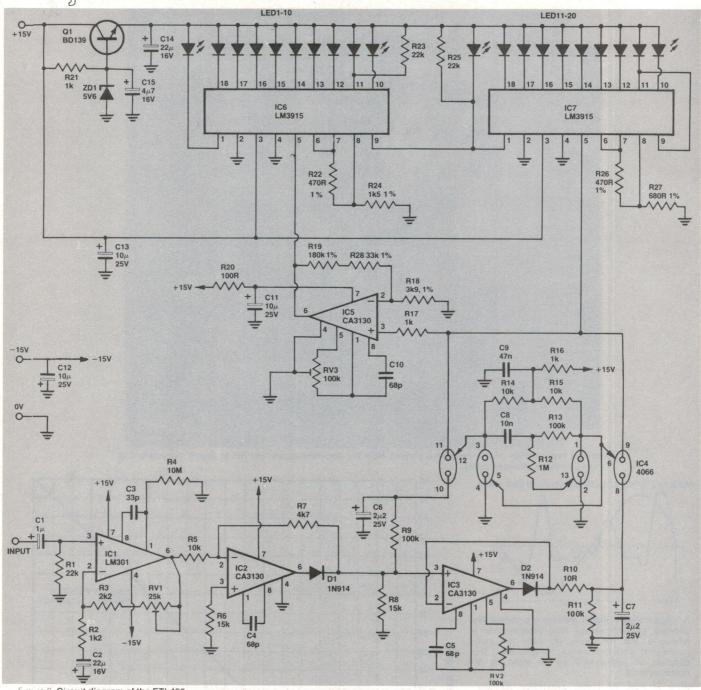


Figure 2. Circuit diagram of the ETI-458.

Figure 2 shows a complete circuit diagram for the LED level display. The input is fed first to a prescaling amplifier formed by an LM301 op-amp, IC1, and the associated passive components. This stage has adjustable gain, set by the preset RV1 that allows the 0 dB point to be set to the desired reference voltage. This will be covered in greater depth later, in the setting up procedure. The output of the prescaling stage is connected to the input of a full wave rectifier formed by IC2 and its associated components. Most full wave

rectifiers use several op-amps so this circuit is considerably simpler. A detailed description of its operation is included in the 'How it Works' later in this article and should be of interest to anyone needing a full wave rectifier with a minimum number of components. The output of the full wave rectifier is fed to an averaging filter formed by R9 and C6, and to a peak follower formed by IC3 and associated components. The peak follower has a rapid attack/slow decay characteristic so that it responds quickly to any transients but decays slowly so the transient can be seen easily on the display. The outputs from the peak follower and the averaging filter are connected to the inputs of two CMOS analogue switches. The outputs of these switches are connected together and go to the input of the LED display. Two more CMOS switches are used to form a square wave oscillator. This oscillator has out of phase outputs used to drive the signal-carrying analogue switches alternately off and on at a relatively high frequency. When the switch connected to the output of the averaging filter is on, the average signal voltage is connected to the input of the LED display. This switch is subsequently turned off by the oscillator and the other analogue switch turned on, connecting the output of the peak follower to the LED display. So, only one of the two LEDs is on at any instant, but the rapid switching speed between them and the persistence of vision make them both appear to be on.

Input signals to the LED display portion of the circuit are fed simul-

taneously to the LM3915 driving the upper 30 dB display and via a voltage amplifier to the lower 30 dB display. The biggest problem in the design of an audio level meter with a 60 dB dynamic range arises from the fact that 60 dB below typical 0 dB input voltages could be around 2 mV. This is well below the dc offset voltage of most op-amps so special precautions have been taken in the design to ensure that dc offset errors can be reduced to negligible levels. This is the purpose of the presets RV2 and RV3. These are dc offset controls. Ad-

justment of these is covered in the setting up procedure. The sensitivity of the LM3915 can be adjusted by changing the voltage between pins 6/7 and ground. The IC maintains a voltage of 1.25 V across R22. The current through R22 will be 1.25/470 or approximately 2.67 mA. A further 75 uA is supplied from pin 8 of the device, so the total current through resistor R24 to ground will be 2.67 mA + 75 uA or approximately 2.73 mA. The voltage drop across R24 will therefore be around 2.73 mA x 1.5k or 4.1 V. Adding the 1.25 volts across R22 gives a total of 5.35 V between pins 6/7 and ground.

This means that the topmost LED driven by IC6 will light when the input voltage to the device is 5.35 V. Now, 30 dB below this is:

$$\frac{-30}{20} = \log \frac{x}{5.35}$$

or 1.17 V, which is well above the voltage expected on the output of IC5 due to the dc offset. The reference voltage used was chosen specifically to ensure that this would be the case. Now the easiest way to cascade two LM3915s would be to simply set the reference voltage of the second LM3915 the same as that of the first and precede the first one by a 30 dB gain amplifier. However, with the recommended supply voltage of +/-15 V the maximum peak signal voltage that can be delivered by IC1 will be around 6 V. The operation of the absolute value generator (full wave rectifier, IC2) further divides this by two, so the maximum peak signal voltage available will be around 3 V and the top several LEDs would never be lit. To overcome this problem the reference voltage of IC7 is decreased so that the top LED will be lit by a 3 V input signal, and the gain of the amplifier formed by IC5 is changed accordingly.

The resistors R26 and R27 set the reference voltage of IC7 at 3.1 V and 30 dB below this voltage is

$$\frac{-30}{20} = \log \frac{x}{3.1}$$
, or 98 mV.

Now, the top LED driven by IC6 must correspond to this voltage, so the required gain around IC5 is 5.34/98 mV or 54.6. The values of the resistors R19 and R18 set this gain at (180+33+3.9)/3.9 or around 56 which is a good enough approximation, amounting to an error of less than 0.5 dB.

HOW IT WORKS — ETI 458

The input stage consists of a variable gain amplifier formed by IC1 and its associated components. This is a conventional IC amplifier circuit in which the gain is determined by the values of the components RV1, R3 and R2. Specifically:

$$A_V = \frac{R2 + R3 + RV1}{R2}$$

So the bigger the value set on RV1, the greater the gain. Capacitor C2 has the effect of decreasing this gain for very low frequencies, or dc, decreasing the dc offset on the output.

The second stage is the full wave rectifier or 'absolute value generator'. As mentioned in the text, most full wave rectifiers require more than a single op-amp, so this stage will be of use in any application requiring a full wave rectifier with minimum component count. For negative-going signals the stage functions as an inverting amplifier with a gain of 0.5. This is determined by the values of R5 and R7. When the input signal goes positive the output is driven hard against its negative supply voltage, which in this case is 0 V. So the output stage is turned off, and has a relatively high output impedance. In this state the resistors R5, R7 and R8 form a potential divider and connect the input signal to the output directly. Again, the output voltage is one half of the input voltage. In order for this circuit to work, the output stage in the op-amp must be CMOS so that the output can go completely to 0 V and have an output impedance high enough not to short out the signal voltage from the potential divider. This is the reason the CA3130 is used. Furthermore, this is a relatively fast device which ensures that the full wave rectifier will have a frequency response that covers the entire audio spectrum. The one disadvantage of the circuit is that it requires a high load impedance since the output signal for positive-going input signals is obtained from the potential divider and not from the op-amp itself. In this application the load is around 100k (R9) which causes negligible error.

The output of the full wave rectifier is fed simultaneously to an average filter formed by R9 and C6, and to the peak hold circuit formed by IC3 and its associated components. The peak hold circuit is really nothing more than a 'precision diode' that charges a capacitor to the peak voltage. The precision diode is formed by including a conventional signal diode in the feedback loop of a fast op-amp. If an input signal is applied which is less than the forward voltage drop of the diode, the stage is

effectively in open loop gain (around 320 000 for the CA3130). The output voltage will rise very quickly, turning the diode on. Since the output of the diode is connected to the inverting input of the op-amp, the stage functions with unity gain once the diode has been turned on. Capacitor C5 ensures stability of the stage while preset RV2 allows adjustment of dc offsets due to this stage. The output of the peak hold circuit charges capacitor C7 through resistor R10. The combination of R10 and C7 defines the attack rate of the peak detector.

As shown, the value of R10 is 10 ohms and this is small in comparison to the output impedance of the CA3130, but is included in case some applications require the peak detector to have a slower attack rate. With the values shown, the LED level meter will display single 50 uS pulses accurately and this is entirely adequate for any audio application.

Resistor R11 discharges the capacitor and its value of 100k dictates a decay rate of around one second. This gives the level meter its rapid attack, slow decay characteristic and enables even short transients to be spotted.

As explained in the text, both the average and the peak levels of the signal are displayed simultaneously. This is accomplished by multiplexing the outputs of the peak and average detectors. This is done by switching between the output of these two circuits at a relatively high frequency (say a few hundred Hertz). In the circuit, this is done with CMOS transmission gates. The 4066 was chosen mainly because its on resistance is a little lower than the older 4016 and this enables the remaining two gates in the package to be used as the driving oscillator. The oscillator is formed by resistors R12 to R15 and capacitor C8, with the associated two transmission gates. The frequency of the oscillator is determined by the values of R13 and C8 at around 150 Hz.

IC5 functions as an amplifier stage as discussed in the text. Once again dc offset adjustment is provided, this time by RV3. Capacitor C10 provides the necessary compensation to ensure stability. Details of the two LED drivers and the amplifier formed by IC5 are in the main text.

The transistor Q1 and the associated components R21, C15 and ZD1 form a simple 5V regulator to power the LM3915s. Capacitor C16 is essential for stability of the LED drivers and must be mounted close to the LEDs.

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PS12 ETI 142 Power Supply
PS12 ETI 142 Power Supply 0-30 V 0-15 A (fully protected)

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TE13 ETI 117 Digital Voltmeter 1975 Display
TE14 ETI 117 Digital Voltmeter 1976 Display
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TE18 ETI 118 Digital Frequency Meter 1975
Display TE19 ETI 118 Digital Frequency Meter 1976

TE19 ETI 118 Digital Frequency Meter 1976
Display
TE33 E.A. Simple Function Generator
TE34 ETI 487 Real Time Audio Analyser
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R33 ET I 718 Shortwave Radio R34 ET I 490 Audio Compressor R35 ETI 721 Aircraft Band Converter (less XTALS) R36 ETI 726 6 or 10 metre Power Amp R37 ETI 475 Wide Band A.M. Tuner R38 ETI 585 Ultrasonic Switch

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MS EIT 549 INDUCTION BAILANDE MEAN INCLUDES WIRE for SEARCH head M4 ETI 547 Telephone Bell Extender M5 ETI 602 Mini Organ (less case) M6 ETI 544 Heart Rate Monitor M7 ETI 044 Two Tone Doorbell M8 ETI 043 Heads and Tails M9 ETI 068 L.E.D. Dice Circuit M10 ETI 620 Touch Switch M10 ETI 539 Touch Switch M14 ETI 701 Mashead Amplifier M23 E.A. Elektronic Roulette Wheel M25 E.A. Digital Metronome M26 E.A. Voice Operated Relay M29 E.A. Sound Effects Generator M30 ETI 551 Light Chaser 3 channel 1000

watt/ch
M32 E.A. Remote TV Headphone
M34 ETI 650 STAC Timer
M36 ETI 557 Reaction Timer
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M38 ETI 814 Dinky Die
M39 E.A. Electronic Combination lock (including

lock) M40 E.A. Mast Head Amplifier M41 ETI 576 Electromyogram M42 E.A. Prospector Metal Locator including

M43 ETI 561 Metal Locator less dowel and

M43 E11 561 Metal Locator less dowel and tubing potplant stand M44 E.A. Musical Tone Generator M45 E.A. Light Chaser 3 channel M46 E.A. Power saver for induction motors M47 E.A. Twin Tremelo for Organs /Stage Amps M48 E.A. Lissajouis Pattern Generator

M49 E.A. Selectalott M50 ETI 1500 Discriminating Metal Locator M51 E.A. Light Chaser M52 E.A. Cylon Voice

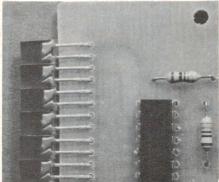
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Project 458

Internally, the LM3915 consists of a string of comparators; each one compares the input signal to a reference voltage it derives from a ten-way potential divider (see Figure 3). The accuracy of the LM3915 is determined by these internal resistors and is therefore very good. To ensure the display is accurate over the entire 60 dB range it is only necessary to ensure that the changeover from one LM3915 to the other is accurate. Resistors R18, R19, R22, R24, R26, and R27 have been specified as 1% tolerance types for this reason. This is probably unnecessary for most applications. I have built the unit using 5% types and the error was only around 1.5 dB which is effectively hidden by the 3 dB increments between LEDs. Figure 4 shows the accuracy of one of the prototype units built with 1% resistors in the places specified. If the accuracy were perfect, all the dots would lie on the straight line. The deviation from the line is only small, so the unit is very accurate over the entire 60 dB dynamic range.

Transistor Q1 forms a simple voltage regulator delivering 5 V to the LEDs. This decreases the power dissipation in the LM3915s. The current consumption from the positive rail is around 100 mA while the negative rail needs only several milliamps. If the display is to be used from an existing power supply in a preamplifier for example, care should be taken to ensure that the relatively high positive rail current does not upset the preamplifier performance. In the Series 5000 preamp a separate positive rail is used for the display to decrease any possibility of interaction between the display and the audio signal voltages in the preamp.



Close-up of the pc board showing orientation of the LEDs. IC7 at lower right.

Construction

The pc board is virtually essential for this project, particularly if you are constructing it as part of the Series 5000 Control Preamp.

Start construction by mounting the

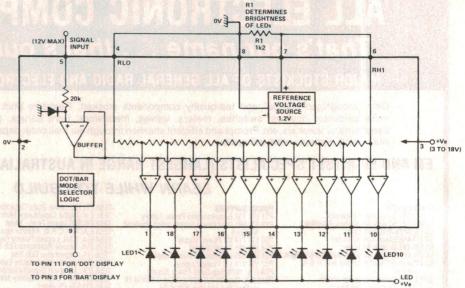


Figure 3. Internal block diagram of the LM3915.

LEDs. This is by far the most difficult part of the project. The LEDs must be inserted evenly and with equal heights, and this is not easy. Furthermore, the LEDs must be inserted the right way around. The longer of the leads represents the anode of the LED. Check the orientation of each LED against the overlay, before soldering. The best way I found to mount the LEDs is to start by inserting the first LED on one end of the display. Bend this LED flush against the edge of the pc board. Now solder the leads and bend the LED upright again. Insert the next LED and ensure that its height on the board is identical to the first. Now solder the second LED into position. Continue like this for the remaining eighteen LEDs, checking the orientation of each one as you go. After all the LEDs are soldered into position check that the heights are all even and make any adjustments needed now by reheating the appropriate solder joints. Be careful when soldering the LEDs that you do not overheat the leads; this will damage the device and is very easily done. Once all the LEDs are even bend the whole line down against the circuit board as shown in the photographs.

Now all the other components can be mounted. The order of mounting is not really important although it is good general practice to solder the passive components first (resistors and capacitors). And then solder the ICs and transistors. In the Series 5000 Preamp the LED level displays are mounted directly above one another, so all components should be mounted as close as possible to the pc board. The presets are mounted against the circuit board and this is best done by bending their leads at right angles first, and then

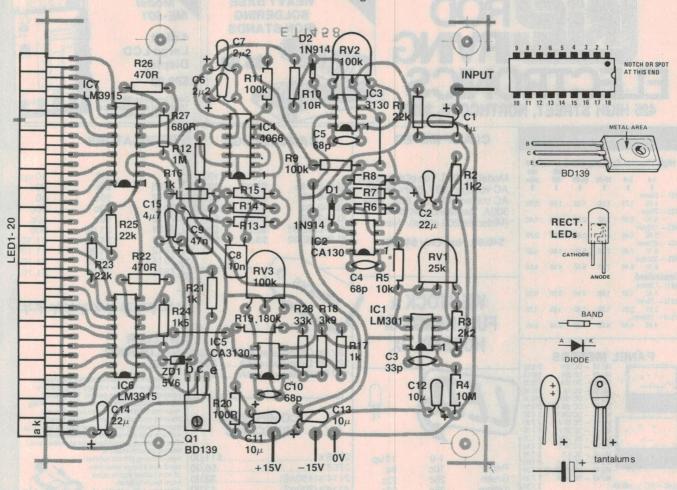
soldering. Similarly, many of the larger capacitors, such as the greencaps and ceramics, may have to be folded against the board. Leave sufficient lead on the components so that this can be done. Alternatively, bend the component over before soldering. Be careful with the orientation of all polarised components, such as transistor Q1 and the electrolytic and tantalum capacitors. Tantalum capacitators, for example, are very intolerant of reverse biasing.

Setting up proceedure

Once all the components have been mounted on the pc board and checked, the unit can be switched on. Ensure that the power supply you are using has sufficient current capability for the positive rail and that it is correctly connected to the supply points on the circuit board. If the input is touched with a finger two LEDs should light and move up the display. If all is well the dc offsets can now be adjusted. The preset RV2 adjusts the dc offset of the peak follower. This will be adjusted to equal the dc level of the average filter, i.e; that from the output of the full wave rectifier. The overall dc offset can be nulled by RV3.

First connect the input of the LED level meter to earth on the board. This ensures that no signal voltage will be present when the adjustments are made. Now turn both RV2 and RV3 fully clockwise; both LEDs should run off the bottom of the display. Turn RV3 slowly anticlockwise until the second LED from the bottom has just turned on. If RV2 is now turned anticlockwise also, a second LED will light on the display. This is the peak level LED. Adjust RV2 to superimpose this LED

pk/av LED level meter



onto the second bottom LED. Now adjust RV3, turning it clockwise again until the LED has just run off the

bottom of the display.

The final stage in the setting up procedure is to align the meter for the appropriate 0 dB level. Preset RV1 varies the gain of the prescaling amplifier stage formed by IC1. Adjustment of this preset will vary the input voltage required to light the top LED between 260 mV and 2.5 V. If your application requires 0 dB to be a higher voltage than 2.2 V, use a potential divider at the input to decrease the input signal voltage. If more gain is required increasing the value of the preset from 25k to 100k will decrease the necessary input voltage to around 70 mV, which should be sufficient for most applications.

LED is designated +9 dB, so the fourth LED from the top is 0 dB. Calibration of the 0 dB reference is best left until the preamp is finished and the procedure will be described in the Series 5000 Preamp construction article, coming

In the Series 5000 amplifier the top soon.

DESCRIPTION OF THE	PARTS L	.IST — ETI 458 ———————————————————————————————————
Resistors	all ½ W, 5% unless marked otherwise	C9
R1, 23, 25 R2 R3 R4 R5, 14, 15 R6, R8 R7 R9, 11, 13 R10 R12	1k2 2k2 10M 10k 15k 4k7 100k 10R	C15
R16, 17, 21 R18 R19 R20 R22, R26 R24 R27 R28	1k 3k9 1% 180k 1% 100R 470R 1% 1k5 1% 680R 1%	LED1 - 20 Siemens LD80-2 or sim. Miscellaneous ETI-458 pc board (double-sided); one 6 BA bolt and nut. Price estimate We estimate the cost of purchasing all the
RV2, RV3 Capacitors C1 C2, C14 C3 C4, 5, 10	100k min. trimpot1µ/6V tant22u/16 V tant33p ceramic68p ceramic	state of the state
	2u2/25 V tant, 10n greencap	supplied (if used) etc — whether bought as separate components or made up as a kit.

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HS4 - 225m	nm				
8.10		7.10	5.90	4.50	4.30
HS5 - 300n	nm				
8.90	8.40	7.90	6.50	4.90	4.60
Unanodis	ed				
HS11 - 38n					
1.40		1.00	0.90	0.80	0.70
HS12 - 75m					7.11.
2.50	2.20	1.90	1.60	1.25	1.20
HS13 - 150		100			
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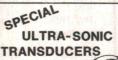
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100	8 Pin 14 Pin 16 Pin	1-9 .9 1.1 1.2	0 10-25 0 .82 0 1.00	1 1 1	0,000uf 0,000uf 0,000uf 5,000uf	16V 25V 40V 40V	9.00 9.50 11.90 12.00	12 Way 16 Way 20 Way 40 Way	.75 1.00 1.35 2.70	.65 .90 1.25 2.50
			0 1.70 81LS97 TRANSISTOF 2N301 2N657 2N301 2N1613 2N1711 2N1657 2N1813 2N1711 2N1813 2N1711 2N1813 2N1219A PN2222 2N2463 2N2484 2N26467 2N2894 2N26467 2N2894 2N2905 2N2906 2N2903 2N3054 2N2905 2N2907 2N3905 2N3905 2N3905 2N3905 2N3905 2N3905 2N3905 2N3905 2N3905 2N3906 2N3906 2N3906 2N3906 2N3906 2N3906 2N3906 2N3568 2N3689 2N3684 2N3689 2N3684 2N3689 2N3684 2N3699 2N3684 2N3699 2N3684 2N3699 2N3693 2N3709 2N	2.10			TIP32C TIP33A TIP24B TIP34A TIP24B TIP42B TI	40 Way 1.00 1.10 1.20 1.50 1.10 1.30 1.30 1.30 1.30 1.20 1.50 0.12.00 0.12.00 0.0 NS1.40 0.0 NS1.40 0.0 NS1.40 0.0 NS1.40 0.0 NS3.50 0.0 NS3.5	2.70 8295 DM8578	2.50 25.00 3.50 3.50 3.50 10.00 17.50 99.00 3.00 3.00 3.00 3.00 85 1.20 90 90 90 90 90 90 90 90 90 90 90 90 90

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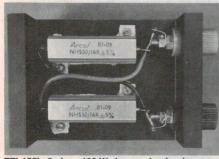
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See Situations Vacant columns for advertisement for PLANT PURCHASING OFFICER and SENIOR SPARE PARTS CONTROLLER, also for Transport & Works' plant organisation.

High power 'dummy loads' for audio amplifier testing

Apart from a multimeter and perhaps an oscilloscope, a resistive dummy load of 4, 8 or 16 ohms impedance capable of dissipating up to 100 watts is just about the most useful item of test equipment the audio enthusiast could have. Here are several ways to build one.

Andrew Kay Roger Harrison



ETI-155b 8 ohm, 100 W dummy load using non-inductive resistors.

WHEN IT COMES to designing electronic equipment — from the very simple to the very complex — if one asked several designers how they would go about a certain design problem undoubtedly you'd get a different answer from each. Here again, we see a fine example illustrating that old saying — "... there's more than one way to skin a cat."*

The project staff at ETI have spent some considerable time over the past two years developing a variety of amplifiers. The fruits of these labours have been duly published and enjoyed by many readers. However, we've always lacked a *decent* dummy load for such work and have sort of *made do* with such contraptions as a string of one ohm 5W resistors dangled in a tub (plastic!) of water, lengths of electric jug element, etc, etc. Whilst jury-rigging such things is in the finest traditions of electronic design and development, the (more than) occasional mishap is not just a

frustrating interruption but often a decided nuisance giving rise to dark mutterings, steam from the ears and shouts of "we'll have to get a *decent* dummy load?!!"

As no doubt many of our more intrepid readers, and/or do-it-yourself audio fanatics, have discovered, such things are hard/difficult/impossible (... delete whichever not applicable) to come by.

Then, Everest Electronics came to the rescue. Eagle-eyed readers will have seen the item we ran in News Digest in the March 1981 issue concerning the Arcol range of metal-clad power resistors carried by Everest. When the information arrived, quick as a flash we organised some non-inductive types for a dummy load. Several weeks later two 16 ohm 50 W non-inductive Arcol resistors arrived on the Editor's desk. An hour later we had a working dummy load! Naturally, everybody thought it would make a good project...

In the meantime, a freelance associate of ours, Andrew Kay, had desired exactly the same thing. Andrew, however, went about solving the problem a different way. He purchased a batch of one watt 1% resistors and made a 50 W dummy load. But, he figured, why not have a little more versatility and make two the same, allowing parallel and series connection to obtain a 4 ohm, 100 W dummy load or a 16 ohm 50 W dummy load as well as a twin 8 ohm 50 W dummy load enabling testing of both channels of a stereo amplifier at the same time! Frankly, we

don't know why we didn't think of it earlier ourselves.

So — here follows the description of several ways to skin a cat/rockmelon/ whatever, or build some high power audio dummy loads.

Multi-resistor method (ETI-155a)

By parallelling resistors of an appropriate value, one can obtain an effective resistance of the wanted value and



ETI-155a 4, 8 or 16 ohm dummy load (50 or 100 W) using 98 390 ohm 1 W resistors.

^{*} For cat lovers we'll modify that to "... skin a rockmelon", or something similar!

Project 155

wattage rating. Now, the cheapest, most common power rating for carbon film resistors is one watt (1 W). To obtain a 50 watt resistor, 50 would need to be paralleled. To obtain an effective resistance of eight ohms, each 1 W resistor would have to have a value of 400 ohms. The nearest preferred value is 390 ohms. Fifty in parallel would give an effective resistance of 7.8 ohms which is about 21/2% lower than the ideal eight ohms. However, 49 in parallel gives an effective resistance 7.959 ohms — less than ½% out. If you require the tolerance of your load to be within 1% or better, then you'll have to use 1%, 1 W resistors. If you only require a tolerance of +/-5%, then the common 5%, 1 W variety will do the job. Either way, you're better off using 49 resistors so that the effective resistance of the load comes closer to the ideal eight ohms.

The dummy load described here consists of two eight ohm loads, which enables the testing of both channels of a stereo amplifier.

The idea itself is not at all new or original, having been used by radio amateurs for years to obtain resistive dummy loads for terminating radio transmitters while they are on test. The advantages of a dummy load for any kind of power source are:

- the power source (in this case an AF power amplifier) is presented with an ideal resistive load of the correct value.
- the chances of damaging expensive loudspeakers during experimental phases of construction are eliminated, and
- completely silent "full power" testing is made possible even for extended periods of time; which is great for public relations and your ears.

Essentially each dummy load consists of 49 high stability 1% metal film resistors connected in parallel to give a terminal resistance of 8 ohms. The author used cheap, readily available Beyschlag type MBE 0414 1 W series. Since the tolerance rating of the resistors is 1%, the upper tolerance limit for the combination is 8.04 ohms and the lower limit is 7.88 ohms. The number of resistors to be bought was a compromise between the desire for a result of exactly 8 ohms and the need to keep the cost to a minimum. Obviously, larger numbers of resistors could be used (say 70 x 560 ohms in parallel) and the reader can easily vary the circuit to suit the pocket and availability of the resistors. The resistors used in this project can be obtained from

Crusader Electronic Components at 81 Princes Highway, St. Peters NSW, for about 6¢ each. This price is for quantities of 100 up, but since the dual circuit uses 98 resistors there is no difficulty here.

Separate terminal posts are provided for each load so that two separate 50 W sources can be terminated in 8 ohms each or the two halves may be connected in series to give a single 16 ohm 50 W load; and last but not least parallel connection of the two halves will result in a 4 ohm 100 W load. Because metal film resistors are used there are no inductive effects to worry about such as could occur if wirewound units were employed. The stray capacitances present are so low as to be insignificant.

Construction is simple, if somewhat tedious. Lots of soldering is involved! The author used two ordinary household tin cans; one can has a lid (e.g. a coffee tin) the other is a smaller one of the throw-away type (baked beans etc!). The top and bottom of the smaller can were used as soldering planes for terminating the ends of the resistors while the larger can was used to house the project with the lid carrying the terminal posts. Since the coffee tin is virtually leak proof you could fill it with some kind of insulating fluid such as transformer oil and thereby increase the dissipation capability of the dummy loads.

Tin-plated steel is very easy to solder but the sharp edges are dangerous to careless fingers. Blank copper clad printed circuit board could be used instead but does not withstand heat as well as the plain metal sheet.

The arrangement of tin cans may not seem very glamorous but it is highly effective and very cheap — the whole cost of the project comprises about \$7 for the resistors and about \$2 for the terminal posts. The tin can housing can be spray-painted and the terminal posts labelled and marked to suit individual needs.

Before starting choose a medium sized coffee tin with a resealable lid for the case and select a tin can of smaller diameter which will fit easily into the coffee tin. About eight or nine centimetres in diameter should be fine for the smaller tin can. Using a can opener remove the top and bottom of the smaller can and discard the contents (maybe you should eat the contents — but that's really outside the scope of ETI!). Also, discard the remaining cylindrical portion of the can! Mark up one of the tin-plated discs so obtained

with a grid of ten by ten lines as shown in Figure 1 to give 100 intersections.

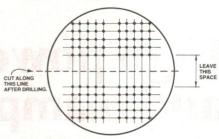


Figure 1. Drilling and cutting details for the tinplated discs obtained from a small can.

Allow a space of about 10 mm along one diameter as shown. This will allow the discs to be cut in half later. Clamp both discs together onto a drill bench or a block of wood, ensuring that they are exactly superimposed. Drill a hole on each intersection of the previously marked lines. Make the holes slightly larger than twice the diameter of the resistor leads; this will assist assembly later on. Take care that your hands are kept clear during drilling since if the drill bit grabs, the two tin discs will whirl around very much like a meat slicer, and almost as sharp! Only 98 holes are needed so don't get carried

When the holes are drilled, cut the two discs along the middle space left along one diameter so that you end up with four half discs each with 49 holes. Tin the area around each hole with solder and proceed with assembly.

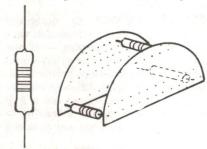


Figure 2. Cut the resistor leads as shown at left and then solder three resistors to two half-discs as shown to make a rigid assembly.

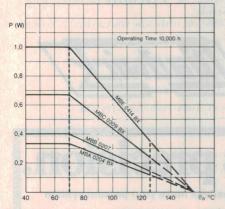
Trim all the resistor leads as shown in Figure 2 so that one lead is longer than the other on each component. Take two matching half-discs and using three resistors assemble a rigid structure as shown in Figure 2. Insert the resistors, one row at a time, in between the two tin plates with the leads poking through the holes. If you insert the longer lead of each resistor into its hole first, the other end should be short enough to allow manoeuvring into the hole in the second plate. After one row of resistors is in place solder all the leads of that row on

audio dummy loads

both plates, then proceed with the next row.

Repeat the assembly for the second half of the unit then trim all excess leads flush with the surface of the tin plate. Using a stiff brush (e.g. an old toothbrush) scrub the soldered surface with methylated spirits to remove deposits of flux.

Connect an ohmmeter between the plates of each load — the reading, believe it or not, should be pretty close to 8 ohms. Inspect all solder joints and resolder if the reading is not correct. Install the four terminals in the lid of the coffee tin using one red and one black terminal for each half of the unit. Lay the two assembled resistor pads side by side as shown in Figure 3. Using fairly stiff copper wire connect the upper plates to one terminal each. Use



The upper curve in this graph shows the typical dissipation characteristics of the Beyschlag resistors used in the ETI-155a dummy load. Power dissipation is derated at operating temperatures above 70°C.

the same colour terminal for both plates as this will be important later if the loads are to be connected in series or parallel. Using the same sort of wire, but insulated, connect the lower sides of the resistor assemblies to the other two terminals. You should finish up with an assembly which will be supported under the lid of the coffee tin and which is so positioned as to allow it to be inserted into the container and for the lid to be

sealed.

To prevent the two halves of the load from shorting together, install an insulating spacer between them using a scrap piece of copper-clad board or matrix board. If using the pcb material, ensure that enough copper is removed to insulate the two halves from each other. If using the matrix board, you will have to drill a couple of additional holes and use small screws to attach the spacer to

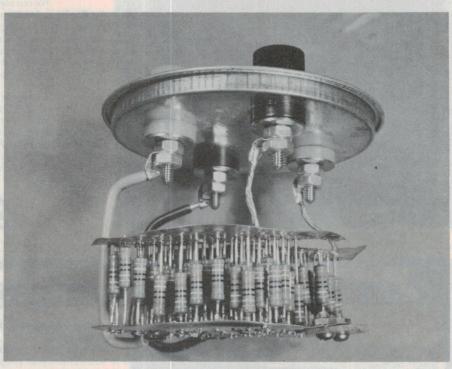
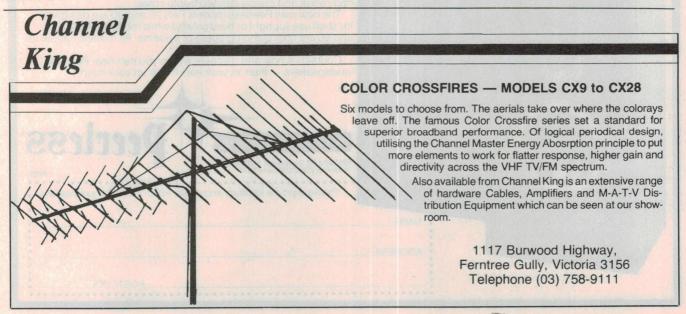


Figure 3. The two half-disc pairs are assembled side-by-side and heavy gauge wire soldered between the discs and the terminals. An insulating spacer of matrix or pc board holds the two assemblies apart.



HOW MANY TIMES



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audio dummy loads

the resistor assemblies.

Before inserting the assembled unit into the coffee container, mark the lid to indicate which terminals are connected by the resistive pads.

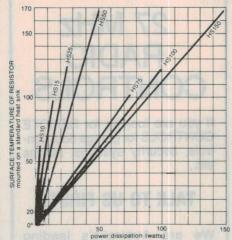
To test the unit, connect each load across a known working amplifier or if this is not convenient, use a car battery (not more than 12 V) as the driving source. If using an amplifier, connect an ac voltmeter across the load under test. If you can use a sinewave generator to drive the amplifier, all the better. Adjust the amplifier volume control to give about 10 to 15 volts across the loads. Check by feeling the resistors with your hand that they are in fact warming up. Increase the output of the amplifier until the voltage across the loads is about 20 volts. This should result in the resistors getting quite hot after a couple of minutes.

If using a car battery, connect the two loads in parallel and connect the battery across them. Check the current drawn; it should be approximately 3 A with a 12 volt battery.

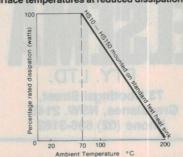
When testing is satisfactorily completed, install the whole assembly into the coffee container and press down the lid. If you plan to use the loads continuously, fill the container with insulating oil before assembling.

Metal-clad resistor method (ETI-155b)

This has to be just about the world's quickest project! Two 50 W, 16 ohm Arcol resistors connected in parallel were used as a single 8 ohm, 10 W resistor is more expensive. The Arcol resistors have two diametrically



Temperature-dissipation characteristics of the Arcol metal-clad resistors. The curve marked 'HS50' applies to the types specified for the ETI-155b dummy load. (Temperatures shown are surface temperatures at reduced dissipation).

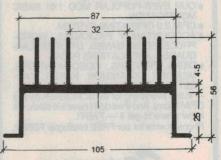


Power derating graph for the Arcol resistors.

opposed mounting tags drilled to take 6 BA bolts. We mounted them on a short (71 mm) length of heatsink obtained from Autotron Australia, of P.O. Box 202, Glen Waverley 3150 Victoria. We understand a number of component suppliers keep stocks of this product.

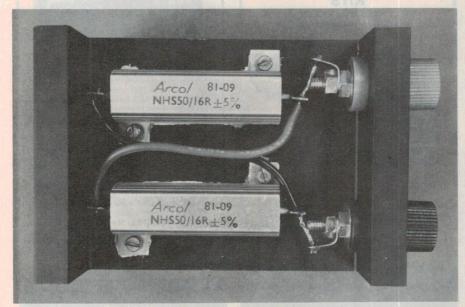
We chose it because its shape is very convenient for this application but almost any suitable heatsink on which the Arcol resistors can be comfortably mounted will suit.

The two resistors are mounted in the 'well' beneath the fins, positioned such that the securing bolt holes do not foul any of the fins. The photograph makes this clear. Two large terminal posts are mounted on one side to provide convenient connections and the resistors are wired in parallel in the manner shown in the photograph.



Cross-sectional profile of the Autotron type XA heatsink used for the ETI-155b dummy load. This style of heatsink is obtainable in a variety of lengths.

With the heatsink used, at a dissipation of 100 watts, the heatsink temperature rapidly rises and will reach 150°C after some minutes! Fan cooling keeps it within bounds, but if you expect to use the dummy load for lengthy periods then a larger section of the Autotron heatsink or whatever you wish to use is recommended. To keep the resistors at 70°C or below, (their maximum temperature at full dissipation before derating) we suggest either a single 500 mm length (standard size, natural finish) or two 200 mm lengths (standard size, black anodised).



The ETI-155b assembled — the world's quickest project!

-PARTS LIST — ETI 155 —

ETI-155a

98 x 390R, 1 W, 1% or 5% carbon resistors. 4 x large binding posts, two black, two red. Tin cans to suit — see text; high current wire (see text and pics).

ETI-155b

2 x Arcol 16R, 50 W, 5% non-inductive resistors, type NHS50.

2 x large binding posts, one black, one red. Heatsink (see text); high current wire (see text and pics).

Price estimate ETI-155a

ETI-155a

\$7 - \$9 \$18 - \$22

Note that these are **estimated** prices only and **not** recommended prices. A variety of factors may affect the price — cost price movements, whether you use 1% or 5% resistors, type of heatsink employed, etc, etc.

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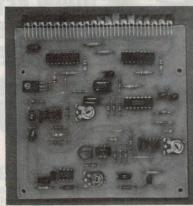
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Lab Notes

Safety with CMOS

Certain elementary safety precautions must be taken when handling CMOS ICs or designing CMOS circuits. Ray Marston explains all in this month's Lab Notes.

EARLY CMOS ICs earned a reputation for being easily damaged by static electricity, either when being handled or when being soldered into circuit boards, etc. Subsequently, manufacturers tried to overcome this 'fragility' problem by providing the ICs with extensive built-in input and output protection on each gate in each package. These protection networks do a fairly satisfactory job, but provide the designer with a few extra problems when employing CMOS circuits.

CMOS protection networks

CMOS ICs are, by definition, metaloxide semiconductor devices, in which the input signal is applied to the nearinfinite impedance (about 10¹² ohms) of the metal-oxide gate. Typically, the gate oxide has a breakdown voltage of about 80 V; if a gate oxide breakdown does occur, the resultant damage to the device is catastrophic and irreversible. To protect the CMOS against excessive input voltages (particularly arising from static energy), all modern CMOS

ICs are provided with extensive built-in protection on all inputs and outputs.

Figure 1 shows the standard protection network that is used on the vast majority of B-series CMOS devices. Here, all diodes marked as 'D1' are used to prevent the input or output from swinging more than 600 mV below the Vss (0 V) rail, and all diodes marked as 'D2' are used to prevent the input or output from swinging more than 600 mV above the VDD (supply positive) rail. D3 is intended to prevent the VDD terminal from swinging negative to the Vss pin (electrostatically) when the device is being handled.

There are a couple of minor exceptions to the standard version of the protection network. One of these is the type used on the 4049B and 4050B series of hex buffer/converters which, as shown in Figure 2, have their inputs free to swing well above the V_{DD} rail. These particular ICs are specifically intended for use in logic-level conversion applications, in which (for example) the input may come from a 12 V CMOS net-

work but the output and the IC supply rail are matched to a 5 V TTL network.

Another exception is the 4066B type of transmission gate or bilateral switch, and its equivalents. These devices comprise a bilateral electronic switch and a switch-control network. In these circuits, all switch-control networks have the type of input protection shown in Figure 1, but the switches themselves have the simple protection network shown in Figure 3.

Note in Figures 1-3 that all diodes marked with asterisks are 'parasitic' devices, which just happen to occur fortuitously as an inherent part of the CMOS manufacturing process, while all other diodes are specifically designed into the circuits. Also note that the networks are intended only to give protection against 'normal' electrostatic discharge voltages. When the networks are subjected to ordinary dc signals, the diodes are liable to burn out if their forward currents exceed 10 mA or so, thereby causing possible catastrophic damage to the IC substrate.

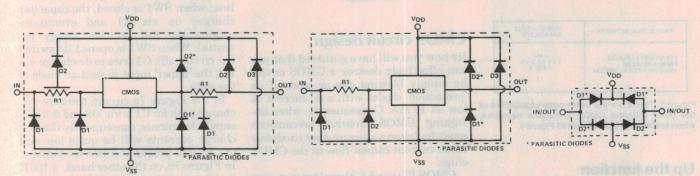


Figure 1. These are the standard electrostatic discharge protection networks used on most B-series CMOS ICs. The two diodes associated with the resistors are distributed across the entire resistance, as shown.

Figure 2. This protection network is used on the 4049B and 4050B hex buffers. Note that the input is free to swing above the positive supply (V_{DD}) rail.

Figure 3. The 4066B quad bilateral switch has standard B-series protection on its gate control input terminals, but has this simplified form of protection on its 'switch' elements.

Lab Notes

Major CMOS manufacturers such as RCA reckon that an electrostatically charged human body can be approximated by the circuit of Figure 4, in which the 'body' has an effective capacitance of 100 pF and a source resistance of 560R. The manufacturers have carried out extensive tests with this model by charging the 'body' to various

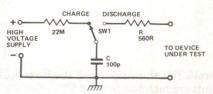


Figure 4. Manufacturers use this equivalent-body discharge network when evaluating the capabilities of their CMOS protection networks.

voltages and then discharging it (via the 560R series resistor) into different terminal combinations (input, output, Vss, VDD) of CMOS devices to establish worst-case capability figures for the three types of electrostatic-discharge protection networks. It should be noted in these tests that the 560R series resistor acts as a current-limiting voltage dropper, so the voltage actually reaching the CMOS device is far lower than the initial electrostatic voltage.

The results of the manufacturer's protection capability tests are shown in Figure 5. As you can see, the standard protection network can withstand a 4 kV electrostatic discharge. A quick calculation shows, however, that this represents a peak protection-diode current of several amps, yet we've already seen that these diodes can withstand dc currents of only 10 mA or so. Puzzled?

PROTECTION NETWORK	WORST - CASE CAPABILITY
STANDARD B-SERIES	4 kV
4049B AND 4050B	1 kV TO 2 kV
4066B BILATERAL SWITCH	< 800 V

Figure 5. These are the worst-case capabilities of the three different CMOS protection networks, when tested with the network of Figure 4.

Up the junction

Just about the only way of destroying a diode is to literally vaporise its junction, and this can only be done by applying an

adequate amount of power for sufficient time for the melting process to take place. Since a junction must inevitably be formed on a substrate, which has a finite mass, all junctions inevitably have a certain amount of thermal inertia and are, in fact, destroyed by energy overloads (power-time product), rather than by simple power overloads.

Consequently, it is quite normal to find that a diode rated at 1 A (for example) can, in fact, withstand brief current surges up to several hundred amps. Similarly, CMOS protection diodes, which have very low dc current ratings (10 mA), can withstand very high levels of surge current (several amps), provided that the surge current

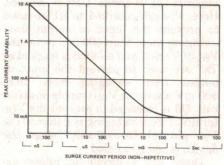


Figure 6. Typical surge-current capabilities of CMOS protection diodes.

duration is very brief. Figure 6 shows the typical surge current capabilities of these protection diodes. Remembering that the 100p — 560 R 'human body' equivalent circuit has a time constant of a mere 56 nS, it no longer comes as a surprise to note that these diodes can withstand several amps of peak current from a 4 kV discharge!

CMOS circuit design

By now you will have gathered that you can effectively destroy a CMOS device by simply blowing one or more of its 'protection' diodes with a dc current as low as 10 mA. Consequently, when designing CMOS circuits, precautions must be taken to ensure that excessive diode current cannot flow in the CMOS chips.

CMOS ICs can be 'blown' by excessive signals applied to either the input or the output terminals. If several CMOS stages are cascaded, empirical experience shows that a front-end 'blow' will usually destroy only a single device (because low energy levels are normally involved), but a rear-end (output) 'blow' will often have a ripple effect (because high energy levels are involved) and cause the destruction of all ICs in the chain.

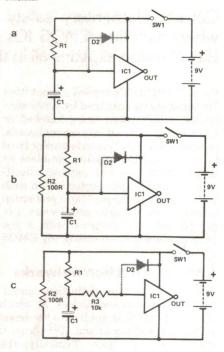


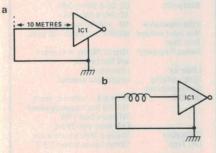
Figure 7. Circuits (a) and (c) are safe, but circuit (b) will almost certainly cause front-end 'blow'. See text for explanation.

The most common cause of front-end 'blow', and its cure, are illustrated in Figure 7. Here, a capacitor is connected directly between the IC gate and the 0 V line; when SW1 is closed, the capacitor charges up via R1 and eventually attains the full positive supply potential. When SW1 is opened (to switch the circuit off), C1 tries to discharge via D2, the 'upper' input protection diode of the gate.

In the Figure 7a circuit, the only discharge path for C1 is via D2 and the IC's supply terminals; consequently the discharge currents will be quite low and the IC will probably suffer no damage. In Figure 7b, on the other hand, a 100 R resistor is connected across the supply terminals, so C1 will try to discharge to ground via D2 and R2, and the resulting 90 mA peak current will almost certain-

ly result in the destruction of the chip. In practice, R2 may well take the form of various resistors and semiconductor devices distributed throughout the total circuit.

Figure 7c shows the cure for the Figure 7b design problem, a 10k resistor wired in series with the gate to limit the C1 discharge currents to a safe value. Whenever you design CMOS circuits and have to connect a capacitor between a gate and the 0 V rail, always make sure that the capacitor discharge current is limited to a safe value, either by a series gate resistor or by some other factor.



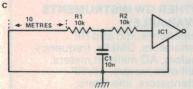


Figure 8. Long input cables, as in (a), can be equivalent to an inductor (b), and present another front-end blowing hazard. The cure is simple (c).

Figure 8 illustrates another possible cause of front-end 'blowing', and its cure. In Figure 8a, it seems that the IC's input is safely grounded by the 10 m of input cable (in practice, this cable may go to a low impedance sensor, etc), but in actual fact (Figure 8b) this cable will inevitably be inductive and can easily pick up unwanted radiation and possibly feed destructive signals to the IC input. Figure 8c shows that the circuit can be rendered safe with a simple filter (R1-C1) and a series gate resistor (R2).

Back-end blowing

The most common cause of back-end blowing is unexpected back-EMFs (from inductive loads) reaching the CMOS output by breaking through

from power-driving circuitry.

Inductive loads, such as relays, can generate surprisingly large back EMFs as their fields collapse at switch-off, as can be proved by connecting a relay inthe 'buzzer' mode shown in Figure 9. Typically, a 12 V relay will generate a back-EMF of about 300 V! If you ever use CMOS to switch a relay or other highly inductive load using a transistor driver, always protect the transistor with a pair of 1N4001 diodes connected as shown in Figure 10a. If you want to be really safe, you can use another pair of similarly connected diodes to directly protect the output of the CMOS stage, as shown in Figure 10b.

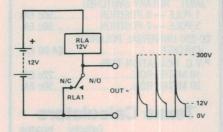


Figure 9. This 'buzzer' circuit can be used to check the magnitude of the back-EMF from a relay. 300 V is typical!

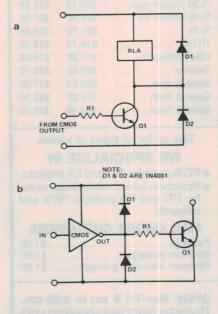


Figure 10(a). A transistor relay-driver can be protected with a pair of diodes. (b) The output of a CMOS stage can be given added protection with a similar arrangement.



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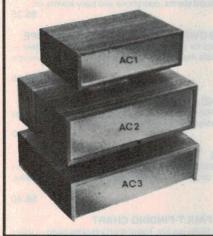
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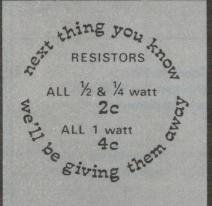
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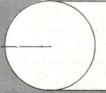
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EG-510ED-2B	12.0	8.4 - 15.0	2400	69	CCW

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Ideas for Experimenters

These pages are intended primarily as a source of ideas. As far as reasonably possible all material has been checked for feasibility, component availability etc, but the circuits have not necessarily been built and tested in our laboratory. Because of the nature of the information in this section we cannot enter into any correspondence about any of the circuits, nor can we produce constructional details.

'Broadcast Booster' for AM band DXers

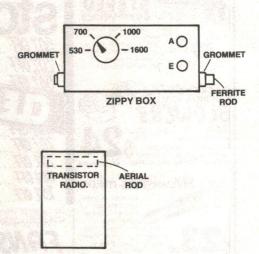
Here is an inexpensive method of providing some receiver front end selectivity and signal boosting for transistor AM receivers, an idea passed on by Paul Spresser of Ipswich, Queensland.

The unit is comprised of a ferrite rod (like those generally used for transistor radio antenna coils) with a coilcum-antenna wound on it, tuned by a dual-gang 'broadcast' capacitor. The ferrite rod and coil acts as an inductive 'link' to the transistor radio's loopstick antenna. Construction is fairly noncritical. Using a ferrite rod around 150 mm or so long by 9.5 mm diameter, wind 50 to 60 turns of 20 gauge insulated wire (exact gauge is unimportant, but nothing thinner than, say, 26 gauge) onto the ferrite rod — not too near one end. Secure the coil with insulating tape. This can be mounted in a plastic zippy box as shown in the drawings, along with the dual-gang capacitor. This capacitor should have a maximum capacitance of around 400 pF.

For best reception, connect up a good 'ground' and a long wire antenna, at least six metres long. Place the tran-

FERRITE ROD

TRANSISTOR RADIOS AERIAL ROD.



sistor broadcast radio with its loopstick antenna aligned parallel to the ferrite rod and reasonably close to it (about 30 mm), tune in a station and tune the dual-gang capacitor for best reception. Further improvement can be made by increasing or decreasing the coupling between the booster and the transistor radio.

If the station tuning on the booster is crowded toward the minimum capacitance end of the dial, take a few turns off the ferrite rod coil. If the station tuning is crowded toward the other end, add a few turns.

This booster improves front end selectivity and image rejection quite noticeably. Adjacent stations can generally be adequately separated, although both will be heard.

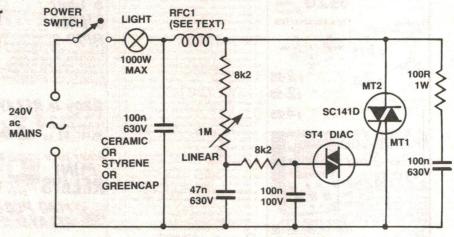
If you mount the booster in a zippy box having a metal lid, connect the lid to the circuit earth. Banana sockets make good aerial and earth connectors.

Simplest triac light dimmer

Triac control circuits are legion in the electronics literature, but try as he might, B.J. Pfeiffer of Lower Mitcham in South Australia could not find a really simple triac light dimmer.

The circuit here has about the minimum possible component count and can handle a lamp load up to 1 kW with a variation from full off to full on.

The circuit is straightforward, the only 'problem' component being RFC1, an item which is not generally commercially available. However, a suitable RF choke is easily made. Mr Pfeiffer suggests winding a layer or two of plastic insulation tape on a 50 mm length of 9.5 mm diameter ferrite rod. Close-wind a layer of 22 B&S enamelled copper wire over the insulation tape, then wind tape tightly over the rod in a



couple of layers. Bind the winding ends firmly.

Construction is non-critical except for taking the usual precautions with

mains-type assembly. Watch the ratings of the capacitors and see that the power switch is properly rated (5 A, at least).

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7818	90c	80c
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4.33619 4.440 4.550	HC18 HC18 HC18	\$6.90 \$6.90 \$6.90

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3.2768	HC18	\$7.9
3.2768	HC33	\$7.9
3.330	HC33	\$7.9
3.330	HC18	\$7.9
3.579545	HC33	\$7.9
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3.6000	HC18	\$4.9
4.000	HC33	\$4.9
4.000	HC18	\$4.9
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562	.75	495	1.60	1018	3.18	1685	.75	34 E	1.25	LA 3301	6.95	7222	6.25
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Ideas for Experimenters

Meter for the ETI-560 Mains Cable Seeker

Adding a meter to provide a visual indication to your Mains Cable Seeker (May 1980 issue) is a simple job, according to K. Howell of Renmark, S.A.

The meter is connected between the junction of diodes D1 and D2 (meter negative) and the junction of resistors R3 and R4 (meter positive). Meter sensitivity is relatively unimportant, anything from, say, 100 microamps to 1 mA would suffice.

With both a visual indication and an audible indication, you can't help finding that hidden mains wiring!

Any ideas?

Have you had a bright idea lately, or discovered an interesting circuit modification? We are always looking for items for these pages so naturally, we'd like to hear from you.

We pay between \$5 and \$10 per item — depending on how much work we have to do on it before we publish it.

The sort of items we are seeking, and the ones which other readers would like to see, are novel applications of existing devices, new ways of tackling old problems, hints and tips.

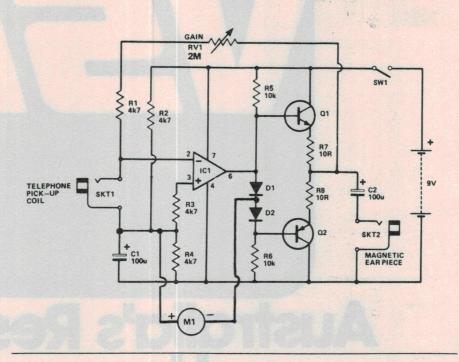
ETI-640 VDU mod. cures "interlace jitter"

J.J. Wilson of Modbury in South Australia writes that the following small modification to the very popular ETI-640 S100 VDU cured a case of cyclic jitter of one TV sweep line — which he dubbed 'interlace jitter' — on his VDU.

The cure is very simple:

- 1) Remove the diode from pin 2 of IC38.
- 2) Add a 6k8/½W 5% resistor between pin 4 of IC38 and the wiper of RV1.

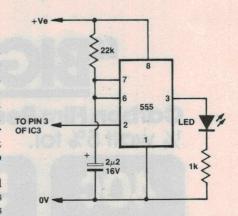
This should result in a display that is perfectly stable.



Visual beat for ETI-604 metronome

Mr A. Partridge of Launceston, Tasmania, constructed our Accentuated Beat Metronome (ETI-604, September '77) and found it very useful. However, he felt the lack of visual indication of the beat was a drawback. Connecting a LED and limiting resistor from pin 3 of IC3 and 0 V in the project was unsuccessful as the flashes were too brief to be of use.

The solution he found was to add another 555, wired as a monostable as shown in the circuit here. This 'stretches' the beat pulse and has the additional advantage that, with the 555's 200 mA sourcing capability, it can drive a small incandescent lamp direct-



ly, although a LED is shown here. Any suitable LED may be used, such as the common TIL220 — either red, green or yellow, to suit your taste.

Quick & easy front panels

Laminex (or similar material) offcuts are readily available at hardware and builders' suppliers. Front panels of this laminated plastic material are attractive and inexpensive.

It may be cut to size with tin snips or hacksaw, drilled, scribed, filed, etc. As it is so easily worked and the end result is cheap and useful, try it on your next experimental project.

An excellent idea — from J.L. Elkhorne of Chigwell, Tasmania.

FRRATA-

A rather obvious, but potentially dangerous error occurred in the circuit on the top left of page 60 ('Power Monitor') in the March issue. It shows the mains active input connected to the earth at the output. The mains active input should instead go to the fuse. Correct your copy now. Correction slips were inserted in the majority of copies distributed.



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Dear Sir,

I recently purchased Permostat due to the high recommendation in your magazine. Like yourselves, I do not like liquid cleaners, but after the terrific write-up and test I decided to give it a try. (ETI, Jan 1981). I have a pretty good stereo system valued at \$11 000 of which great care is taken. It is only used at home.

I purchased the 1812 Overture 'test' record; due to the many frequencies in the one record it cost me \$21. I played it twice and then used Permostat on it. The amount of rubbish the needle picked up from the record was tremendous, in fact I consider it too much for such a new record, especially as the needle had just been replaced at a cost of \$300. Before a single track from one side was finished I had to clean the rubbish from the needle—a considerable amount.

I now have a problem, as a residue has remained on the needle which is impossible to remove. Also the record concerned has lost some of its dynamic sound and appears to be slightly muffled in sound.

Permostat was used strictly to instructions and we feel it has either damaged our needle and/or our record as there are not the same precise, sharp notes as before.

The only thing I can think of is that the humidity, which is high here, has somehow affected this product. We purchased a kit plus refill as we have over 300 records and were going to eventually get them all done, but after the 1812 Overture and its consequences we will not be doing them.

Could you please advise as to what can be done to combat our now murky sound and dirty needle. I have a Denon turntable with moving coil and a DL303 cartridge. I am also wondering if there is any way possible that our needle could be damaged as it's very expensive and only two weeks old and should last us two years.

Hoping you can offer a helpful reply as we are very dissatisfied with this product. If not could you please let us know who we can contact to get the information we need.

Greig Tucker Wulagi N.T.

We thought that the best way to answer your letter, Mr Tucker, was to show it to the Australian importer of Permostat,

Derek Pugh of Concept Audio. Fortuitously, Derek called into our office,
bringing with him David Milty of Milty
Products, the UK manufacturer of
Permostat, who happened to be visiting
Australia at the time. Both gentlemen
read your letter and discussed with us
the problems you experienced and a
solution. Here is their reply.

(Roger Harrison)

We are sorry to read of the problems you have encountered with Permostat but can assure you that absolutely no damage has been done to your stylus, or indeed to your record.

To fully understand what has occurred, we must go into some depth when looking at a PVC recording. Firstly, it is incorrect to assume that a record straight out of the packet is indeed clean. This is not the case as factory dust and debris is often in the groove to a considerable degree — held there of course by a strong static discharge.

It is important therefore when taking a brand new record from its sleeve that it be thoroughly cleaned in an effort to remove as much of the debris as possible. There are many reputable cleaners on the market that will do this, but most are of the brush or pad variety that find it very hard to dig the dust out of the groove in defiance of the static charge.

By far the most effective cleaning device in this respect is the Pixall Roller, a new version of which is due on the market very shortly. This comprises a specially formulated adhesive tape that is rolled across the record. The elasticity in the adhesive surface allows deep penetration into the grooves and removal of not only surface dust but dust in the groove as well.

Having therefore attempted to clean the record as best you can, Permostat should be applied according to the instructions. It is common practise to put too much Permostat on a record. This should not be done. Also, buffing should take place for at least 30-40 seconds on both sides of the record of course and the buffing should be very, very firm.

The record that you have mentioned in your letter would, I am afraid, have been very, very dirty from the onset and the dust and debris in the groove has been rising up your stylus and collecting around the shank. In time of course your stylus will

thoroughly clean the record but you do have the slight inconvenience in the meantime of the build-up on your stylus. This, we must emphasise, is a dry build up and can be easily removed with a reputable stylus cleaner.

As I am sure you can see, there is a very logical explanation as to what has been happening and again we hasten to assure you that your stylus in no way has been damaged and your record in no

way impaired.

Regarding your specific record of the "1812 Overture", it may be best to seek out a Keith Monks Record Cleaning Machine and use it to thoroughly clean your record before once again applying the Permostat. Permostat is soluble in water so removal would be no problem. Certainly a thorough cleaning of this nature of your record, together with a thorough cleaning of your stylus and the correct re-application of Permostat should ensure that you will not encounter similar problems again.

We would appreciate greatly hearing from you when our recommendations have been carried out and we do hope that as a result your confidence in

Permostat will grow.

Derek Pugh Concept Audio Pty Ltd

Dear Sir.

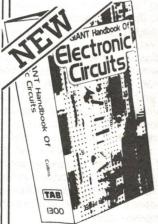
It is not often I get to see your wonderful magazine as I understand you are not permitted to sell it in Britain and I only get the occasional copy when Zel, mummy's Aust. rep., remembers to put in a copy with the bundles of AWWs, Aust. Polo News etc.

I considered myself lucky to be able to pick up a copy of your Tenth Birthday issue (congratulations, chaps) when I visited Australia just recently and was fascinated by the array of interesting articles. I found the report on Negative Ion Generators and the report on Voyager I's encounter with Saturn particularly fascinating. Being a music and audio enthusiast from way back (having your own band instils a certain musicality into one's outlook) I was particularly interested in your Sound Reviews especially the Marantz Tt1000 turntable. While I appreciate Mr Challis' gracious suggestion that your country send one as a wedding present, I think something peculiarly Australian would be more appropriate. Nonetheless I think we shall obtain one to go with the Linn-Sondek, as it's always handy to have a spare, don't

I look forward to seeing more of your splendid publication over the next decade.

Charles

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Shoparound

THIS PAGE is to assist readers in the continual search for components, kits and printed circuit boards for ETI projects. If you are looking for a particular component or project — check with our advertisers if it is not mentioned here.

ETI-156 instrument probe

This project hinges around the highspeed hybrid buffer from National Semiconductor, the LH0033CG. It's not cheap, but way ahead of whatever's in second place! This IC will set you back around \$30 odd, we understand, and Radio Despatch Service in Sydney and Radio Parts in Melbourne have indicated they are stocking this item. In addition, the following firms have indicated they have the device on order and may well have stocks by the time this issue goes on sale: Magraths, Rod Irving Electronics and Polykits (317 Swanston St, Melbourne). We understand the LH0033 may also be available through Dick Smith stores — but phone first, or try their mail order service.

The Jabel type PH3T plastic probe housing we obtained from Radio Despatch Service. Jabel products are distributed by Watkin Wynne, 32 Falcon St, Crows Nest NSW 2065. (02)43-2107.

If you elect to use chip ceramic capacitors for bypassing, they may be obtained quite inexpensively from Vitramon, 289 Bay St, Brighton-le-Sands NSW 2216. (02)599-2325.

Constructors should have little difficulty in obtaining the other components, although it may require a little shopping around to locate axial lead solid tantalum capacitors for C1 and C3.

ETI-458 LED level meter

The LM3915 dB LED display drivers have only recently become readily available here and we don't have complete information on who will have stocks when this issue goes on sale. However, Sydney readers might try Jaycar, Electronic Agencies and Radio Despatch Service. Melbourne readers should try All Electronic Components, Ellistronics, Polykits, Magraths, Rod Irving Electronics and Tasman Electronics. Dick Smith Electronics may be stocking this item but, as this was not confirmed as we went to press, phone first or enquire from their mail order department.

As for the LEDs, any rectangular LEDs that will mount together with a 2.5 mm spacing will suit this project. Check that first.

The CMOS switch IC, the CD4066, was chosen for its low on-resistance and should not be substituted. You may have to shop around for this one but suppliers who keep a wide range of IC types generally stock the 4066. The CA3130 should not be a scarce item, likewise the 301 op-amp.

One per cent tolerance resistors are, thankfully, relatively common these days. Note that 5% resistors may be used in lieu of 1% types, with some degradation in accuracy.

We understand this project will be widely stocked as a project.

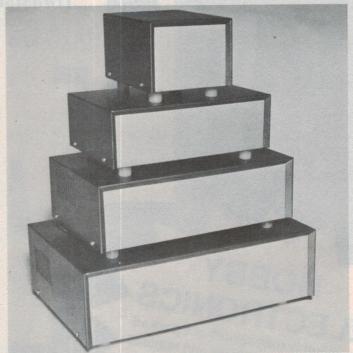
ETI-155 dummy loads

For the ETI-155a dummy load, 1% resistors and a chass sistors should not be difficult to obtain closure is not required.

as many suppliers stock the standard E12 range these days. However, Andrew Kay purchased the Beyschlag types from Crusader Electronic Components, 81 Princes Highway, St Peters NSW. Note that 1 W rated resistors are required.

The Arcol NHS50 resistors used in the ETI-155b dummy load are distributed in Australia by Everest Electronics, 61 Compass Drive, Seaford S.A. 5169. The NHS50s will set you back around \$7 each.

The Autotron type XA heatsink extrusion we used in the ETI-155b is manufactured and distributed by Autotron Australia, P.O. Box 202, Glen Waverley Vic. 3150. We understand a number of component suppliers are stocking this item. We used the XA-007, which is 71 mm long and only useful for short-term operation of the dummy load. A 500 mm length, type XA-05, would be better for longer term usage. Any heatsink having a thermal rating around 1°C per watt or better should suffice. We chose the Autotron type as the 'well' incorporated provides a convenient, protected mounting for the resistors and a chassis or other en-

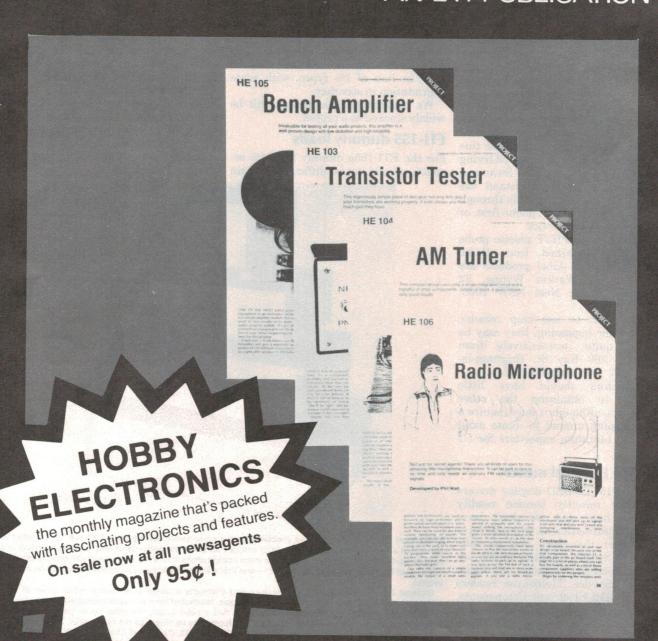


Instrument cases always present problems to the constructor. Rarely does he have the facilities to construct a 'professional-looking' case or cabinet, let alone the patience or skill! Putting a project in a case is part of the 'tidying up' process for most constructors and is therefore generally considered last. Then, cost becomes an important factor — and that's where the biggest compromise comes on many a project.

Ballarat Electronic Supplies manufacture and distribute a range of attractive metal cases at very competitive prices. Their 'C' series instrument cases, illustrated here, are constructed with an aluminium 'U' base and steel cover. The aluminium base has a satin finish, the steel cover being finished in hammertone paint, and the front wrap-around is formed so as to recess the front panel. There are six in the range, designated (in size order) C642, C453, C853, C1063, C1066 and C1284. The C642, for example, is 150 mm wide by 55 mm high by 100 mm deep overall and has dimples in the base to serve as feet. The C1284 is 305 mm wide by 105 mm high by 200 mm deep. The larger cases are supplied with bolt-on plastic feet and have slots in the rear of the top cover for ventilation. Known as 'K&W' cases, many component suppliers stock them.

Further details from Ballarat Electronic Supplies, 5 Ripon St, Ballarat Vic. 3350. (053)31-1947.

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BUMMUNICATIONS

Updated Comni VHF receiver

GFS Electronic Imports of Melbourne recently announced the Comni R01010, an updated version of a synthesised airband AM receiver which can be used in a wide variety of applications including airlines, flying clubs and the Department of Transport.

ration on both 240 Vac and 12 Vdc, coaxial-type spacing from 108-136.975 MHz the entire VHF navigation and display. communication aeronautical band. Highly effective impulse noise re- measures 160 mm wide by 56 mm duction is claimed to be achieved by high by 250 mm deep. It is supplied using multistage active IF noise with all mounting hardware, plus a blanker circuitry, and a sensitivity of detachable telescopic antenna for less than 1 microvolt for 20 dB desk-top operation. signal to noise ratio is achieved by a double conversion superhet receiv- GFS Electronic Imports, ing system.

Easy selection of the 1120 (03)873-3939.

The receiver is designed for ope-channels is achieved by use of a channel and a phase-locked loop frequency switch, with a front-panel-mounted synthesiser gives full frequency LED giving a received signal indicoverage with 25 kHz channel cation. Frequency readout is also provided by a red LED five-digit

The unit weights 3 kg and

For further information contact McKeon Rd, Mitcham Vic. 3132.



Bringing digital storage capabilities to the most widely used frequency ranges in the RF spectrum, Tektronix recently introduced the 7L14 Spectrum Analyser.

The 7L14 will be of special im-coverage from 10 kHz to 1800 MHz. radio)

longer lifetime and lower operating different 7000 Series plug-ins. costs, resulting from the use of a place of the storage CRT.

The 7L14 provides frequency NSW 2113. (02) 888-7066.

portance to operators using the HF, Other features include a built-in VHF and UHF bands, and has been limiter to protect the first mixer; designed to meet the needs of user 70 dB on-screen dynamic range, groups such as broadcast stations spurious free; minus 130 dBm sen-(AM, FM and TV), military com- sitivity, with 30 Hz resolution; CRT munications, CATV companies, and readout of control settings; four-toutility companies (specifically firms one shape factor resolution filters; and agencies that use two-way tracking generator and counter options; and a display mainframe The 7L14 is claimed to have a compatible with more than 25

More information can be obstandard P31 Phosphor CRT in tained from Tektronix Australia Pty Ltd. 80 Waterloo Rd, North Ryde

New FM transmitter from QEI

Rank Electronics recently announced the release of the QEI Model 675T150, a 150 watt FM transmitter.

Claimed to be a 'state-of-the-art' output from 100 to 150 watts with VSWR protection.

programmable phase-locked loop frequency synthesiser, and is stereo generator and 811 SCA conditions. generator.

The 675T150 uses a conservatively rated power transistor in the output stage, and a driver transistor allows an exciter of 10 W output to drive the final stage to 150 W. The unit is convection cooled for

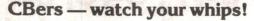
100% solid state transmitter, the maximum reliability, and the power 675T150 features adjustable power supply is current-limited to prevent the output stage from drawing excessive current during severe The transmitter exciter uses a mismatch conditions. In addition, a VSWR protection circuit is incorporated to reduce the input to designed for use with the QEI 722 the final stage under mismatch

> details contact: For further Sydney . - Kevin Sylvester (02) 449-5666; Melbourne -Brooker (03)541-8444; Brisbane -David Power (07)44-0251; Adelaide John Sullivan (08)295-0211; Perth — Pat Cahill (09)443-1811.

> Hume would be well covered. In fact, coverage could extend as far south as Melbourne and beyond Albury to

> The heart of the repeater will be a Philips 828 transceiver and a polar 254 antenna system.

> Since all 2m operators within the repeater's coverage will be greatly benefitted by it, SADARC would welcome inquiries from anyone who would like to take part in this project, as well of course as any financial assistance anyone might be able to offer. Contact SADARC, P.O. Box 692, Shepparton Vic. 3630, or their publicity officer, VK3DFA.



The SEC has renewed its call to CB mobile operators to take care with aerials, following the deaths of two men in Gippsland, Victoria.

improve the range of their radio's reception. The aerial touched high voltage overhead power lines and both men were electrocuted.

SEC Chief Electrical Inspector, Mr Len Francis, says accidents with not uncommon.

"A typical incident occured when an antenna of a mobile radio unit contacted a 22 000 volt line some time ago. An SEC crew investigating the supply interruption found that Peter Strachan on (03)615-2881. although the vehicle had left the

The men died while erecting a CB scene, the antenna was still welded aerial on Mount Tassie in a bid to to the line. That particular operator should consider himself very lucky in avoiding serious injury or death.", Mr Francis said.

The SEC urges all CB enthusiasts to look up and observe the position of power lines before erecting an CB radio antennae mounted on antenna. If the antenna is mounted vehicles contacting power lines are on a vehicle, check travel routes for power lines, as special care must be taken when the combined height of the vehicle and the antenna is more than four metres.

For further information, contact

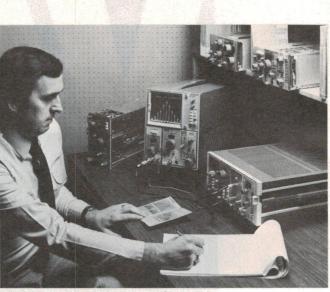
The missing link

Good news for 2m operators!

As many operators may have noticed, there is at present a dead spot for 2m communication along the Hume Highway for a considerable distance north of Seymour.

Plans are at last afoot to fill this gap. The Shepparton and District Radio Club is well advanced in a project that will result in the installation of a repeater, hopefully on Mt. Wombat. Tests carried out from Mt. Wombat have proved that the position has great potential for coverage, and the dead spot on the

82 - June 1981 ETI





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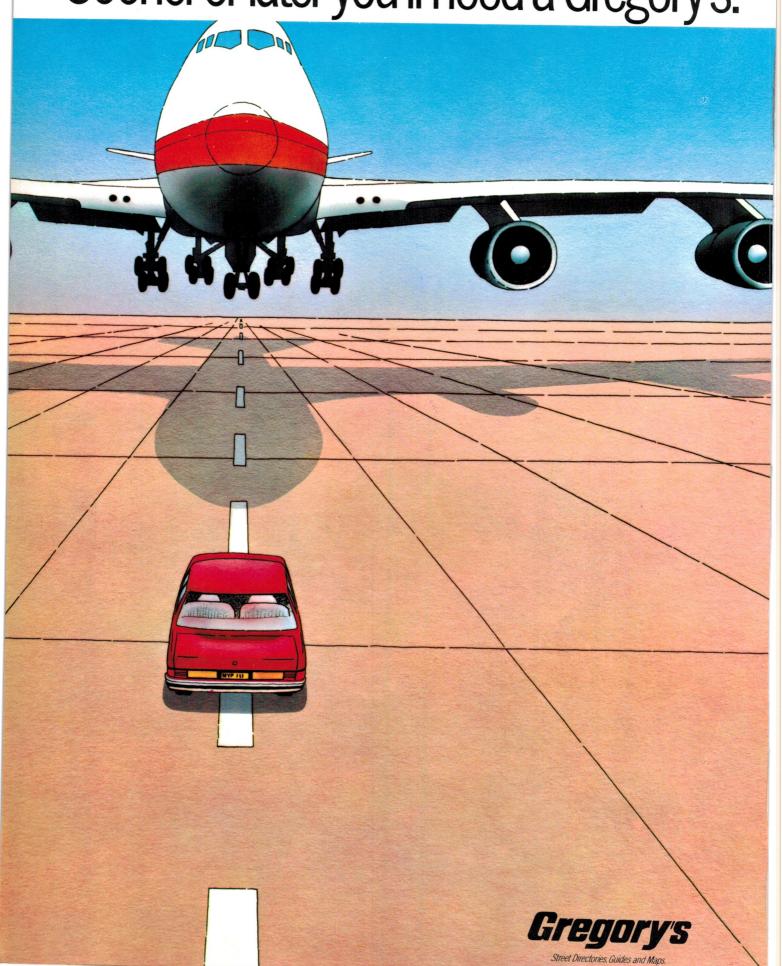
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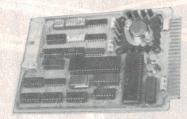
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Your connection to the expansion facilities, up to 32K or RAM, dual disks and the outside world.

Full 53 keyboard with upper case, lower case and graphic elements. Drive it in polled mode and you can detect up to eight keys pressed simultaneously - how's that for real-time games?

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The Commodore PET has become the standard for the Personal Computer Industry.

The Pet is completely integrated, with the processor, memory, keyboard and visual display unit contained within a robust housing, allowing easy transportation with no interconnecting cables necessary. In order to retrieve and save your data and programs, a storage device is used which operates like a cassette recorder, with your information recorded reliably on standard cassettes. The PET has 16k bytes of RAM. Optional equipment permits expansion to 32k. Also, it has 14k bytes of ROM.

The Pet communicates in BASIC the easiest computer language. Easy to learn and easy to use, BASIC has now become the standard for personal computers, with literally thousands of programmes available. The PET is also programmable in machine language, allowing more efficient use of the system.

The full-size keyboard is capable of producing letters, numbers and graphic symbols. Upper and lower case is standard. Characters appear

on the screen in a pleasant green colour designed to reduce eye fatigue and may be displayed in normal or reverse print.

PET's IEEE-488 Bus- just like H.P.'s mini and full size computers permits direct connection to over 200 pieces of compatible equipment such as counters, timers, spec analysers, digital voltmeters spectrum printer plotters from H.P., Philips, Fluke, Textronix and others.

The full range of Commodore Disk Drives and Printers are plug-compatible with the PET and a comprehensive range of cassette and disk based programmes are available through the extensive network of Commodore Dealers.

APPLICATIONS

The Commodore PET is a creature of many faces. Its applications are limited only by the imagination.

The future of the PET is virtually unlimited; its present capabilities are already many and impressive. As a personal computer, the PET can teach languages and mathematics; play games; create graphic designs; store meal recipes and change

number of portions: maintain personal budgets, records and checkbooks; operate appliances and temperature controls.

As a management tool, it delivers the information the executive needs, in the form he can use, and available to him alone. Trend analyses charts and graphs can be almost instantly available.

The professional may use the PET for maintaining appointment schedules, recording schedules, recording income and expenditures and filing all the specialized information and forms he may need to make his work more efficient - from medical records for a doctor to income tax computations for an accountant.

engineer, mathematician, physicist, has a tool far superior to the very best programmable calculators yet developed... at a cost that is comparable...and with almost infini-

tely greater versatility. And the businessn businessman has that computer can maintain payroll records, inventories, keep accounts payable operate receivables, issue cheques and handle correspondence.

Commodore PET 4016 Computer **Technical Specifications.**

Computer/Memory

Read/Write Memory (RAM) 16K bytes available to the user.

Read Only Memory (ROM) 14K bytes in total, divided into:

8K BASIC interpreter available immediately you turn on your PET,

5K Operating System 1K Test Routine

The 6502 micro-processor chip makes the PET one of the fastest and most flexible BASIC systems. Significant features of Commodore BASIC are:

- 960 simple variables
- 960 integers
- 960 string variables 960 multi-dimensional array fields for the above 3 types of variables
- Up to 80 characters per program line with several statements per line
- Upper/Lower case characters and graphics capability
- Built in clock
- 9-digit floating point binary arithmetic
- True random number generator
- Supports multiple languages; machine language accessibility

Keyboard

74-Key professional keyboard Separate calculator/numeric pad. Upper-case alphabetical characters with shift key to give 64 graphics characters

Can be set for lower case and shifted upper case characters.

Screen

40 characters wide by 25 lines (1000 characters in 8×8 dot matrix)

23 cm screen phosphor screen.

Brightness control.

64 ASCII plus 64 graphics characters. Blinking cursor with full cursor control,

including programmable control

Screen editing capabilities Full cursor control (up, down, left, right). Character insert and delete.

Reverse character field.

Overstriking. Return key sends the entire line to the CPU

regardless of cursor position. Input/Output

8 bit parallel input/output port. IEEE-488 Bus (HP-IB and IEC Bus) allows up to 12 other peripherals to be connected.

Two cassette ports. Video signals for additional displays.

Serial output port.

Technical Data

Dimensions:Height 355 mm (14"), Width 419 mm (16.5"), Depth 185 mm (18.5"), Shipping Weight 20.9 kg (46 lbs). Power requirements 240V ± 10%, Frequency

50 Hz, Power 100 Watts.

Commodore BASIC

APPEND	GOSUBRETURN	STOP	SPC
BACKUP	IFTHEN	SYS	LEFT\$
CLOSE	INPUT	VERIFY	RIGHT\$
CLR	INPUT #	WAIT	MID\$
CMD	LET		CHR\$
COLLECT	LIST	SGN	ASC
CONCAT	LOAD	INT	LEN
CONT	NEW	ABS	VAL
COPY	ONGOSUB	SQR	STR\$
DATA	OPEN	SIN	TI
	POKE	cos	TI\$
DEF/FN	PRWT	TAN	ST
DIM	READ	ATN	DS
DIRECTORY	RECORD	LOG	DS\$
DLOAD	REM	EXP	+
DOPEN	RENAME	AND	
DSAVE	RESTORE	OR	*
END	RUN	NOT	1
FOR/NEXT	SAVE	TAB	^
GET	SCRATCH	Pos	π

COMPUTING TOD

Experimental dynamic RAM made with simple polycide technology

IBM researchers have fabricated experimental one-micrometre dynamic RAM circuits with far simpler process technology than has previously been used, while retaining density and performance.

The simplification is based on a materials improvement, with polycide (tungsten silicide on made polycrystalline silicon) replacing polysilicon.

The RAM can be constructed with one layer of polycide rather than the two layers of polysilicon commonly used. By using an unconventional layout for the RAM, the researchers achieved the same circuit density with layers of polysilicon.

Memory cross sections, fabricated by electron-beam direct writing with one-micrometre with ground rules, have cells 34 square micrometres in area, believed to be the smallest ever

Dynamic RAMs have been better with polycide. Because is only one-fourteenth that of for re-entry vehicles. polysilicon the risetime for the polycide wordlines in such cells sistance, they can withstand is only one-tenth that of polysilicon wordlines.

Further, contact resistance for metal-to-polycide is only one-eighteenth that for metalto-silicon.

These improvements are significant for VLSI circuits by electron-beam methods because of the large number of interconnections and contact holes used.

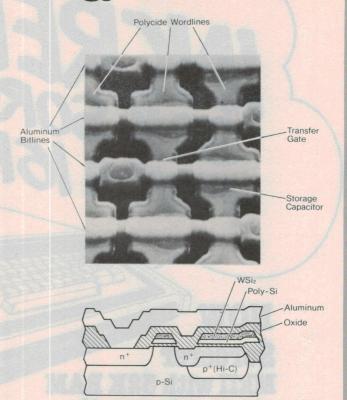
The use of polycide in fieldeffect transistors was first proposed in 1977 by IBM researchers Billy L. Crowder and Stanley Zirinsky, as a better way to deal with the increases in one layer of polycide as with two resistance that occur as the dimensions of devices and circuits are scaled down.

> These increases can be dealt by using multilevel schemes, but making the many contacts and interconnections needed for VLSI structures is itself a problem.

Silicides for electronics applimade with one layer of poly- cations are practical outgrowths silicon, but performance is far of a space-age materials development. They were originally the sheet resistance of polycide intended to be used as cladding

> In addition to having low recorrosive environments and high temperatures, and they can be patterned into fine-line structures.

The work described here was



IBM researchers have fabricated experimental one-micrometre dynamic RAM (random-access memory) circuits with far simpler process technology than has previously been used, while retaining density and performance. The scanning electron micrograph shows part of a memory array fabricated with polycide (tungsten silicide on polycrystalline silicon) wordlines and aluminum bitlines. The drawing shows a cross section of a memory cell.

Robert H. Dennard, Mon Yen Conference, held recently in Tsai, Matthew R. Wordeman, New York City. and Alice Cramer, and was G. Present announced at the 1981 Inter- from 'Research Highlights' performed by Hu H. Chao, national Solid State Circuits by IBM

Computerland to sell Commodore business machines

Computerland and Commodore Australia Pty Ltd have announced that the two companies have come to an

Video Interface Computer, which will sell for less than \$400.

Included in the package are that he was looking forward to Commodore's full hardware the Computerland relationship. and software lines, including He also said, "Our PET, CBM, their soon-to-be-released V.I.C. and V.I.C. computer lines will continue to be available through expand our product offering as puterland Stores between late V.I.C. is an abbreviation for our own network of dealers too. the public's awareness of small March and 30 June. They will

"I believe that the Commodore dealers and Computerland Nigel Shepherd, Managing stores can successfully co-exist public is on the verge of accept- compatible with THE SOURCE Director of Commodore, said since they essentially market in ing computers in a big way, and database.

agreement in which Computerland will sell the American computer company's much-celebrated equipment.

different ways.'

Computerland's Graham agreed, "It has always computers increases.

this line extension is one of our Richard ways to be ready."

These new products will be been our intent to continue to phased into participating Comhave "off the shelf" software "In our view, the Australian including Visicalc, and be

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- 2 cassette control ports both with motor control!
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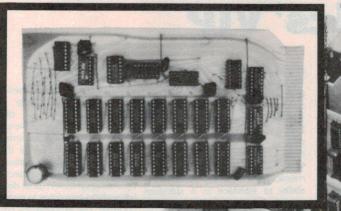
*See page 140 for this exciting system.

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 - (Does not include EPROMS)

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Printout

Vector Graphics' VIP

From small beginnings the Vector Graphics organisation has become a sizeable force in the microcomputer industry — all brought about by the drive and administration of Lore Harp, one of the few women in the microcomputer business.

Lore arrived in the US in the early days of the micro industry. would have been shipped to a Coming from a small town near customer — that is, assembled and Dusseldorf in Germany, Lore has become one of the great and operating system software. American success stories, building Vector Graphics from a small backyard company producing computer boards up to a multimillion dollar corporation.

The show really got on the road when Lore realised the potential of marketing a memory board that her husband, Dr. Bob Harp, had designed on the side while working for a PhD in electronics at Stanford 1976 Lore had joined forces with a friend and formed a company to market boards to the growing number of hobbyists, all wanting to get into the micro world. The Vector '8K Baby' was born, and Vector was national company we know today.

Vector has always stuck to very rigid policies; these include comprehensive quality control, shipping on time, production of documented products, good the telephone.

The Australian agents for Vector Graphics are Dicker Data Projects at in most Australian states. It comes as no surprise to see a woman, Company's image and traditions.

ETI was most impressed when be put through its paces.

The Vector VIP exposed

for stand-alone use in a number of detachable and plug compatible. applications. Hardware features random access memory; (b) six slot, capacity of 315K bytes. fully shielded and terminated S100 motherboard; (c) 4 MHz single Peripherals board computer; (d) 64K dynamic Printer systems — The VIP series RAM on board.

It arrived on our desk exactly as it fully tested, including both hardware

The VIP is a compact single unit housing the VDU and keyboard. with an attached single-sided 'Unistor' mini-floppy disk drive unit. The system was obviously designed to sit neatly on a desk, and took up little space. This is a big plus when aiming at the small business market where a system has to fit in as best it can. The VIP is well designed and housed in a rugged metal box that University in California. By August obviously spelt business. It could be used sitting on a factory floor controlling factory records or in a swish advertising agency keeping track of expense accounts and space bookings.

The video console housed a on the way to becoming the multi-non-detachable typewriter style keyboard and a separate 10-key numeric pad. The keyboard was well graded and very sensitive to touch, a feature great for the typists of the world but for those who rely on two fingers and lots of luck this manuals, and trouble-shooting over takes some getting used to. The screen displayed the standard 24 lines x 80 characters in an ASCII 8 x 10 dot matrix format. The green Carringbah in Sydney, with agents phosphor screen is very easy to see during both day and night use and there was none of the annoying Fiona Dicker, carrying on the flickering sometimes found on VDUs. Reverse video and screen intensity are operator adjustable. the Vector VIP, the smallest in the The disk drive unit is not removable Vector range, arrived on our desk to from the console and did provide a bit of a hassle when trying to move them around the office. Having spoken to Fiona about this I am in-The Vector VIP is a general purpose formed this has been remedied and microcomputer system designed the whole unit is now completely

The quad density micropolis 51/4" include: (a) 56K of user available diskette drive provides a storage

is capable of interfacing to one

printer at a time, either one of Vector's system printers or one of the many standard printers available on the market, through the RS232C interface. We used an Anadex from Bell and Howell (watch ETI for a review) and gained very good results, quickly and without fuss.

Communications - The communications capability includes the ability to interface to a standard asynchronous modem or acoustic coupler, and standard software with the system enables 30 characters per second — that is 300 bits per second, data transmission rate. It also provides emulation of a 'dumb' serial terminal, making the VIP series suitable for use as intelligent. In providing the VIP with a CP/M terminals handshaking with larger mini or mainframe systems for distributed data processing in offices or remote processing in environments such as universities or colleges.

Operating system

The operating system is the industry standard CP/M2 from Digital Research, allowing use of most CP/M-compatible software. The documentation with the system was truly amazing and by far the best seen to date. Documentation completely covered every aspect of the operating system in great detail and left nothing remaining to mystify and confuse. The explanations covering the use of the disk drives and loading of the operating system were excellent.

Software

operating system, Vector has virtually opened a 'Pandora's box' with all sorts of available goodies to meet almost every conceivable requirement. Application packages can be obtained from a number of

VECTOR VIP SPECIFICATIONS

	Height (mm)	Depth (mm)	Width (mm)	Weight (kg)
Console	324	457	533	10
Disk drive	82	211	145	7.9

Video terminal

Screen 305mm monochromatic Characters ASCII, 8 x 10 dot matrix Dark on light or light on dark selected by software (normally light on dark) Contrast Operator adjustable Display area 24 lines x 80 characters Radiation

Complies with US Federal Regulation for Radiation Control, as required by the Radiation Control for Health and Safety Act of 1968, implemented by Title 21, Subchapter J, Code of Federal Regulation

Diskette drive

Diskette used 16 sector, hard sectored, 133 mm 480K unformatte Capacity 250K bits/second Average rotational latency time 100 milliseconds 30 milliseconds Access time track-to-track Settling time 10 milliseconds Head load time 75 milliseconds Drive motor start time 1 second Rotational speed 300 RPM Recording density 5248 bits per inch (BPI) Track density 100 tracks per inch (TPI) Surfaces used on diskette

Communications

Interface Asynchronous baud rates 110, 150, 300, 600, 1200, 2400, 4800, 9600

8000 hrs

1 in 106

Reliability

Seek error rate

MTBF

MTTR Media life 3 x 106 passes on single track Head life 104 hrs Soft error rate Hard error rate 1 in 1012

Associates Company, and Davidson available include: (1) The system Software in Melbourne. A number of can support up to three additional high level languages can be mini floppy drives by adding supported, including Microsoft additional Unistor modules. (2) BASIC 80, one of the fastest and Letter quality or high speed dot most powerful general purpose matrix printing facilities. (3) languages available. The Vector Graphics capabilities including Execuplan Package is totally Vector's high resolution graphics brilliant for the executive wishing to display board, Vector's video obtain fast and accurate forecasting digitiser board, a graphics monitor, and planning.

Development tools — including and associated software. SCOPE — an advanced, screenoriented program editor; RAID a full-screen simulator-debugger The Vector VIP is an impressive, fast for assembly language programs; and very reliable desk-top micro ZSM Assembler using the 8080 suitable for a vast number of subset mnemonics; and the ex- business, industrial and educational tended systems monitor on PROM, applications. It is easy to use and allowing direct manipulation of should provide the first-time user memory and input/output, can be with a valuable means of entering used for enhanced programming the computer age. and program manipulation.

sources including the US Lifeboat Optional hardware and software a TV camera to go with the digitiser,

Conclusion

Elaine Ray

Dot-matrix printer breaks \$500 price barrier

Dick Smith Electronics has just announced the release in Australia of a compact 80-column, tractor-feed impact printer which offers a variety of features for \$495 including sales tax.

The new printer features 30 Northstar Horizon etc. characters per second printing, with 5 x 7 dot-matrix characters and the capacity to print the full upper and lower case ASCII character set. It is also capable of high-resolution graphics printing, software selectpaper up to 204 mm wide, and is fitted with a standard Centronicstype parallel interface. This makes it compatible with most microcomputers like the System-80, Exidy Sorcerer, Tandy TRS-80, availability.

The new printer measures a compact 328 x 127 mm and weighs just 2.5 kg. It is manufactured for Dick Smith Electronics in Japan, and uses a single hammer print head. This considerably reduces the able. It accepts fan-fold sprocketed number of moving parts in the head, and is claimed to give improved reliability.

The new printer has DSE catalogue number X-3252. Check your local Dick Smith store or dealer for

Nelcon '81 in Dunedin

Nelcon '81 (New Zealand National Electronics Conference), the eighteenth in a series of yearly conferences held with the aim of providing a forum for the New Zealand electronics community, will be held at Otago University in Dunedin from August 25-27.

Around 20 papers will be presented on the themes of educa- presented range from the appli-Zealand context. Papers to be as an aid in the learning process.

tion, application, and economics of cation of electronics in industry to electrical technology within the New the use of microprocessor systems



First, disposable nappies now, disposable print heads!

Just like the baby's nappy, the print head in EPSON's new MX-80 Matrix printer is disposable.

characters, service is as simple as multi-pass techniques to generate changing a ribbon cartridge. "In fact", says a spokesman for Australian distributor, "you just snap in a new one. It is as easy as that. The only tool you need is at the end of costs less than \$50.00."

This feature is considered by Epson to be a real breakthrough, bringing excellent service reliability and dependability in whatever application the printer is employed.

The MX-80 provides the choice of ing in as many as four distinct 12 different combinations which bones' price. can accommodate nearly any printing requirement. More than half of local Warburton Franki office.

Rated at a full 50 to 100 million these utilise multi-strike and/or 'correspondence quality' printing.

Ideal for manuscripts, mailing Warburton Franki, the Epson labels, proposals and nearly any other function where attractive, the head out, throw it away and snap clean, clear, well-formed characters are required. "So long as you are not trying to fool someone into thinking your arm. Anyone in your office can that you actually typed a document do it and the replacement head or letter," says Warburton Franki, 'the MX-80 can handle nearly all of your text processing requirements".

Other attractive features of the MX-80 are bi-directional printing logical seeking of shortest lines -80 cps — 64 graphics characters forms handling etc. At \$900 40, 80, 66 or 132 columns of print- (discounts on quantity available) Warburton Franki regard the MX-80 printing density modes, a total of as a fully loaded printer at a 'bare

For further details contact your

events will be held.

Further information and registra- 56, Dunedin, New Zealand.

In conjunction with the con- tion forms are available from ference a trade display and social Nelcon '81, c/- Extension Department, Otago University, P.O. Box MICRO-80 is a monthly magazine dedicated to users of SYSTEM 80 and TRS-80 microcomputers. Owned and produced entirely in Australia, each issue of MICRO-80 contains at least six programs, articles, useful hints and answers to readers' problems; all designed to help pieces of software and 10 hardware projects. Most of the programs and articles are written by our readers to whom we pay publication fees thus enabling them to make their hobby pay. MICRO-80 readers can save money by buying Tandy products at 10% discount from an authorised dealer — for details see any issue of MICRO-80. Our sister business, MICRO-80 PRODUCTS, sells Australian designed and produced software and high quality, imported goods at low, sensible prices. We repeat, if you own a SYSTEM 80 or TRS-80.

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Daisy Wheel Typewriter/Printer

MICRO-80 has converted the new OLIVETTI ET-121 DAISY WHEEL typewriter to work with the TRS-80 and SYSTEM 80 or any other microcomputer with a Centronics parallel port (RS 232 serial interface available shortly). The ET-121 typewriter is renowned for its high quality, fast speed (17 c.p.s.), quietness and reliability. MICRO-80 is renowned for its knowledge of the TRS-80/SYSTEM 80 and its sensible pricing policy. Together, we have produced a dual-purpose machine:-an attractive, modern, correcting typewriter which doubles as a correspondence quality Daisy-wheel printer when used with your micro-computer. micro-computer.

How good is it? - This part of our advertisement was typeset using an ET-121 driven by a TRS-80. Write and ask for full details.



To: MICRO-80

Expiry date

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SYSPAND 80 is a self-contained module which connects to the expansion port on your SYSTEM 80 and gives you a CENTRONICS parallel port to drive a printer PLUS the TRS-80 40 line bus, SYSPAND 80 allows you to connect all Tandy peripheral, including the expansion interface, disk drives, MICROTEK MT-32 memory expansion unit and the fabulous EXATRON STRINGY FLOPPY.

TRS-80 MEMORY EXPANSION UNIT MT-32 ... \$149.00

The MT-32 is manufactured by MICROTEK Inc., USA. It provides a CENTRONICS printer port and sockets for up to 32K of dynamic RAM. It comes complete, ready to plug into the expansion port of your Level II 16K machine. (Will also work with your SYSTEM 80 via SYSPAND 80).

16K MEMORY EXPANSION KIT

ONLY\$ 30 incl. p&p

These are prime, branded, 200 ns (yes, 200 ns!) chips. You will pay much more elsewhere for slow, 350 ns chps. Ours are guaranteed for 12 months. A pair of DIP shunts is also required to upgrade the CPU memory — these cost an additional \$4.00. All kits come complete with full, step-by-step instructions, no soldering is required. You don't have to be an electronic type to instal them. type to instal them.

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Disk drives are expensive and so are diskettes. As with any magnetic recording device, a disk drive works better and lasts longer if disk drive works better and lasts longer if the head is cleaned regularly. In the past, the problem has been, how do you clean the head without pulling the mechanism apart and running the risk of damaging delicate parts. 3M's have come to our rescue with SCOTCH BRAND, non-abrasive, head cleaning diskettes which thoroughly clean the head in seconds. The cleaning action is less abrasive than an ordinary diskette and less abrasive than an ordinary diskette and no residue is left behind.

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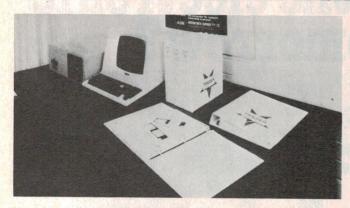
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TRADER for traders

Anderson Digital Equipment recently released a new software product called 'TRADER', which stands for Total Real-time Accounting from A.D.E.

signed and written in Australia for ment for attention. are simple and easy to follow.

the user to teach himself without invoice. fear of damaging his own vital data. TRADER uses a data-base confee.

can professionally advise how the ports, according to A.D.E. system will benefit the prospective program problem reporting pro- 3149 Vic. (03)543-2077. cedure where a user can detail his

The product is a fully integrated problem on a set form and mail it to suite of computer programs de- A.D.E.'s software support depart-

use with the North Star micro- TRADER features a unique concomputer. The product comes with trol routine that looks at each a set of manuals that A.D.E. claim transaction and decides what should be done with it. For example, TRADER incorporates invoicing, if an invoice is created TRADER stock control, debtors ledger, updates the debtors ledger, reduces creditors ledger and general ledger. stock and updates the general With each set of TRADER soft-ledger file. This is done autoware, you receive a diskette with matically with no action by the sample data on file which permits operator other than creating the

addition, monthly training cept where, for example, each courses are available for a nominal debtor's transaction is linked to the respective debtor; the system does Each dealer is trained in the not need to sort. Therefore the user concepts and use of TRADER and can quickly and easily obtain re-

For further information contact user. To assist with program Anderson Digital Equipment Pty problems A.D.E. has developed a Ltd, P.O. Box 322, Mount Waverley,

Local support at US prices

A new computer centre opening in Sydney this month will offer its own microcomputer products on the basis of standard US retail pricing.

Q.T. Computer Systems (Aust), associated with Q.T. Computer applications. manufacturers.

software, offering a complete Q.T.'s press release. service to the microcomputer user. S100 boards, all available either as test all products before delivery. bare boards or assembled and tested.

There will also be a full range of opening at 283 Clarence Street, complete systems, customised to opposite Sydney Town Hall, is suit all commercial and industrial Other supplies Systems Inc., USA, one of the stocked will include diskettes, largest stateside microcomputer ribbons, print wheels and some hard to obtain components - and if Q.T. Computer Systems (Aust) they don't have a particular item in will specialise in both hardware and stock, they'll get it, according to

Q.T. Computer Systems say they The centre will cater particularly for will provide a fully equipped repair microcomputer users, providing a centre, staffed by a qualified wide range of IEEE-compatible engineer, and will commission and

> All standard warranties will be fully backed and local software

Exorciser buss boards

Two multipurpose microcomputer cards, the 68MB02 16K RAM/ 16K EPROM memory card and the 68GPB03 general purpose card are available from Micro Gear and would be of interest to Motorola micro enthusiasts or users.

gether with a small number of grammable timers. support chips make these cards Also provided are 16 analogue input Motorola Exorciser buss signal be typically 25 microseconds. compatible and the 68GPB03 is Two digital to analogue con-

board is configured as four connectors. independent 8K blocks, two of RAM This card comes fully assembled

communication interfaces, each of assembled and tested for an which can be synchronous or additional \$180 plus tax. asynchronous. Baud rates can be Contact Micro Gear, 3 Coora generated in both hardware and Place, Churchill 3842 Vic. (051) software. Thirty-two programmable 67-1498 or (051)22-1157 after input/output lines are provided as hours. well as two 6840 timers, which in

Combination and versatility to- turn contain three individual pro-

attractive to the amateur as channels with a resolution of 12 bits well as the advanced user. Both are and input ranges programmable to designed and manufactured in -5 to +5 volts and -10 to +10 Australia by Micro Gear. They are volts. Conversion time is claimed to

also Exorciser outline compatible. verters with an output voltage range The 68MB02 is capable of occu- of 10 volts and a resolution of eight pying up to 32K of memory, 16K in bits, together with a reset on power RAM and 16K in EPROM. 2114s are up circuit are also included. The used for RAM while the EPROMs are board occupies 32 consecutive lo-2716 using single supply 5 V types. cations and can be positioned to EPROM can be expanded in 2K any 32 bit block in the 64K address increments whilst RAM can be ex- space. All signals are available on panded in 1K increments. The two double-sided gold-plated edge

and two of EPROM, which can be and tested on the digital side allocated to any 8K boundary in the but without 2 x 6820, 2 x 6840, 64K address space. This card is 2 x 6850/6852 and hardware baud available as a bare circuit board for rate generation. Price is \$160 plus \$75 plus 15% sales tax, if applicable. 15% sales tax. It can be obtained The 68GPB03 contains two serial with all analogue circuitry and

mmm Club call

A Compucolor II Intecolor microcomputer users group (CUWEST) has been formed in Western Australia.

CUWEST meets every month and has a software library available to members. Anyone interested in joining the group can contact J.D. Newman at 8 Hillcrest Drive, Darlington W.A. 6070. Phone

The Northern and Western Suburbs Computer Users' Group of Melbourne held its inaugural meeting on Thursday night, February 5th.

The Group, comprising 12 users of "TRS-80" computers and a correspondent member in Brisbane, represents considerable expertise in computer programming, especially in BASIC and assembly languages, as well as hardware adaptation. Operators of microcomputers and others interested are welcome to join the group to participate in exchange of knowledge and mutual help in improving computing technique.

Meetings will be held at 142 Pascoe Vale Rd, Moonee Ponds, each second Thursday at 7 pm. Those interested in joining the group may telephone David Coupe (03)370-9590 or Clive Budd (03) 370-2917.

mmmm

support will be provided for all microcomputer products, par- Mr. Leon Mor, and it is suggested ticularly for CP/M based systems, you contact him for further details. TRS-80 and Apple.

The centre will be managed by (02)929-8655.



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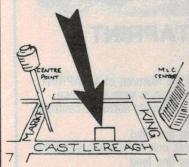
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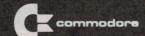
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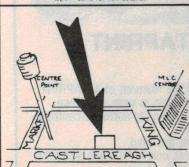
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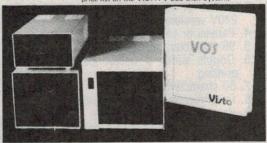


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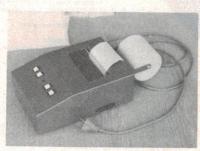
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"Mark II" System 80 for business

Dick Smith Electronics has announced the release of a new System 80 Mark II Business Computer, specifically designed for small to medium business computing applications.

The new machine is a development from the existing model I virtually all of the features which have made the basic machine so widely used by schools, colleges and private individuals, together with many additional features designed to make it more suitable for serious business use.

These features include full video display of lower-case letters (essential for serious word processing), and a separate numerical keypad for fast and easy entry of numbers. The machine also features a built-in "communications terminal" program, making it ideal for accessing data base services.

The additional features of the Business Computer make it virtually three machines in one: a computer, a word processor (with suitable software), and a data communications terminal.

Like the original System 80, the Mark II Business Computer provides the industry-standard Microsoft "Level II" 12K BASIC as standard. This means that it can run just about all of the enormous range of software that has been written in this version of BASIC.

As well as providing display of the full upper and lower case character System 80 computer. It provides set on its video screen, the new Business Computer provides lower case letters having "true descenders" for maximum clarity.

It also provides a choice of two different keyboard modes: "typewriter" mode (lower case letters normal, with the SHIFT key used for capitals) and "computer" mode (upper case letters normal). This makes it ideal for both word processing and normal computing.

In addition to the normal typewriter-format keyboard, a separate 15-key numeric keypad is provided which lets you key in long strings of numbers rapidly with one hand and a minimum of effort.

The numeric pad is complete with BACKSPACE/ERASE, decimal point, comma and duplicate ENTER keys, quite separate from the main keyboard. In addition it provides four special "user-defined" keys which may be used to input special control codes or to call up certain functions.

Other features of the System 80 Business Computer include an optional automatic flashing cursor, which may be used or disabled as



desired, protection bounce' on keyboard reliability.

Using a matching Expansion Unit, add-on memory card and available is a Stock Control and various peripheral units such as Pricing System, a General Ledger printers, floppy disk drives and a System, an Accounts Receivable modem, the machine can be ex- System and a low cost mini word panded into a very powerful small processor business system. It can have up to processing. 48K of user RAM memory, up to printer. An Australian-designed and each state. manufactured acoustic coupling

and improved "anti- modem is also available, for data the communications. The System 80 scanning, for greater Mark II will be supported with a range of Australian-developed software. Among the software currently for casual

Listed as catalogue number four floppy disk drives with a total X-4100, the new Dick Smith System capacity of more than 400 000 80 Mark II Business Computer is characters, and a choice of either a priced at \$1495.00. The new higher speed matrix printer or a machine will be available from Dick word processor-type daisywheel Smith branches and resellers in

Switchmode supplies feature tiny size, low price, top specs.

Switchmode power supplies, in great demand in the micro market, have not been widely available here, but that should change according to Autotron, who recently gained the Power General

Perhaps the best example of US manufacturer Power General's product prowess is their recently released 1050 series of 50 W switchmode supplies.

Each power supply has overall dimensions of 120 x 120 x 44 mm, weighs 560 g and costs \$139. The available dc outputs (5 V at 10 A, 12 Vat 4 A, 15 Vat 3.4 A, and 24 Vat 2 A) are floating, so either side may be grounded.

True off-the-line switchers in the series achieve 80% efficiency in power conversion, according to Power General. Output ripple and noise are quoted as 50 mV peak-topeak or 15 mV RMS. Supply inputs are pin-strappable — either 85 to 130 Vac or 170 to 260 Vac at 47 to 470 Hz.

The line regulation is given as and the temperature coefficient 202, Glen Waverley 3150 Vic. ±0.02%/°C. The transient response

is quoted as 300 μ s to 1% of the final value: the input-to-output isolation 2500 Vac; and the holdup time 16 ms after the loss of ac power.

Full-rated output is provided over an ambient temperature range of 45°C and operating 0 to temperature is specified from 0 to 70°C. Current limiting, fusing, inrush-current limiting, soft start, and remote sensing are all standard features.

Every series 1050 unit uses Schottky rectifiers to minimise the diode voltage drop across its rectifiers.

single-sided MIL-SPEC FR4-grade printed-circuit board enhances the reliability of the power supply assembly. The price of \$139 is for 1-9 units with delivery ex stock. A catalogue is available on request $\pm 0.1\%$, the load regulation $\pm 0.2\%$ from Autotron Australia, P.O. Box

New address for Christchurch group

The Christchurch Microcomputer Users' Group have asked us to publish their new address.

To contact the group write c/- Jay D. Mann, 330 Centaurus Rd, Wednesday of each month at 7.30 phone Christchurch 32-5652.

The group meets on the second Christchurch 2, New Zealand, or pm in the Christchurch Polytech Lecture Hall.

Uncovering more of the Z80!

Holmes and Watson would have turned in their graves if they had read the article on uncovering the Z80 in April ETI, says reader Stephen Dennis of Dundas, NSW.

It is evident that Dr Moriarty distracted the otherwise thorough investigation that was made, because several other undocumented instructions can be found.

'If one looks at the numerical order listing of Z80 op-codes in the back of the Zilog Assembly Language Programming Manual, a strange omission occurs between CB 2F and CB 38. After looking at the operations that occur, the following rotate instruction can be deduced (elementary, my dear Watson):

reg 2 x reg + 1

i.e: shift left once and add one, hence the new mnemonic:

RLO reg machine code: CB 30 to 37

where reg is any of A, B, C, D, E, H, L, (HL) (the machine code corresponds to the standard Intel/Zilog convention for register values, i.e: B=0 C=1 . . . (HL)=6 A=7).

'If one looks even harder at the other unused Z80 op-codes (those with ED as a prefix), it is possible for one to find even more op-codes. However, to date most of these are duplicates of other Z80 codes or have as yet unknown effects on the CPU (i.e. not so elementary, Watson).

The best way to check these instructions is to try using them, because after all that is what the spirit of hobbyist computing is about (even if the manufacturers and advertisers tell you differently).

Printout

For Sorcerer Apprentices

Well, what can I say except "here we are again"? Over the past few months I have been incredibly busy, including extended trips abroad; time to write this article was simply not available to me. Without wasting any more space, let's go to the letters to the editor, still from last year.

Dear Editor,

Thanks for including some SORCERER programming ideas and facts in ETI. At last. When I go to buy an electronic mag, I always check for any useful information contained therein that I can use on my Sorcerer computer. So I bought the ETI magazine and will continue to do so if such articles continue to appear. To close I include a simple method of clearing a section of memory.

In Exidy Monitor e.g:

EN 0 (CR) 0000. 0 (CR)

0001: / (CR)

MO 0 1 S100 (CR)

In a flash, location 0 to 100 inclusive are loaded with '0's. To test this simply use the Monitor [DU 0 100 (CR)] command and examine the display.

Greg Fergus.

Thanks Greg. I can only hope that you'll see this page when scanning through ETI at your newsagent and apologise for what surely appears to you to be an inexcusable delay. Since I have no further travels on my diary for the rest of this year I suggest that you make a point of getting ETI regularly in the future. Your method of clearing memory is very nice and fast. Another approach to this for those who are not too familiar with hexadecimal calculations goes like this: (again in Exidy Monitor):

EN F080: (CR) >F080: 21 / (CR)

(You will now see an exclamation mark in the top corner of your

MO F080 F345 F081 (CR)

You'll see that there are 709 (2C5hex) exclamation marks on the screen. 2C5 hex is the difference between F345 hex and F080 hex, but there was no need to know this figure. This works of course in any location of memory, I used the screen addresses so you can see what's going on.

Over the last few months an ever increasing number of secondhand computers became available through publications like "Trading Post" or the daily papers. I rang several sellers to find out the reason for their decisions to sell. Two major reasons clearly stood out: upgrading to a bigger system (mainly small businesses) or lack of understanding of the computer. The second reason is a cause for concern, since the acquiring of a computer system is a rather expensive exercise. I for one was lucky to have a computer background; however, I found the first few days with my very own computer to be a most frustrating experience. No one could provide any answers to the seemingly insurmountable number of problems that suddenly awaited me during every spare minute of my life.

Today there are several companies and user groups who are quite capable of providing some answers to most problems. As far as I am aware, classes are available at some institutions for Apple computers but I have not heard of any classes for Sorcerer users. Sorcerer Mark II owners have the benefit of a much improved manual in comparison to the first publication. Here are a few of these improvements:

This instruction, although explained in the first manual, has been understood and utilised by very few. The revised edition of the BASIC user manual has the following to say about the WAIT instruction:

WAIT $\langle I,M\rangle[,\langle T\rangle]$. I,M and T must all be integers from 0 to 255, I is the number of the I/O port, M specifies which bits are to be tested, and T specifies a condition the tested bit must satisfy.

A bit is a single digit, either a zero (0) if the bit is off or a one (1) if the bit

is on (8 bits equal 1 byte), and a port is similar to a memory address (the Sorcerer has 256 such ports and uses ports 252 through 255), which can be scanned, written to or tested. The command "PEEK" is similar to "INP", "POKE" is similar to "OUT" while "WAIT" could be compared with an "IF...THEN" loop.

One application listed in the manual is WAIT 254, 16 which waits for the horizontal sync pulse to ensure that direct poke-ing to the screen

RAM does not "drop out".

I am interested to hear from any person who has used WAIT in other applications so we can pass on that information. By the way, WAIT uses ANDs and XORs, as most of you probably know by now. But what do ANDing, ORing, exclusive ORing mean in understandable terms?

*** ONE-STROKE INSTRUCTIONS:

There are seven more of these instructions situated on the numeric keys 1 through 7 on top of the alpha keyboard. Use them by pressing «GRAPHIC» and «SHIFT». They are:

«GRAPHIC» & «SHIFT» 1 = STR\$

«GRAPHIC» & «SHIFT» 2 = VAL

GRAPHIC & SHIFT 3 = ASC

GRAPHIC & SHIFT 4 = CHR\$

(GRAPHIC) & (SHIFT) 5 = LEFT'S GRAPHIC> & (SHIFT) 6 = RIGHT\$

(GRAPHIC) & (SHIFT) 7 = MID\$

*** AND: is used to zero a bit position in a byte. Examine the following truth table:

0 AND 0 = 0

0 AND 1 = 0

 $1 \, \text{AND} \, 0 = 0$

1 AND 1 = 1

*** OR: (logical) is used to set a bit position in a byte:

0 OR 0 = 0

0 OR 1 = 1

1 OR 0 = 11 OR 1 = 1

*** XOR: (exclusive OR) is used to compare two bits or to complement a bit:

 $0 \times OR 0 = 0$

 $1 \times OR 0 = 1$

 $1 \times 1 = 0$ In other words.

AND: if either bit is zero (0), the result is always zero (0).

OR: if either bit is one (1), the result is always one (1).

XOR: if one, AND ONLY ONE operand is one (1), the result is zero (0). An obvious application for this is: When you XOR any bit with 1 (one), it will be the opposite of what it was before.

"So far so good" was my reaction when first introduced to this, but in what situations do I use all this newly acquired knowledge? There was no obvious need for all that stuff. Then I wrote a program which was to allow me to enter data without worrying about lower or upper case. I used to write this in the following fashion:

IF A\$ = "Y" OR A\$ = "y" THEN

Nowadays I simply AND the character with 95 decimal. Here's why: 95 decimal equals 05F hexadecimal or 01011111 binary. Now, looking at our truth tables above, we will see that any figure between 96 dec (60 hex) and 127 dec (7F hex) will be converted to a number minus 32 (20 hex), but if it happens to be between 40 hex and 5F hex, no change will take place. Of course, I still must ensure that the character to be ANDed is not below 40 hex (less than an "A"). Should we decide that all letters have to be in lower case, we simply OR the figure with 96 dec (60 hex). In Exidy BASIC this would look like this:

100 A\$ = "X" : REM no need for 'LET A\$ = "X"!

200 A\$ = CHR\$ (ASC (A\$) OR 96): REM A\$ = "x"

300 A\$ = CHR\$ (ASC (A\$) AND 95) : REM A\$ = "X"

400 B\$ = "Hello": A\$=

500 FOR X = 1 TO LEN (B\$)

600 A = A\$ + CHR\$ (ASC (MID\$ (B\$,X,1)) AND 95)

700 NEXT X: REM A\$ is now "HELLO"; B\$ REMAINS "HELLO"

Bye for now, A.P.F. Fry

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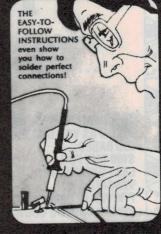
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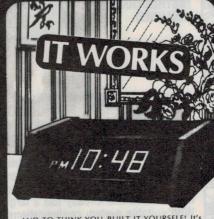


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A learners' microcomputer Part 2

How to get into microcomputing without boiling your brain cells or breaking the bank

This month we go into the 'architecture' of the microprocessor, an RCA CDP1802, detailing the functions of the various portions necessary for an understanding of how our computer works.

Design: Hugh Anderson

Development: Graeme Teesdale

THE OBJECTIVE of this project is to teach the beginner something about microprocessors while attempting the practical aspect of it. The following text in general will relate to devices used in this project; however, some comparisons with other device groups will be done.

Architecture

A block diagram of the 1802 is illustrated here.

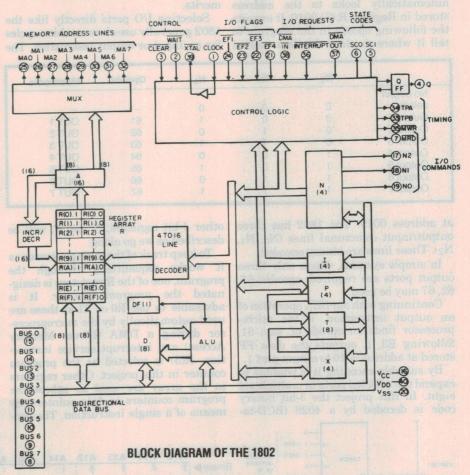
The principal feature of this microprocessor is the sixteen 16-bit register arrays, labelled (R). The 16-bit register gives an addressing capability of 216, i.e: 65 536 bytes, normally known as a memory system of 64K bytes. Because the 1802 is only an 8-bit microprocessor it is necessary to handle the 'R' register in two 8-bit bytes, designated as R(x).1 for high order bytes and R(x).0 for low order bytes, where R(x).1 indicates the selected 'R' register D to F, i.e: 0-15, and registers over decimal 9 are indicated in a hexadecimal code using letters A to F. The array can also be used as a 32 by 8-bit array if necessary.

Individual registers in the array (R) are selected by one of three 4-bit binary code registers: N, P, X — these in turn are converted to a 16-bit address to the R registers by the 4-to-16 line decoder. Each of the three registers has a particular use. For example: the X designator selects one of the 16 registers R(x) to 'point' to the memory for an operand (or data) in certain ALU (Arithmetric Logic Unit) or I/O (inputoutput) operations.

R(3).1 (R(3).0

R(3) 0 0 1 0

Address in memory where data is located instruction, assuming register 3 is to be used.



The memory system can be considered as a number of pigeon holes (up to 64K) starting at address 0000 and going to FFFF, each having eight individual storage compartments. I.e. an 8-bit data word (or byte) can be stored in two hexadecimal numbers, and hence

the data at memory location 0010 could be, for example, FF.

An output instruction will take the data FF stored in location 0010 and place it at an output port connected to the outside world.

Project 660

address 0010 OUTPUT PORT 1 OUTSIDE WORLD

How the microprocessor (CPU), random access memory (RAM) and input/output (I/O) sections of the computer are interconnected.

In this case the data is stored in RAM, memory which can be written to or read from.

The control instruction for the 'x' register is SET X (mnemonic SEX!) and opcode of EN where N is the selected 'R' register. Getting confused yet?! As we have selected Register 3, opcode is E3. When the microprocessor reads the opcode E3 in the program it automatically looks to the address stored in Register R (3) 0010. It expects the following opcode in the program to tell it where to place the data located

decimal decoder) to enable selection of RAM, a colour encoder IC and I/O IC plus additional expansion ports.

Not all microprocessors use this system; some consider designated memory locations as an I/O port. An example is the Motorola 6800 microprocessor. The popular Motorola 6800 D2 kit considers I/O ports at addresses 8004, 8006. Both methods have their merits.

Selecting I/O ports directly like the 1802 generally uses less machine cycles for an input or output instruction. The

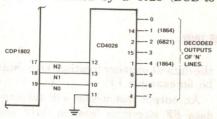
along the following	N ₂	N ₁	N ₀	Opcode	Mnemonic
No output			- 144 545	77.4	V tales
select	0	0	0		_
Output 1	0	0	1	61	OUT 1
Output 2	0	1	0	62	OUT 2
Output 3	0	1	1	63	OUT3
Output 4	1	0	0	64	OUT 4
Output 5	1	0	1	65	OUT 5
Output 6	1	1	0	66	OUT 6
Output 7	1 1	1	1	67	OUT 7

at address 0010. The 1802 has three output/input communal lines (N_0, N_1, N_2) . These lines are binary encoded.

In simple systems, where only three output ports are required, opcodes 61, 62, 67 may be used.

Continuing with our SEX operation of an output instruction, if the microprocessor finds an opcode of, say, 61, following E3, it outputs the data FF stored at address 0010 to output port 1.

By suitable encoding it is possible to expand the selected ports in multiples of eight. In this project the 3-bit binary code is decoded by a 4028 (BCD-to-



The 4028 BCD-to-decimal decoder decodes the CPU's 3-bit I/O commands and selects a colour encoder IC (1864), an I/O IC (6821) or RAM.

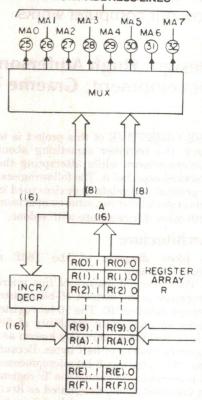
other 4-bit registers, N and P, will be described as we go along.

To keep track of the microprocessor as it works sequentially through the program, one of the R registers is designated the program counter. It is advisable to use R0 or R1 as these are used automatically by the microprocessor during a DMA (Direct Memory Access) and interrupt service instruction. R(3) is selected as the program counter in this project. Other registers in the array are used as subroutine program counters or data pointers, by means of a single instruction. The con-

tents of the P register can be used to call any R registers. The control instruction is SET P (mnemonic SEP), opcode DN, where once again N is the designated 'R' register.

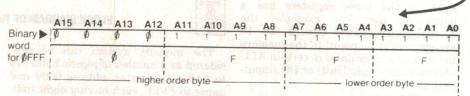
To enable the microprocessor to select any address location from 0000 to FFFF a 16-bit address buss must be used. To reduce the pinout count on the 1802 microprocessor chip, RCA have a bitmultiplexed address line labelled MA0 to MA7.

MEMORY ADDRESS LINES



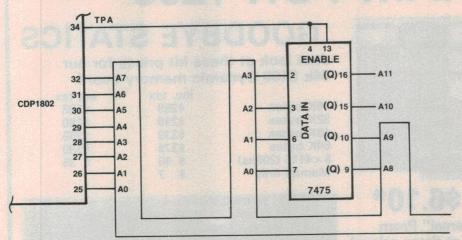
In each cycle, the high order byte, A8—A15, appears on the memory address lines MA0-7 first. Those bits required by the memory address are latched by a timing pulse, TPA. The low-order byte of the 16-bit address appears on the address lines after the termination of TPA. Latching of all eight higher-order address bits would permit a memory system of 64K bytes.

Taking address 0FFF as an example:



How the binary word represented by the hex number 0FFF appears on the 1802's address lines. As there are only eight lines, the higher order byte (A8-A15) appears first, followed by the lower order byte (A0-A7). In the 1802, the order of the lines is inverted (A8 is where A0 is, etc).

learners' micro



Part of the latch circuit used to temporarily store the lower four bits of the higher order address byte. When the TPA pulse from the 1802 goes low, the 7475 puts the data that was present on the data inputs, at the time TPA was present, onto address lines A8-11 while the 1802 switches the lower order address byte onto the data buss lines A0 to A7.

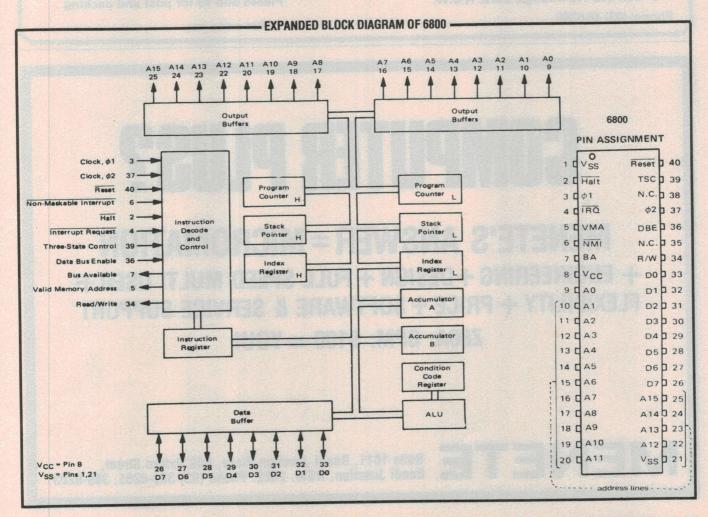
It can be observed from the table at lower left that it is necessary to decode only the lower four bits of the higher order byte plus all of the lower order bytes for the microprocessor to address memory location 0FFF. It is important to remember that with the 1802 multi-

plexed address lines A8 becomes A0 and so on, up to A15 (i.e. they're inverted).

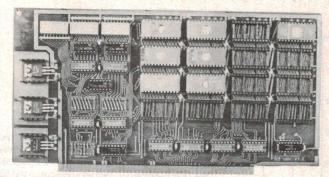
The 7475 is a 4-bit bistable latch and is used as temporary storage for the lower 4-bits of the high byte. When the ENABLE pins (4 and 13) go low, the information that was present at the data

input at time of transition is returned at the Q outputs. The latching pulse (TPA) is obtained from the 1802 at pin 34. Just using this one 4-bit latch it is possible to decode the necessary address lines.

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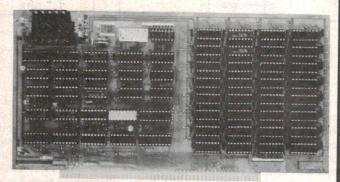
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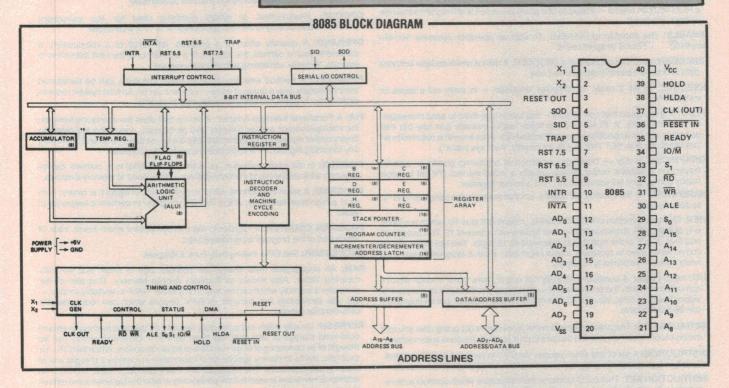
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The 8085 microprocessor has the lower order address bytes multiplexed on the data buss lines.

NOTE: Owing to a last-minute problem with supply of some critical components, we have had to hold over the description of the construction of the ETI-660 computer,

which should now commence in the July issue, assuming no further intervention of problems beyond our control, fairies at the bottom of the darkroom, etc.



☆ ☆ ☆ GLOSSARY OF TERMS ☆ ☆ ☆

ALU: Arithmetic Logic Unit. That portion of the microprocessor which performs calculations according to a set of instructions.

ADDRESS: A label or name (usually a binary or hexadecimal number) specifying a particular memory location.

ALPHANUMERIC: Letters and numbers.

ARRAY: A group of related variables or constants — generally given a name or a label. Arrays are often located in consecutive memory locations.

ASSEMBLER: A program which takes a series of mnemonics and puts out a corresponding binary code.

ASYNCHRONOUS: Refers to a circuit in which the various elements are not arranged to change state in synchronism, or in synchronism with another signal.

BCD: Binary-coded decimal. A binary code to represent decimal numbers; for example, a 4-bit binary code can represent the numbers 0 to 15.

BASIC: The most widely used high-level computing language in the hobbyist field. Allows you to communicate with a computer in a fair approximation of pidgin English.

BAUD: A measure of the speed or rate of data transmission. A baud is the transmission of one bit per second.

BINARY: The 'base two' number system in which everything is represented in 'ones' and 'zeros'.

BIT: A single binary digit, representing either a 'one' or a 'zero'.

BOOTSTRAP: This refers to the process of inserting instructions or entire routines directly onto the data and address busses of a computer.

BRANCH: A certain instruction included in a program which makes the processor perform a step out of the usual sequence, usually if a certain specified condition is satisfied. A branch instruction will skip or jump following instructions.

BUSS: Generally, a system of interwiring in which each line has a designated task or carries only specific signals. A buss permits communication between devices making up a computer and can be one-way (unidirectional) or two-way (bidirectional).

BYTE: A binary number, usually of eight bits. It can represent a number from 0 to 255 (8-bit byte) as there are 256 possible combinations of ones and zeros eight bits long.

CPU: Literally, 'Central Processing Unit'. Generally a shorthand term referring to the microprocessor in a microcomputer. One of those terms used more out of tradition than for the sake of clear writing.

CLOCK: An oscillator that provides timing signals which synchronise the operations carried out by the microcomputer.

DMA: Direct Memory Access (or Direct Memory Address) — means what it says. The term refers to the practice of fetching data directly from memory by an external (or peripheral) device without the need for intervention by the microprocessor. A good technique for speeding data transfer.

DEBUG: A BUG is a mistake or similar problem that has crept into a program. The act of debugging removes it. The terms were said to be introduced when a small insect crawled into an early computer and stuffed up its operation. The 'bug' was ceremoniously taped to a page of the report which explained the fault. No, we don't *quite* believe it either, but it's a good story anyway and its inclusion is a long-standing tradition.

DECODER: A device which changes one code to another. For example, a 4-bit binary code may be changed to a 1-of-16 code. Certain ICs are made to perform such a function, generally called 4-to-16 line decoders. A 4-bit to 1-of-10 decoder is called a BCD to decimal decoder — for each 4-bit code in, the decoder will activate the appropriate one of its ten output lines.

DISABLE: The opposite to ENABLE. To halt an operation — to turn something off (if only temporarily). Also, look it up in your Funk & Wagnell.

Project 660

DUMP: If you have a wheelbarrow full of dirt and tip it out, that's dumping the dirt. If you have your computer memory full of data and you transfer it to tape, that is called dumping. Note you can dump from memory to tape and from tape to memory. Pity you can't do it with wheelbarrows sometimes.

EPROM: Erasable Programmable Read Only Memory. First, look up ROM. It says, basically, that ROM is a memory you can't muck around with. An EPROM is thus a ROM you can rewrite — Erasable (the ability to rub out) and Programmable (the ability to program).

ENABLE: The opposite to DISABLE. To start an operation (promise her/him anything . . .). Funk & Wagnell refers.

ENCODER: Aha! The opposite of a DECODER. A device which accepts uncoded data and turns it into the appropriate code.

EXECUTE: To kill a cutey. In computer language — to carry out a series of instructions.

FLAG: A linen object that one salutes — but sailors use them to send messages. So do computers. A FLAG is an indicator signal (usually just one bit) that generally signals a condition to a peripheral device. (If an answer is required to an instruction such as SET THE FLAG, it's usually "Aye, aye cap'n").

GRAPHICS: Literally — drawings; a method of producing graphs or pictorial figures on a suitable output device, usually a video monitor (TV set for most hobby computers) but sometimes a chart recorder or printer.

HARDWARE: All the circuitry, pc boards, etc that make up the computer and/or its peripherals.

HEX: Shortened version of HEXADECIMAL, meaning '6 plus 10', which is a funny way of saying 16. It refers to the number system with a base of 16. This uses 0 to 9 and then A to F of the alphabet to represent its 16 digits. Two hex digits can be conveniently used to represent a byte (eight bits). Funk & Wagnell reckon it's a nasty curse — so do many hobbyists.

I/O: Input/Output. A computer generally has one or more 'ports' through which it communicates with 'the outside world' — peripheral devices such as a keyboard, video display, printer, etc. An I/O port may be just an input or just an output or it can be bidirectional.

INITIALISATION: The process by which the processor is got going after you turn the power on. In some systems you have to do it, in others it's done automatically.

INSTRUCTION: A set of bits which causes the CPU to carry out a particular task. Usually a basic or fundamental command understood by the microprocessor.

INSTRUCTION SET: That set of fundamental instructions which controls a microprocessor's or computer's basic set of possible operations. In general, the larger the instruction set the more powerful the microprocessor.

INTERFACE: A device or circuit which effects the transfer of data (in the appropriate form) from one system to another or one part of a system to another part. In homo sapien terms, this is represented by the FIST — as in, "ya wanna punch in ter face?"

INTERPRETER: A program which permits the computer to operate with a high-level language. It literally interprets what you've written in the high-level language into a code (bloody great strings of 1s and 0s) which the microprocessor 'understands' (i.e. acts on).

INTERRUPT: What happened to Mt St. Helens. In a computer, an interrupt suspends (hence, interrupts) normal program execution while something else happens (usually urgent and involving a peripheral).

LANGUAGE: A repertory of instructions — symbols, expressions, etc, used to 'call up' the instructions or procedures a processor can execute. Higher-level languages are easily understood by mere humans and computers of all sorts designed to work with such languages (BASIC, for example) use an INTERPRETER to change this into the MACHINE CODE under which the processor operates.

LOAD: The opposite to DUMP. Generally it means 'to store' binary data, usually into some form of memory.

MACHINE CODE: The binary form of instructions which a processor actually 'understands'.

MEMORY: Sort of 'electronic pigeon holes' in which binary data is stored. There are many forms of memory, generally in two classes: volatile and non-volatile. Volatile memory 'loses' what's stored in it when you turn the power off. Memory ICs (RAM chips) are an example. Non-volatile memory permanently stores data. Cassette tape is an example.

MEMORY-MAPPED: An organisation of the hardware and software of a computer to enable data to be transferred directly from memory to an output port without involvement of the microprocessor.

MNEMONIC: Not related to manic, but you'll become that if you try and remember too many mnemonics. A mnemonic is a nickname for a particular instruction and is thus made easy for us humans to remember (we have quasi-volatile memories, generally affected by lack of sleep, drink and self-abuse). The 1802 microprocessor, for example, has one mnemonic called 'SEX' — see how easy mnemonics are to remember!

MODULATOR: A device, included in every good hobby computer, which takes the computer's output and converts it to an RF signal on a suitable TV channel, giving you a cheap, convenient visual display — even if you don't understand what's on the screen.

MULTIPLEX: The principle of arranging or communicating information from a number of sources by selecting each source sequentially.

OPCODE: Hexadecimal or binary numbers used for the instructions communicated directly to the microprocessor or CPU of a computer.

OPERAND: A quantity which is a constant, the result of a computation, a parameter you've defined, the address of a quantity or of the next instruction to execute. Virtually 'something with which to operate'.

PARALLEL: A method whereby data, so many bits wide, can be transferred simultaneously over a group of wires — one wire per bit. An 8-bit system requires eight wires. In effect, the bits are transferred 'in parallel'.

PIA: A Peripheral Interface Adaptor. A device that does the interfacing between the microprocessor and/or memory and peripherals, converting the outgoing binary coded signals to the appropriate signals for the peripherals and converting any incoming signals to the appropriate code for the computer.

POINTER: In the microprocessor, or in memory external to it, pointers can be registers allocated to listing memory address—they 'point' to memory locations.

PROGRAM: A set of instructions, either in mnemonics, in digital (i.e. binary) form or in a high-level language, which tells the computer to perform a sequence of tasks.

PROGRAM COUNTER: A register in the microprocessor which keeps track of which part of the program is being executed.

PROCESSOR: See CPU. Failing that, Funk & Wagnell.

RAM: An uncastrated male sheep; the zodiacal sign of Aries, and the verb meaning batter. Also known as Random Access Memory. That part of the computer's memory which can be read in any order. RAM is inevitably volatile—but new technology has brought us RAM devices which can retain data for considerable periods.

REFRESH: Usually refers to the process required by dynamic memory to ensure continued storage of data that has been sent to it. Dynamic memory can be thought of as consisting of large numbers of small capacitors. When the CPU, for example, wants to store a specific bit of information in memory it charges one of these small capacitors. The capacitor will slowly discharge itself, however, so additional hardware is used to look periodically at all of the capacitors and refresh the charge on each one.

RESET: Simply — go back to the start, do not pass GO, do not collect 200 bytes.

REGISTER: A general-purpose memory, or set of memory locations, built into the microprocessor itself. Sometimes, particular registers may be designated for a specific purpose.

ROM: Read-Only Memory — the opposite to Write-Only Memory (the latter being fairly rare and thus unconscionably expensive for the hobbyist, apart from being totally useless). Read-Only Memory is memory which cannot be altered or otherwise mucked about with. In humans it is that part of the brain which stores forever shameful, embarrassing or otherwise traumatic events.

ROUTINE: A whole program or part of a program designed to perform a single function or action.

SCRATCHPAD: A piece of wood on which cats are supposed to sharpen their claws in lieu of the furniture. In a microprocessor, a scratchpad is an area of memory used to store data temporarily or for making quick data transfers. It is probably the most frequently used area of memory.

SERIAL: The sequential transfer of data — one bit after another. Usually only one wire is needed for serial data transfer.

 $\mbox{\bf SOFTWARE:}$ Programs, routines, instructions, mnemonics — all that stuff the electronics works on.

STACK: A colloquial expression for a vehicle accident. Here, we're talking about a sequence of registers or memory locations. Usually, the last bit of data put in is the first fetched out.

STACK POINTER: An address that specifies the location of the last entry in the STACK.

STROBE: To rapidly read registers or memory locations in sequence.

SUBROUTINE: A part of a program which performs a specific task and which is available for use elsewhere in a program or routine, as often as you like.

VDU: Visual Display Unit. Usually a TV set in hobby computers, but may be a video monitor which accepts the digital output direct from the computer.

VARIABLE: A specified location in memory (usually RAM) which is allocated a specific meaning in a program or routine. It may contain a fixed data value or changing data during program execution.

£@%*!: Term used by extremely frustrated or provoked computer hobbyist when his program refuses to be debugged or his computer won't boot up.

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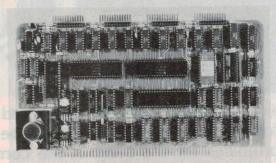
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Talking computers — new hope for the blind





A voice generator recently developed by the National Institute of Health (USA) will soon be opening up job opportunities for the blind in the computer and allied fields.

AS MICROCOMPUTERS start to produce synthetic speech, technology will open up job possibilities for the blind in programming, word processing, information storage and retrieval, telephone access reservations and other similar areas where a computer can instruct the operator by means of synthesised voice production.

At the Federal Administration in America, a blind computer programmer has been using a prototype microcomputer-controlled voice generator for over a year. With this particular application the host computer communicates by synthesised voice with the programmer, who inputs to the computer by a conventional keyboard.

A programmer with sight can of course see the printed read-outs or the data on a VDU. The blind operator has two systems available that will allow him to receive the same communication through other senses: tactile devices using his fingers, or listening to a voice generator.

Audible output, or listening to a voice generator, is by far the more popular,

Alan Concannon.

mainly because it is a more natural method. This system is also more efficient because blind people use their sense of hearing all the time for communication, and have it developed to an excellent degree.

Tactile devices include braille line printers and an optical-to-tactile converter called **Optacon**. This allows the user to scan the printed text or a display screen with a hand-held camera, and the tactile image of each symbol is presented to the finger of the other hand.

Voice generator

Two basic operations are involved in the generation of the 'voice'. First the printed words and punctuation are converted to commands, which in turn are converted into a suitable form to drive a voice synthesiser. Then the synthesiser converts these commands into an audible output. The most common method for this today uses a phoneme synthesiser with a speech-by-rule program under microcomputer control.

The generator used by the NIH (National Institute of Health, USA) for their experiment used a phoneme synthesiser that produces 64 different phonetic sounds, the program determining which are required for each word. Whenever a word has been completely vocalised, the microcomputer initiates transfer of pending phonemes for the next word. This transfer takes place in microseconds, so that speech output is continuous.

Speech synthesisers are also fairly versatile units in that they can control not only speed of delivery but also pitch and accent, and may be placed in either the manual or programmable mode.

The synthesiser unit is interfaced

with the computer, many miles away, via a telephone line. Data from the blind operator's keyboard is received and transmitted along the telephone line to the host computer. Data from the host computer is also transferred to the speech synthesiser over the telephone line for the blind programmer to hear.

The microcomputer used by the NIH included a CPU, 4K of RAM, 4K of PROM, a serial interface for the keyboard and a display screen. A display screen was used so that should garbled or complicated outputs come through the synthesiser and not be understood, the Optacon method can verify the material. The synthesiser can, however, spell out each word of the text for clarification, so a display screen may be dispensed with if desired.

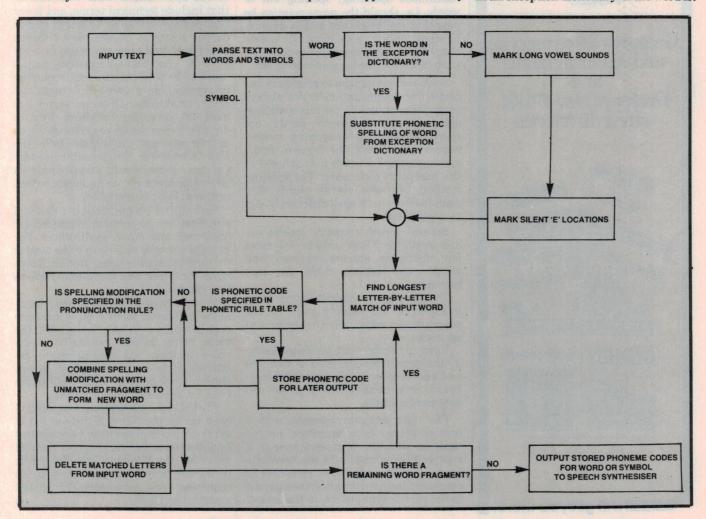
Fewer than 5% of common English words are mispronounced. Of these proper nouns, technical terms and foreign words produce the most mispronunciations. The text-to-phoneme conversion rules allow, where possible, one-syllable words only to be used.

Words are assembled from sequences of alphabetic characters and are pronounced upon the appearance of any nonalphabetic or punctuation character, such as space. Numbers may be pronounced on a digit-by-digit basis or a units-tens-hundreds basis. Therefore "27" could be pronounced "two seven" or "twenty seven". The digit-by-digit convention was selected for the NIH prototype because of clarity and correct vocalisation of lists of numbers separated only by commas, eg. 400, 700, 1000, etc.

Punctuation marks are verbalised, since special meanings may be assigned to them in various programming languages. Whenever the machine produces phrases or larger units of text, programmed pauses are generated for end-of-phrase symbol, eg. full stops, question marks, colons, etc. An unvarying designation is given to symbols that share the same representation; eg. hyphens and minus signs are both pronounced as minus signs.

How the voice generator works

As the unit goes through the text, it is analysed and separated into words and symbols. Each word is then searched for in an exception dictionary. If the word is





found the phonetic codes corresponding to that particular word are sent directly to the speech synthesiser. If the word is not found in the exception dictionary, the program scans for long vowel sounds, silent 'e' locations and other usual patterns which will be vital in determining pronunciation.

Various other pronunciation rules are then applied to determine which possible phonetic sound will be produced for the letter or letter sequence of a word. For example, 'cy' as used in the word 'cycle' would be given an 's' and not a 'k' sound.

The program then repeatedly applies word fragment rules of pronunciation to convert the remaining letters of words into phonetic codes. These are stored until all the codes for a word have been generated. Then all the symbols in a text string are matched and the program finally determines the appropriate complete verbalisation and pause duration.

Controlling the generator

The blind programmer can select three different modes of output: full words, i.e. continuous speaking; spelling out of words, i.e. should the pronunciation be poor then spelling it out makes comprehension easier; repeating of words, should for any reason the programmer wish to hear a word or sentence again.

Spell-out sometimes is necessary because the pronunciation is not always very clear, especially for mis-spellings, abbreviations, composite words, obscure computer programming symbol names like SQ.RT. for "square root", and any complex words not included in the exception dictionary. The spelling method can also clarify words that sound alike but are spelt differently, e.g. to, too and two.

Several speech response options are also available. These include slow rates for clarifying obscure phrases and words, and higher rates for text browsing.

To eliminate outside interference headphones are usually worn by the programmer, rather than using general speakers.

Voice generators can be controlled by a number of methods, such as switches and knobs, codes from a typewriter keyboard, or a combination of panel and keyboard controls.

The voice generator operates with a host computer in a 'translate' output mode or with the microcomputer in a speech 'review' mode. In the translate mode, text from the host computer is converted to phonetic codes, then passed to the speech synthesiser. In the review

mode, the last few hundred words of the text transmitted from the host computer are automatically stored in the microcomputer and are available for review and repetition.

A host computer usually transmits text or character data to the voice output generator faster than the speech synthesiser can operate. Unless the host output is constrained, the storage in the microcomputer may overflow, causing loss of data. Also, should the speech review mode be used, further data transfer from the host computer must be suspended to prevent an overflow of stored text.

Usually, an output suspension control code is sent to the host computer to halt delivery of further data. After the speech synthesiser 'catches up', an output resumption control code is transmitted to restart the text translate mode.

Conclusions

After its trial period the NIH system has drawn the following conclusions from its operating staff.

Higher quality speech is recommended for applications with vocabularies that include technical terms, and fewer user-defined symbols. Although speech output seems to be the most efficient way for a blind programmer to interact with a computer, hard-copy output needs to be used in situations requiring interpretation of unusual formats and error conditions. Optacon scans are used on occasions, working from a display screen, which may clarify words or phrases not intelligible from the Voice Generator. However, the need for Optacon scans should slowly diminish when improved speech and voice output terminal logic is available.

Specialised pronunciation rules and punctuation are sometimes needed for computer and other applications. For example, should voice output be needed for medical applications, pronunciation rules for Greek and Latin may have to be included.

Voice output generators and standalone terminals for the blind are now commercially available in the USA, the price being subject primarily to the voice quality and sophistication required. It is possible to buy a unit similar to the one used by the NIH in the USA for approximately US\$500.

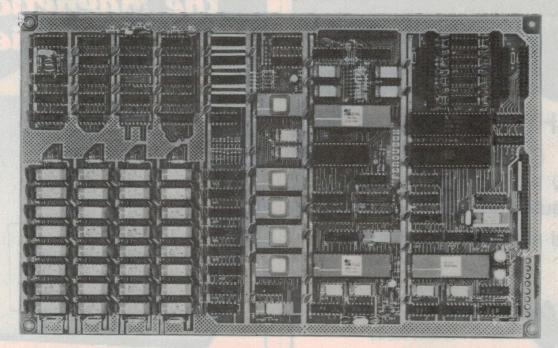
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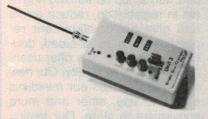
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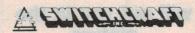
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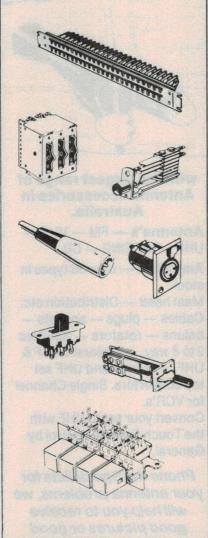
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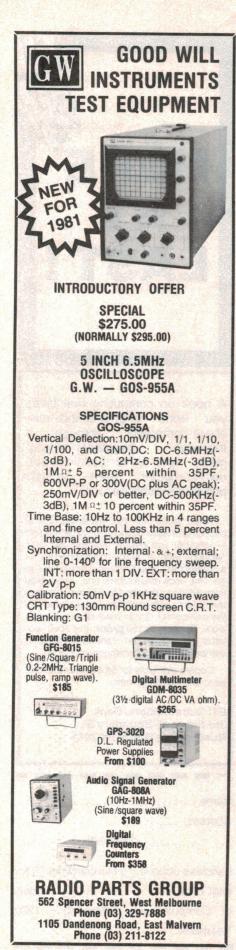
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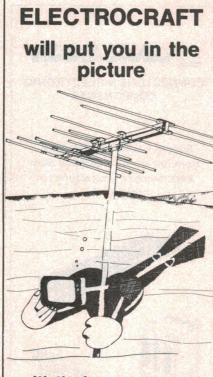
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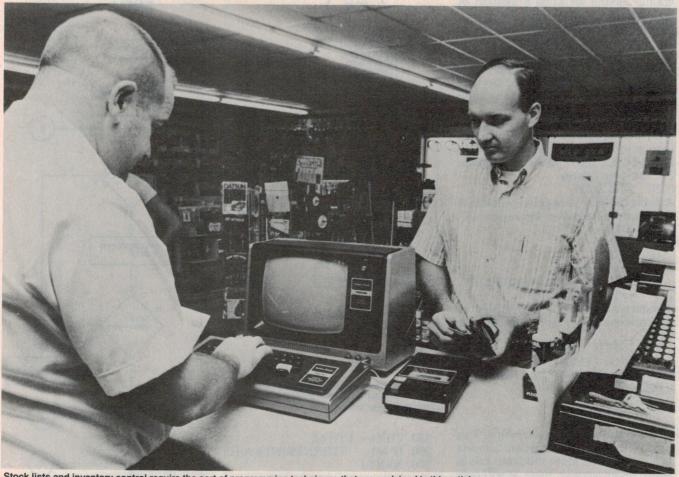
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Stock lists and inventory control require the sort of programming techniques that are explained in this article.

Advanced BASIC

Phil Cohen

If you have followed the previous series on Back Door Into BASIC, or picked up the rudiments of BASIC programming elsewhere, you're probably asking yourself "where do I go from here?"

THE EASY ANSWER to that question in the introduction is — programming experience will reveal all to the initiate. But really, that just isn't true. There's a lot to be learned about programming which most people (myself included) could never develop from scratch — novel concepts and systems, applied mathematical methods et cetera.

These Advanced BASIC articles, which will appear in ETI from time to time, are intended to provide food for thought and to stimulate those who have exhausted their interest in computer games.

The language used is an 8K Microsoft BASIC, and minor alteration will allow

the included examples to run on almost any medium-sized personal computer system.

Although the various parts of this series have been written to cover a particular concept or field of study, these are not covered exclusively—each part introduces other facets which add together to provide a useful program or major program segment in each part of the series.

Sorting

A very common problem in computing is sorting of one type or another. This can be anything from simple alphabetic sorting to sorting of time-dependent data from different sources to give an overall picture. There have been books written on different sorting algorithms — but we're not going into it quite as deeply as that!

This article covers three types of sorting: 1) pigeon-hole, 2) push-down and 3) ripple.

Pigeon-hole sorting

This is the fastest and most wasteful in memory space of the three types. It can only be used in some instances where the minimum difference between successive sorted data is fixed and known, and where the range of input values is also known. Essentially, it

entails having set aside a location in memory for each possible input and putting the incoming data into its reserved location as it comes in.

This type of sort is useful for applications such as the handling of monthly sales figures — it has the additional advantage of reserving space for interpolated data.

As an example of the use of this type of algorithm, the following program will take in up to 52 weekly figures (weekly sales, for example) and then interpolate the unknown figures.

10 DIM A(52)

A holds the data. A negative value in A will mark an 'unknown' figure, so:

- $20 \quad FORI = 1 TO 52$
- 30 A(I) = -1
- 40 NEXTI

Now input the data:

- 50 INPUT "WEEK NUMBER"; N
- 60 IF N < 0 THEN 105

A negative number will end the input of data and cause the results to be printed out.

- 70 INPUT "SALES"; S
- 80 IF S < 0 THEN 70

This stops locations being marked as unknown by mistake.

90 A(N) = S

100 GOTO 50

Now for the interpolation routine. This uses a linear approximation between two values to fill in the unknown values which occur between them. It will only do so between the earliest and latest known data.

105 J = 1

110 J = J + 1

120 IF J>51 THEN 250

130 IF A(J) > -.5 THEN 110

The above will find an 'unknown' week. If there are none left, it will jump to 250, which prints the results. Now find the nearest known figures before and after point J (the unknown week which we found above):

- 150 FOR AF = J + 1 TO 52
- 160 IF A(AF) > -.5 THEN 190

170 NEXT AF

180 GOTO 110

- 190 FOR BF = J 1 TO 1 STEP -1
- 200 IF A(BF) > -.5 THEN 230
- 210 NEXT BF
- 220 GOTO 110

BF and AF now contain the nearest 'known' weeks before and after J, respectively. Now interpolate:

230 A(J) = (A(BF)*(AF-J) +

A(AF)*(J-BF))/(AF-BF)

(This is a standard linear interpolation equation, which can be derived with a bit of geometry). Find the next unknown:

240 GOTO 110

Now print the results when finished:

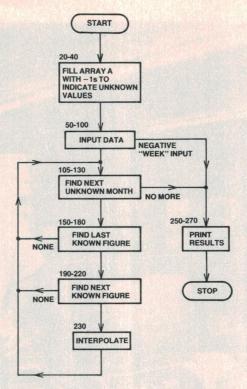


Figure 1. A 'pigeon-hole' sorting routine with interpolation. This can be used to provide an educated guess at missing sales figures.

250 FOR I = 1 TO 52

260 IF A(I)>-.5 THEN PRINT I, A(I) 270 NEXT I

A flowchart for the above program is

Push-down sorting

given in Figure 1.

In applications where a variable number of data points is to be sorted and stored in order, push-down order is often the best course, being fairly simple to implement. The idea is that a list of the items in order is kept in a 'stack' structure. This is simply an array larger than the largest number of data to be held. The data is stored in the array elements with subscripts below a certain value. This value increases as more data is added.

The value of the subscript at the 'top' of the stack is held in the 'stack pointer'. This is incremented as data is added to the stack

The addition of an item of data in the middle of the stack means that the rest of the data has to be 'pushed down' to accommodate it.

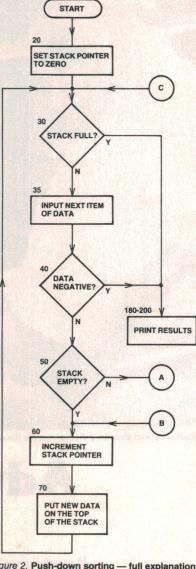
The following example will sort numbers into ascending order using a push-down algorithm (see Figure 2 for flowchart).

10 DIM S(50)

S is the stack — up to 50 items can be stored.

 $20 ext{ SP} = 0$

SP, the stack pointer, points to the



PART 1

Figure 2. Push-down sorting — full explanation in the text.

highest location which is in use. Setting it to 0 indicates that the stack is empty.

- 30 IF SP = 50 THEN PRINT "NO ROOM": GOTO 180
- 35 INPUT "DATA": D
- 40 IF D < 0 THEN 180

inputs the data (which is assumed to be above zero). A number below 0 is taken as an instruction to print the results. Line 30 checks to see if the stack is full up. First, find out where to put D—check to see if it's the *only* data:

- 50 IF SP > 0 THEN 90
- $60 ext{ SP} = ext{SP} + 1$
- $70 \quad S(SP) = D$
- 80 GOTO 30

If it's not the *only* data, find out where it should be in the stack:

PART 2



90 FOR I = 1 TO SP 100 IF S(I) D THEN 130 110 NEXT I

If D reaches this point then it must be the largest item, so

120 GOTO 60

puts it on the 'top' of the stack. If line 100 sends it to line 130, it must come just before item I. Push the data down just before item I:

130 FOR J = SP TO I STEP - 1

140 S(J+1) = S(J)

150 NEXTJ

and then insert D:

 $160 \, \mathrm{S(I)} = \mathrm{D}$

165 SP = SP + 1

170 GOTO 30

Now print the results:

180 FOR I = 1 TO SP

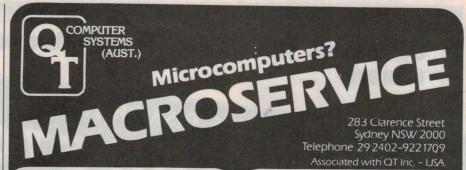
190 PRINTS(I)

200 NEXTI

Ripple sort

This is probably the best-known sorting algorithm. It is also known as bubble sort or even, on occasion, travellingwave sort.

The way it works is this: the data is put into a fixed-length array (or, as in the case of the example to follow, a stack). Starting at one end, the program compares successive pairs of items and swaps their positions in the list if they appear in the wrong order. It repeats this, starting at the same end each time, until it hasn't made any changes in the latest pass. Thus a 'bubble' of change sweeps up through the data array.



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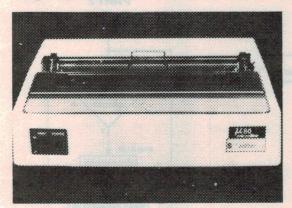
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The following program allows named data items with several parameters to be stored or deleted one at a time. They can also be sorted according to any one of the parameters. A full flowchart is given in Figures 3 to 6.

100 REM MAIN PROGRAM

110 INPUT "HOW MANY ITEMS (MAXIMUM)"; NI

120 INPUT "HOW MANY

VARIABLES PER ITEM ": NV

130 DIM A(NI, NV), A\$(NI), N\$(NV) A holds the parameters associated with each item, A\$ holds the item names and N\$ holds the parameter type names.

140 FOR I = 1 TO NV

150 PRINT "WHAT IS

VARIABLE ";I;" CALLED ";

160 INPUT N\$ (I)

165 NEXTI

170 SP = 0

SP is the stack pointer.

180 INPUT "COMMAND": C\$

190 IF C\$ = "I" THEN GOSUB 1000

200 IF C\$ = "S" THEN GOSUB 2000

210 IFC\$ = "R"THEN GOSUB 3000

220 IF C\$ = "E" THEN STOP

230 GOTO 180

inputs the command and takes the appropriate action. I = input, S = sort, R = remove and E = end.

1000 REMINPUT

1020 IF $SP \rightarrow NI$ THEN PRINT "NO ROOM": RETURN

checks for stack overflow.

1030 SP = SP + 1

1040 INPUT "ITEM NAME"; A\$(SP)

 $1050 \quad FORI = 1 TO NV$

1060 PRINT "WHAT IS THE"; N\$(I); "OF ITEM"; A\$(SP)

1070 INPUT A(SP, I)

1080 NEXTI

1090 RETURN

The above section of code inputs the new item and its parameters and puts them on to the end of the stack, incrementing the stack pointer.

2000 REM SORT

2005 IF SP < 2 THEN PRINT "NOT **ENOUGH ITEMS": RETURN**

Stops the user trying to sort one item! Trying to sort one item will upset the algorithm used.

2010 INPUT "SORT ACCORDING TO WHAT"; S\$

 $2020 \quad FORI = 1 TO NV$

2030 IF N\$(I) = S\$ THEN 2060

2040 NEXTI

2050 PRINT

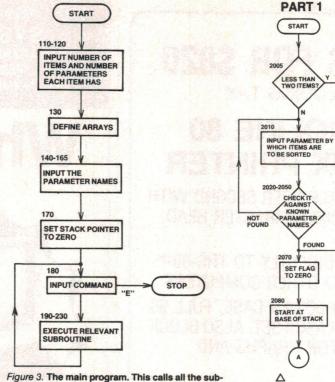
"NOT FOUND": RETURN

finds out which parameter to use in the

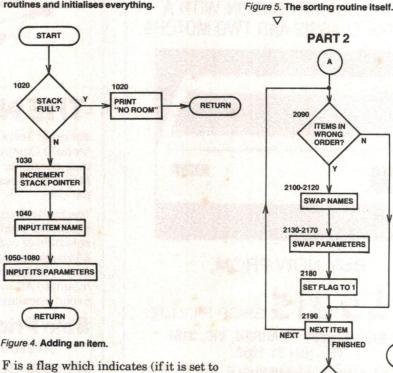
2060 S = I

stores the result of the above so that we can use I for the next loop (it's traditional to use the letter I for this because in FORTRAN it represents the first integer variable).

2070 F = 0



routines and initialises everything.



1) that a swap has been made on the latest pass.

2080 FOR I = 2 TO SP

2090 IF A(I, S) = A(I - 1, S) THEN 2190

If the two items don't need to be swapped, line 2090 skips the next bit.

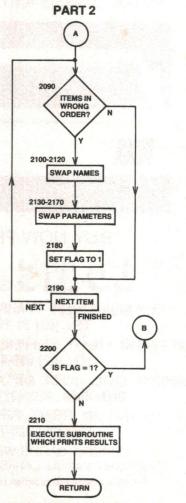
2100 T\$ = A\$(I)

2110 A\$(I) = A\$(I - 1)

2120 A\$(I-1) = T\$

2130 FORJ = 1 TO NV

2140 T = A(I, J)



PRINT "NOT ENOUG ITEMS"

RETURN

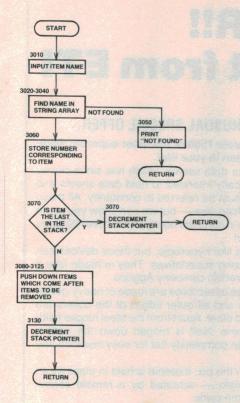


Figure 6. Removing an item. This is fairly similar to Figure 2 in parts.

2150 A(I, J) = A(I-1, J)2160 A(I-1, J) = T

2170 NEXT J 2180 F = 1

swaps the two items (including their parameters). T\$ and T are temporary stores. F is set to 1 to show that a change has been made in the current pass.

2190 NEXTI tests the next pair.

2200 IF F = 1 THEN 2070

repeats the whole thing if there have been any swaps. Hopefully, after enough 'ripples', the program will do one pass without finding any items in the wrong order.

2210 GOSUB 4000 prints the results.

2220 RETURN

3000 REM REMOVE

3010 INPUT "WHICH ITEM"; S\$

3020 FOR I = 1 TO SP

3030 IF A\$(I) = S\$ THEN 3060

3040 NEXTI

3050 PRINT

"NOT FOUND": RETURN

finds out which item is to be removed. Store I (as in line 2060):

3060 S = I

Check to see if the item is at the end of the stack:

3070 IF S = SP THEN

SP = SP - 1 : RETURN

deals with this case.

The next section is essentially a push-down sort in reverse, the only complication being the moving of the parameter values. It might be instructive to look at the differences between Figures 2 and 6.

3080 FOR I = S to SP

3090 A(I) = A(I+1)

3100 FOR J = 1 TO NV

3110 A(I, J) = A(I + 1, J)

3120 NEXT J

3125 NEXTI

3130 SP = SP - 1

3140 RETURN

The next routine prints out the list. All the string variables are limited to eight characters for format reasons. This routine is only entered via the sort routine

4000 REM PRINT

4020 PRINT

4030 PRINT "NAME".

 $4040 \quad FORI = 1 TO NV$

4050 PRINT LEFT\$ (N\$ (I), 8),

4060 NEXTI

4070 PRINT

4080 FORI = 1 TO SP

4090 PRINT LEFT\$ (A\$ (I), 8),

FORJ = 1 TO NV4095

PRINT A(I, J), 4100

4110 NEXTJ

4120 PRINT

4130 NEXTI

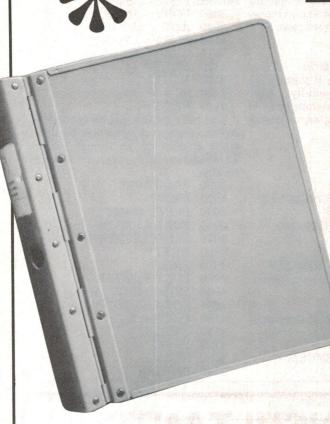
4140 RETURN

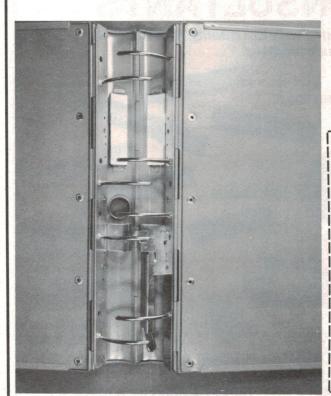
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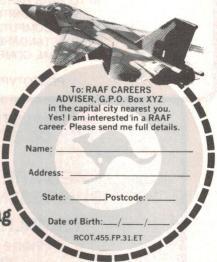
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POKE and PEEK also give access to the display but the speed is not much better, the fastest method of all in BASIC being to PRINT a string containing graphics characters. This method is very successful when small areas of the display are to move, but I still want to see those sine waves rippling across the screen!

The method shown here is a machine code program which sometimes needs to be slowed down to give a viewable display. I shall firstly describe the machine code program itself then show you how to interface such a program to a BASIC language program.

The machine code

This is for your information only; don't worry, you don't have to type in any assembly code to use the graph plotter. All of the references to line numbers in this section are for the assembly code listing. Lines 10-120 are the equivalent of REM statements in BASIC. I include these in my 'library' of source programs because I find assembly code very 'opaque'; that is, the program itself does not suggest how it works. This is also the reason for all the comments down the right hand side of the listing.

The CALL on line 170 is used to get information from the BASIC program. After this call has been made the HL register pair contains a value corresponding to the value V in the BASIC statement: 10 X = USR(V)

Lines 200-260 are mainly concerned with setting up loop parameters, the equivalent of the FOR... NEXT state-

ment. As in any program the input variables need to be tested and the appropriate action taken if they are out of the desired range. This is done on lines 230-240; if the variable is greater than 40 then the loop contents will be skipped and the next variable will be processed. I chose a value of 40 because the screen is 48 graphics characters high and space might be needed for axis and other information. The values in the program will give one free line at the top and three at the bottom. Similarly 'XAXIS' defines the display width as numbers of graphics characters. The maximum is 128, and I chose 120, giving some free space at the screen

If the check on line 240 is not made then values could be input which caused memory locations other than screen memory to be loaded, possibly in the areas of RAM used by the TRS80's housekeeping routines. Most likely you would have to reset the machine to get any more sense out of it!

At this point you need to know how TRS80 graphics are accessed from machine language. In the TRS80 there are two graphics chips, one containing all the information required for the ASCII character set (and more if you know how to get it out), the other really a bit of TTL which switches on graphics blocks at the right instant of time during the screen scan. If bit 7 in the screen memory location being accessed is set at logic "1", then the graphics generator will turn on, otherwise the ASCII generator will be enabled. So we know that we must turn on bit 7 at the required location.

But what is that location? Well, a bit of arithmetic is needed to calculate it and this calculation is what comprises the bulk of the program. Each graphics block corresponds to a byte of memory and is three graphics characters high and two wide. The characters themselves correspond to bits in the memory

A. Lacy

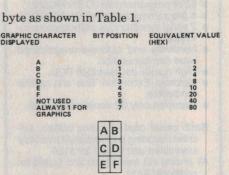


Table 1. This shows the relationship between display memory bytes and the character displayed on the screen.

We must determine the bit to be set as well as the correct location; the procedure used is listed:

1)	Divide the variable by	
	three.	260-290
2)	Save the remainder.	300
3)	Multiply quotient by 64.	350-360
4)	Subtract it from baseline.	410
5)	Get the horizontal	
	position.	420
6)	If odd then add 1 to	
	remainder.	450-460
7)	Subtract position from	
	origin.	500
8)	Convert remainder to a	
	bit position.	520-58
9)	Is it already a graphics	
	location?	590
10)	If not then set bit 7	610
11)	And reset bit 5	620
12)	Put the information on	District P
	the screen.	640
	Check to see if finished.	690
14)	Get the next variable	200

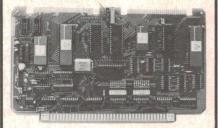
Most of the other operations in the program are concerned with setting up registers prior to the above or with loop counting. In the TRS80, if a machine code routine has been called from BASIC then a RET instruction will return control to the next BASIC statement.

15) And carry on!

The information for the graph plot is

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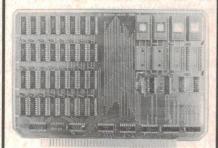
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stored in an integer array as a set of values between 0 and 40. This is rather wasteful of space since each element of the array is contained in two bytes and only the least significant byte is being used. It does make life easier, though, when filling such an array in BASIC.

The code shown is relocatable; that is, it doesn't mind where it is loaded in memory. This is achieved by avoiding references to absolute addresses within the program; in other words, any jumps or branches are specified as forwards or backwards relative to the current position in the program.

The BASIC program

I will describe the program line by line, so treat this section as a set of extended REM statements.

40 GG%(N) is an array where I decided to store the machine code subroutine; it could just as well be put in reserved memory by POKEing the DATA statements. DD%(n,m) is the 'target' array. The program treats this as a list of m arrays, each of single dimensions, and displays them in quick succession, giving the impression of movement.

These DATA values repre-50-100 sent the subroutine.

It sometimes happens that there are several groups of DATA statements in my programs. I always start them with a 255 and end them with a series of 0s. This avoids having to be too precise about numbers of READs. Just laziness really and not necessary here. The first number in line 50, then, is a dummy number; take it out if you are not going to use line 110.

140-160 A way of getting the right bytes in place in the integer array. If you are POKEing the subroutine then you don't need this.

Lets you know something is happening; see line 220. The following lines are included as a simple example to get you started. You will, of course, wish to be more adventurous.

200-230 Now go and have a cup of coffee. "What!", I hear you say. "This was supposed to be a fast graph plotter!" Well, the plotting is fast, but the values to be plotted are still computed in poor old BASIC, so it will take some time to fill the array DD% with 1200 values, especially if complicated functions are used. Line 240 causes the program to wait for you to get back from coffee!

This is it! The first statement on this line is a DISC BASIC feature and it tells the computer where to go to start the machine code subroutine. I have put it just before the USR call because when machine code is stored in array variables it can get shuffled around as the

BASIC program executes, so the entry point needs to be updated before each USR call. The variable used on this line (X9) must have been previously allocated for a similar reason. These problems do not arise when the code is stored in reserved memory, and the DEFUSR statement could go just after the DATA read section to be executed once only. The next statement on this line, USR, passes the location of the start of the array, not the plotting subroutine, so that it knows where to go to get the element values. The USR statement also passes control to the subroutine.

In Level II BASIC the entry point definition is more cumbersome; you will have to POKE values corresponding to the entry point into location 16526D and 16527D.

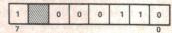
Loops back round to give a continuously moving display.

Using the program

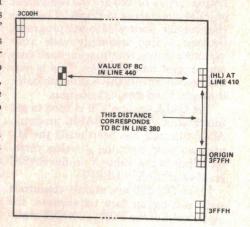
Type in the BASIC listing and RUN it! This will give you an idea of the speed of plotting; each frame seems to appear instantly. Now try various functions on line 220. Remember, you have two independent variables to play with, I2 and I1. Line 260 can appear anywhere in your own program as many times as you wish, so there is plenty of scope for experiment.

For example, a program could be written to alter a few of the target array elements while it is running, maybe under keyboard control. This could give a moving display which also changes over a longer time period.

This diagram shows how the byte position is calculated in the plotter subroutine (line numbers refer to the assembly listing). In this example graphic characters B and C are shown turned on, which means that bits 1, 2 and 7 are logic '1' in that location. The byte will look like this:



This represents a Hex value of 86H (or 134D).



Program Listing

40 DIMGG%(41), DD%(120,10)

50 DATA255,205,127,10,6,120,14,0,126,229,197,254,40,48,60,6,255

60 DATA4,214,3,254,40,56,249,47,104,38,0,203,39,6

70 DATA6,41,16,253,229,193,33,127,63,183,237,66,193,197,203,56

80 DATA56,1,60,72,6,0,183,237,66,71,4,175,55,23

90 DATA16,253,71,126,203,127,32,4,203,255,203,175,176,119,193

100 DATA225,35,35,16,183,201,0,0,0,0,0

110 READ G9:IF G9 < > 255 THEN 110

120 FOR X9=0 TO 41

130 READ Y9: READ Z9

 $140 \quad X8 = 256 \cdot Z9 + Y9$

150 IF X8 > 32768 THEN X8 = X8-65536

60 GG% (X9) = X8

Machine Code

BF1D 06 06

340

LD B,6

FOR LOOP 2

010 :**********GRAFIC 1.4 020 ; GRAPH PLOTTER 030 ; THIS PROG INTENDED FOR USE AS A RELOCATABLE 040 ;USR CALL FOM BASIC. IT WILL RESPOND TO 050;0<=A<40, VALUES OUTSIDE THIS RANGE WILL 060 ; NOT CAUSE A CRASH BUT WILL BE IGNORED 070 ;HL MUST POINT TO THE FIRST ELEMENT OF 080 ; A 120 ELEMENT INTEGER ARRAY 090; Y = 0 CHR POSITION 4 100 ;Y = 119 CHR POS. 63 110 ;X = 0 IS ON LINE 13 120 ; X = 39 IS ON LINE 2 BF00 130 ORG OBFOOH ; SOMEWHERE TO ASSEMBLE IT 0A7F 140 GETHL EQU 0A7FH GET USR ARG INTO HI 0078 150 XAXIS EQU 120 NO. HORIZONTAL POSITIONS 3F7F 160 ORIGIN EQU 3F7FH :BYTF DISPLACEMENTS 161 CALCULATED FROM HERE BF00 CD7F 0A 170 START CALL GETHL ;HL POINTS TO ARRAY(0) BF03 06 78 180 LD B.XAXIS BF05 0E 00 190 LD C,0 FOR SBC LATER BF07 7E 200 LOOPO LD A,(HL) GET ARRAY BYTE INTO A BF08 E5 210 PUSH HL SAVE FOR NEXT TIME BF09 C5 220 PUSH BC SAVE IT FOR LOOP COUNT BFOA FE 28 230 CP 40 ;ARG>40 OUT OF RANGE? BF0C 30 3C 240 JR NC, LOOP4 ; YES SO SKIP THIS ONF BFOE 06 FF LD B, OFFH ; - 1 INTO B BF10 04 260 LOOP1 INC B BF11 D6 03 SUB 3 SUCCESIVE SUBTRACT BF13 FE 28 280 CP 40 DIV BY 3 ROUTINE BF15 38 F9 ON EXIT 290 JR C, LOOP1 B = QUOTIENT BF17 2F ;TO MAKE 2> = A> = 0 300 CPL 301 A CONTAINS 'REMAINDER' BF18 68 310 LD L,B SET UP FOR LOOP2 ;PREPARE FOR *64 BF19 26 00 320 LD H,O BF1B CB27 330 SLA A

170 NEXT X9

180 **REM END OF DATA READ

190 CLS:PRINT@512,"DATA READ COMPLETE, FILLING ARRAY

200 FOR I1 = 0 TO 10

210 FOR I2=0 TO 120

220 DD% (I2,I1) = SIN(I2/20 + I1/1.57)*19 + 20

230 NEXT 12,11

240 CLS:INPUT"PRESS ENTER FOR DISPLAY";D

250 FOR I2 = 1 TO 10

260 DEF USR3 = VARPTR(GG%(0)):X9 = USR3(VARPTR(DD%(0,

12)))

270 FOR X = 1 TO 50:NEXT:REM**IF YOU WANT TO SLOW IT

DOWN!

280 CLS

290 NEXT 12

300 GOTO 250

BF1F	29	350 LOOP2	ADD HL,HL	MULT. BY REPEATED
BF20	10 FD	360	DJNZ LOOP2	ADDITION
BF22	E5	370	PUSH HL	;2[6*HL
BF23	C1	380	POP BC	:HL INTO BC
BF24	21 7F 3F			:CALCLATE SCREEN
	2	000	EDITE, OTTIGIT	ADRESS
BF27	B7	400	ORA	CLEAR CARRY FLAG
CORP CAREAGO	ED 42	410	SBC HL,BC	GET VERT POS.
BF2A		420	POP BC	GET AXIS COUNT
THE RESERVE TO SERVE THE PARTY OF THE PARTY	C5	430	PUSH BC	;SAVE BC FOR DJNZ
0120	CS	430	FUSH BC	LATER
BE2C	CB 38	440	SRL B	:B/2 FOR X-AXIS POS
	38 01	450	JR C,LOOP3	ODD OR EVEN?
BF30		460	INC A	;PIXEL ALIGN
BF31				
	48	470 LOOP3	TOWNS THE RESIDENCE AND ADDRESS OF THE PERSON.	FOR 16 BIT SBC
BF32	06 00	480	LD B,0	GET IT RIGHT WAY
DEGA	0.7	400	00.4	ROUND
BF34	B7	490	OR A	CLEAR CARRY FLAG
	ED 42	500	SBC HL,BC	FOR HORIZ, POSITION
BF37	47	510	LD B,A	;FOR LOOP5
BF38	04	520	INC B	BECAUSE DJNZ DECS
				BEACH
		521		;PASS IN LOOP5
BF39		530	XOR A	XOR WILL ZERO A
BF3A	37	540	SCF	;PUT 1 IN CARRY TO
				SHIFT
		541		;INTO A AT LOOP5
BF3B	1/	550 LOOP5	RLA	, MOVE THE BITS TO
				THE
		551		;CORRECT PIXEL
				POSITIONS
BF3C	10 FD	560	DJNZ LOOP5	THIS MANY SHIFTS
				NEEDED
BF3E		570	LD B,A	;PUT RESULT IN B
BF3F		580	LD A,(HL)	GET DISPLAYED BYTE
	CB 7F	590	BIT 7,A	;IS IT GRAPHICS?
BF42	20 04	600	JR NZ, SET	;IF SO SET IT
BF44	CBFF	610	SET 7,A	;IF NOT MAKE IT
				GRAPHICS
BF46	CBAF	620	RES 5,A	;NON-GRAPHICS
5540	50	000 057	000	SPACE = 20H
BF48	B0	630 SET	OR B	PUT THE EXTRA BITS
5546		0.10		IN THEM
BF49	77	640	LD (HL),A	;DISPLAY THEM
BF4A	CI	650 LOOP4	LOL RC	RESTORE AXIS
0540	C1	000	DOD III	COUNTER
BF4B	AND THE PERSON NAMED IN COLUMN	660	POP HL	FOR ARRAY COUNT
BF4C	23	670	INC HL	;TWICE BECAUSE 1
DEAD	22	600	INC UI	INTEGER :ARRAY
BF4D	23	680	INC HL	ELEMENT = 2BYTES
BF4E	10 B7	690	DJNZ LOOPO	:MORE X-AXIS?
BF50	C9	700	RET	BACK TO BASIC
DF30	Co	700		, DACK TO BASIC

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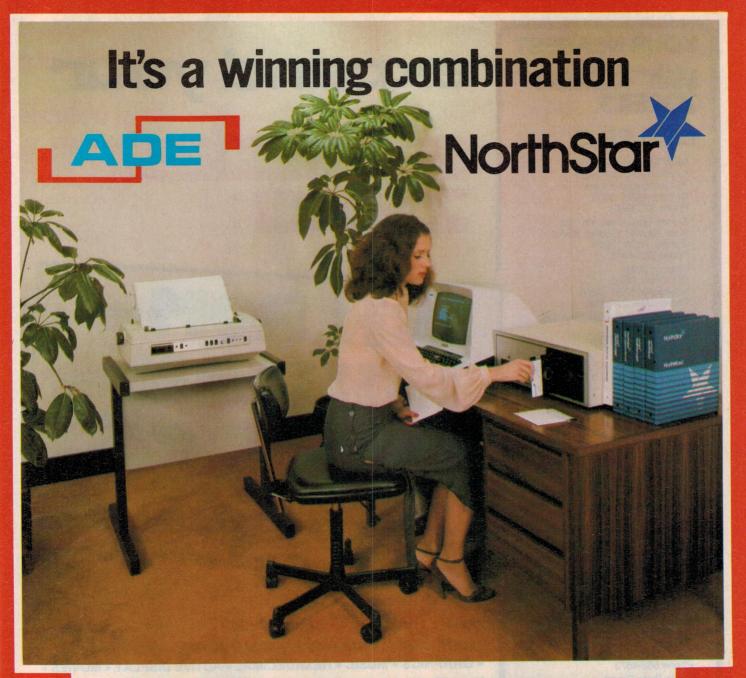
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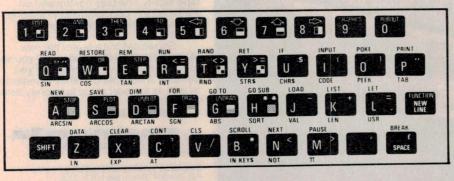
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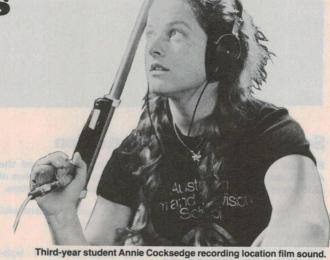
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have solved these problems by with negative feedback, thus eliminating both steady-state distortion (harmonic distortion, switching and lation) and transient distortion is also said to minimise distortion.

The feedforward technique ac- (including transient intermodulation distortion — TIM).

The amps feature a built-in preamp for moving coil cartridges, and a simple construction (only a phono equaliser and the power amp) that avoids sound-muddling capacitors between equaliser out-Sansui engineers are claimed to put and speaker systems. The equaliser is of the high-gain combining feedforward technique dc-servo construction which prevents quasi-dc components from causing instability. The new DD/DC (Diamond Differential dc) construccrossover distortion and intermodution in the AU-D11's equaliser amp

Selectavision under way, stereo TV to come, says RCA

RCA began shipping its Selectavision videodisc players to US distributors early in March and say that they're developing a second-generation system that will deliver stereo sound.

videodisc system with stereo capa- introduction of the initial system. bilities is the production of a television receiver that can pick up and play stereo signals, according to Jack Sauter, group vice-president in charge of consumer electronics.

Referring to the videodisc customer, he noted, "Stereo will build in popularity. Our task is to build a television set that will do something with it.

"Nobody has a stereo television set. We're going to build television sets that will have that capability in '82," he said.

Mr. Sauter said the company has not set a price on the stereo videodisc system, and has not set a formal introduction date for it, other

Among its efforts in developing a than "12 to 18 months" after the

The monaural system, marketed beginning March 22 at a suggested retail price of US\$499.95, will not be upgradeable into a stereo system, he noted. RCA is planning to produce and sell 200 000 units by the end of this year.

Industry sources said the company is testing the market with the monaural system, and plans to make a larger splash with a stereo system in the event of market acceptance of the videodisc player.

The Philips-type laser disc player. currently sold by Pioneer and Magnavox, offers an audio hookup into a stereo system and retails for US\$750.

First trade audio exhibition

The first trade audio exhibition, arranged by the Australian section of the Association of Sound and Communications Engineers, was held recently at the Lantern Restaurant, Sydney, and proved to be a great success.

The first of its kind for members of the public address industry, it featured current and new products in Sydney:

- Audio Engineers Pty Ltd mixers, amplifiers and speaker enclosures.
- Audio Telex Communications Pty Ltd - comprehensive display included public address control racks, the new range of DI amplifiers. Atlas horns and speakers, Turner and Asiatic microphones, headsets and sound column reproducers.
- Amalgamated Wireless (Aust.) Ltd — had the largest exhibit, which included AWA amplifiers, Toa (02)668-9889.

amplifiers, microphones and horn speakers, AKG microphones, Toa radio microphones and loudhailers.

• Freedman Electronics Pty Ltd from four major sound companies had a large display of auditorium sound reinforcement equipment including Freedman speaker enshowed a wide range of Shure closures, Carlsboro 16-channel products including microphones, mixers, power amplifiers, graphic equalisers, echo and reverberation units plus a wide range of stage microphones and stands.

> The Association of Sound and Communications Engineers is a world-wide organisation catering for members of the audio industry. Further information maybe obtained from the Australian representative, Mr. E.C. Moore, 62 Tasman St, Kurnell NSW 2231.

Sanyo takes no chances with videodisc

Sanyo recently announced that it would manufacture all three formats of videodisc equipment — VHD, RCA and Philips VLP — "due to the company's strong orientation towards export markets".

The VHD format is generally favoured in Japan, so Sanyo will manufacture in this format for the home market. Production is planned to commence between June and August of this year.

Sanyo Japan will also start production of RCA format disc players for the US market around March or April of this month, and plans are also under way to extend manufacturing operations for RCA disc players to Sanyo's TV factory in Arkansas, USA, at a later stage.

A production date for Philips optical laser players, primarily intended for the European market, will be postponed until the most popular European disc format becomes more clearly defined.

For further information contact Mr. Bruce Johnson, Sanyo Australia Pty Ltd, 225 Miller St, North Sydney NSW 2060. (02)436-1122.

New Sanyo dictaphone

The Sanyo M1001 dictaphone features 600 mW output, one-touch recording, quick recording and review, a conveniently located pause control, a sensitive inbuilt microphone, a LED indicator that monitors recording input, and uses standard compact cassettes.

It measures only 100 mm x 157 mm x 41 mm and weighs 600 g. Other facilities include a remote microphone input, earphone socket and an external power supply. It comes with a handy carry strap, and normal power is provided by four AA-size batteries. Recommended retail price is \$65.

For further information contact Mr. R. Hopwood, Sanyo Australia Pty Ltd, 225 Miller St, North Sydney NSW 2060. (02) 436-1122.





Ooh, Mac — what a Monster!

Said the actress to the Bishop as he flashed his hi-fi system she'd spotted his Monster speaker cable of course!

drive to the loudspeakers.

Constructed using many strands such cases. of fine copper wire, the cable is plastic encapsulated in a figure-8 format designed to have good flexias 3 milliohms per foot, that's 9.8 milliohms per metre), very low claimed) and low series inductance (0.75 uH per metre claimed). The installing the cable.

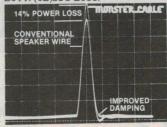
The manufacturers claim that Monster Cable reduces power loss compared to conventional speaker cables, improving damping factor, and their brochure shows a sinesquare pulse test to illustrate the comparison, showing a 14% improvement (about 0.6 dB).

In addition, the manufacturers claim improved amplifier performance can be obtained in some circumstances where the high

Monster Cable (TM) is a specially- capacitance factor of other cables constructed speaker connecting causes instability in some amplicable claimed to improve the sound fiers. The Monster Cable brochure is of a system and provide more power also illustrated with a high frequency pulse test showing what happens in

Monster Cable is claimed to add ... depth and clarity to mid/high frequencies ..." due to its conbility, very low dc resistance (given struction, as well as giving "... tighter, cleaner bass . .

Monster Cable is obtainable in capacitance (69 pF per metre precut lengths or can be cut to length. A variety of terminations are available, such as spade, big banana terminations are colour coded so tips, gold banana tips and gold pin that you get correct phasing when tips. The cable is distributed here by Convoy International Pty Ltd, 4 Dowling St, Woolloomooloo NSW 2011. (02)358-2088.



Video recorder and TV in one

An interesting variation on the video cassette theme is now being marketed by Sharp in Japan.

cabinet into which you pop your video cassette when you want to first off the rank. play or record.

recorder reduces the cost by about

They have released an 18-inch 30%, say Sharp, but don't expect to television with a built-in video see the unit flood on to the market cassette, the unique console system over here; Sharp say they are merely being made possible with the Sharp trying it out in Japan to gauge refront-loading cassette system. The action and that it may be a year or television has a slot in the front more before they look at the export market, and then America would be

However, the concept is interest-Combining the TV and the video ing and worth keeping an eye on.

Dennis Lingane

N.V. Dale Electronics announce imports

Janszen electrostatic speakers, Cizek speakers and Cart Align, a cartridge alignment tool, will soon be available in Australia from N.V. Dale Electronics.

been appointed sole distributors for Infinity styli and record care products, ducts, and J.E. Sugden of the UK Soundguard, Vortec and GLI and products. Systems 80 furniture modules.

Telarc and Delos audiophile Vic. 3056. (03)387-6170.

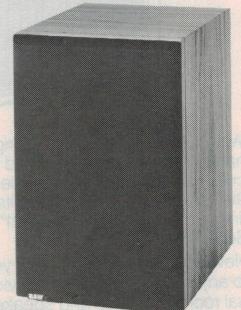
records, Proprius, American Gramo-N.V. Dale Electronics have also phone, Opus and Trend records, speakers, QED audio Victoria, South Australia and Tas- accessories, Audio Pro speakers mania for the following brands: and subwoofers, L & D Speakers, Bozak speakers, Stanton cartridges, Audionics, Berkshire audio pro-

You can find N.V. Dale Elec-N.V. Dale Electronics also handle tronics at 274 Victoria St, Brunswick

B&W DM12

THE UNOBTRUSIVE REVOLUTIONARY

We could have built something smaller, but we were determined to build a miniature speaker that rises way above the limitations hitherto imposed by a small enclosure. In particular, we wanted to achieve wider response and more realistic sound levels in the low frequency range. We also wanted safe power-handling capacity. Thanks to our unique resources in computer and laser technology and the sheer creativity of our design team, DM12 achieves all this and more.







It's certainly small. Just 355mm (14 in) high x 220mm (83/4 in) wide x 270mm (101/2 in) 106dB.

That's the surprising peak sound pressure level DM12 produces from its 12 litre enclosure.

Monitor standard. Frequency linearity + 2 dB 85 Hz to 20kHz.

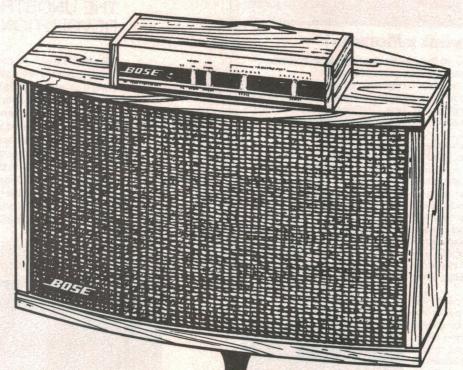
APOC-protected.

B & W's exclusive audio-powered

overload circuit protects against accidental damage or overload.

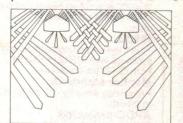
For further information see your B &W Convoy International Pty Ltd 4 Dowling Street Woolloomooloo NSW 2011 dealer or contact Telephone (02) 3582088

Now the Bose® 901® IV Direct/Reflecting® speaker. The evolution of the revolution.



When Bose introduced the original 901° speaker, it was hailed by critics as a revolution. Now the 901° speaker has evolved to its finest form. The new Bose 901° Series IV.

New equalizer controls allow you to adjust this speaker for individual room acoustics and speaker placement. And a new driver



is so advanced in materials and design that Bose has removed the power limitations for home use.

The new Bose 901® IV is durable enough to handle any amplifier, yet can be driven with as little as 10 watts per channel.

You can put together a system with the life-like, spacious sound of the Bose 901® Series IV

Direct/Reflecting®
speaker for a fraction
of what it would cost
to get the same kind
of sound with any
other speaker.



BOSE AUSTRALIA INC., 11 MURIEL AVENUE, RYDALMERE, NSW 2116. TELEPHONE (02) 684-1022, 684-1255.

Sony's inseparable separates.

Sony's new ST-J55 tuner and TA-F55 amplifier come in elegant matching designs. Separately, they're

straight signal processing circuit construction, revolutionary Heat Pipe, and Pulse Power Supply, providing



outstanding. Together, they're out on their own, both in appearance and performance.

The J-55's tuner is frequency synthesized and quartz locked. A neat line of feather-touch switches gives a choice of Memory, Auto, or manual tuning.

The J55's incredible electronic MNOS memory tuning lets you preset your 8 favourite AM/FM stations – including reception adjustments like muting or mode pre-set.

The other half of the team, the 65W F55 Amplifier, features an electronic motor driven volume control,

extremely clean and noise-free sound quality.

The F55 operates with almost any type of MC and MM cartridge; has gold-plated phono jacks, oxygen-free copper wiring, metallized film resistors and polypropylene capacitors.

You won't find better engineering than these. Not even from Sony.

SONY



Optical disc recorders — death blow for present-day turntables?

The new technology of the digital disc 'played' by a laser pick-up with no direct surface contact at all must signal the eventual demise of the conventional record and turntable system. Alan Concannon (Mem. IEEE T.Eng(CEI) MITE MNZEI) reviews the various applications and advantages of this revolutionary technology.

Alan Concannon

GRANDFATHER most probably used a freshly sharpened sliver of bamboo each time he played his record on the gramophone. Then progress introduced the scratchy stainless steel needles, the ones which were supposed to be thrown away after each playing of the old 78s. With the fifties came the plastic record with the microgroove and the sapphire stylus, then finally came the diamond stylus. But the eighties, due to research carried out by Philips Industries and other manufacturers, will see the introduction of the laser beam stylus, which for numerous reasons will quickly cate-

gorise all its predecessors as museum pieces.

Existing hi-fi records and turntables are adversely affected by worn sapphires and diamonds, dust, scratches and many other surface defects. Also, to combat the bogey of scratch, audio engineers had to devise equalisation circuits which allowed treble notes to be recorded over normal strength and played back at reduced volume. This makes the surface noise, which is in the same tenal range, more difficult to hear. But with static, dust and other pollutants gathering around

the stylus, the battle to keep the sound pure is one of constant vigilance.

The new consumer record player

The only really new technology in the future turntable system will be the records, which will become digital compact discs, and the stylus or means of taking the recording from the disc to the amplifier. The new laser pick-up stylus, at least for consumer-standard record players, will work with existing amplifiers and speakers, so initially the

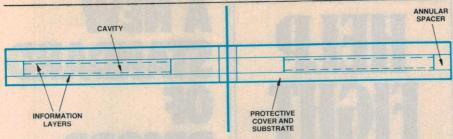


Figure 1. The Philips Air Sandwich or a disc protective system. (Not to scale.)

turntable will be the only hi-fi component to be replaced.

On consumer-standard record players the Philips compact disc (just 115 mm diameter with 60 minutes' playing time) spins on a turntable, but there the similarity ends. Sound reproduction is vastly superior due to the sound information being stored on the disc digitally and read out optically.

Playing the disc is based on the principle of light diffraction, which means that pre-recorded information can be extracted without mechanical contact. The information is stored on the disc in the form of a helical track or microscopic pits, the pick-up head being an optical device using a miniature aluminium gallium arsenide laser. The light reflected back from the metallic layer on the disc contains all the signal information in digital form to reproduce the original recording.

The laser in the pick-up produced today is estimated to last 2000 hours. The proposed units, which Philips originally expected to be on the market during 1980, are no bigger than today's cassette players. The units will provide an output signal for playback through any standard stereo amplifier.

The disc is recorded on one side only and is covered by a metallic layer beneath a transparent protective coating. It is both light in weight and durable, and because the information is not stored on the surface of the disc it is secure against dust, dirt, scratches and general wear. With the disc only 115 mm in diameter, storage creates no problem. Operating the player is de-

scribed as simplicity itself; you select the play, stop, automatic or search mode, and it responds to your requests.

The larger 300 mm commercial optical disc

The new optical disc recorder was based on the technology used in the video disc system for home entertainment, but Philips' designers had a few problems to overcome. They had to develop an inexpensive disc recorder of digital information, which had to have direct read-after-write capability and be able to record in any ordinary enclosure—i.e. not the usual dust-free protected room.

They concluded that an optical disc recording by laser was the answer, and produced a disc that can record as much digital data as 25 magnetic tapes operating at 6250 bits per inch. Due to its protective cover, no processing of the disc is required and its shelf life of ten years is assured with a tellurium-based film.

The system is also error-free, the optical system being designed to check the recorded data instantly and to rerecord at once any part of the disc where irregularities appear. This process was developed experimentally for the Phillips DRAW (Direct Read After Write) information system. The DRAW system records information on a 300 mm disc with 40 000 tracks per side or 1.0 x 10¹⁰ bits per side.

The optical disc

A variety of materials was considered

for the disc substrate and its protection. After taking into account thickness uniformity, strength, optical properties and cost, polymethyl-methacrylate (PMMA, or Plexiglass) was selected as the primary disc material.

A tellurium-based film was chosen for the recording medium, consideration being given to reproducibility, storage, resolution and sensitivity. The sensitivity of tellurium is sufficient to allow at least 10 M bit/second recording with less than 8 mW incident at the film surface. Accelerated aging tests on the tellurium-based film indicated a shelf life of ten years at normal room temperatures.

Protection from scratches, dust, and sticky fingers, etc, is provided by a transparent cover over the disc, this system being known as the Philips Air Sandwich (see Figure 1). The sandwich consists of two discs, each coated with a tellurium layer and separated by ring spacers at the inner and outer radii. The tellurium is placed on to the disc during the assembly process in a clean, dust-free room prior to the protective cover being applied.

The optical system

In the optical system (see Figure 2) the light output from the laser is split into two beams: 90% for recording and 10% for reading or reproducing what is on the record.

The recording beam is encoded with the information signal by a light modulator. The read beam passes two mirrors and a beam splitter, so arranged that the record and read beams are recombined at the objective. The objective focuses the beams on to the information layer inside the disc, the focused power available for recording at the disc's surface being 12 mW.

Since the read beam is slightly off centre to the optical recording axis, the playback spot trails the recording spot by a few microns. This means that the recorded pit is read shortly after writing, making immediate correction



The Sony/Philips laser-read disc system, launched in Japan last October.

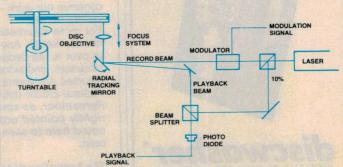
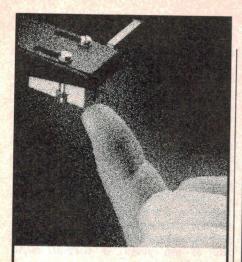


Figure 2. Optical system for the commercial-standard disc recorder.



SC-2 gives your cartridge more than The Finger!

The famous SC-1 stylus brush (standard of the record and hifi industries) now has a synergistic fluid called SC-2.

SC-2 Fluid enhances and speeds cleaning and yet protects diamond adhesives, cartridge mounting polymers and fine-metal cantilevers against the corrosive effects of many other "cleaners."

The Discwasher SC-2 System. Stylus care you can finger as clearly superior.



discwasher PRODUCTS TO CARE FOR YOUR MUSIC

HI-FI DEALERS AND RECORD STORES

HELP FIGHT THE SILENT KILLER

Kidney disease is the silent killer in Australia today. It may be present without apparent symptoms — & hundreds of Australians die of it every year.

But because people can't **see** their kidneys and don't know much about their functions, they miss the vital

early warning signs.

Our kidneys are, in fact, miraculous miniature laboratories containing one to two million filters that help control blood pressure & the important balance of salt & water in our bodies. Yet over 300,000 people consult their doctors each year with kidney complaints. The Australian Kidney Foundation is the only voluntary gift-supported community health organisation solely concerned with fighting kidney disease, the silent killer. The Foundation provides research & education programmes to both the general public and the medical profession. As well as life-giving aid to thousands of ordinary Australians.

We need urgent financial support to continue our work — and we need kidney donors.

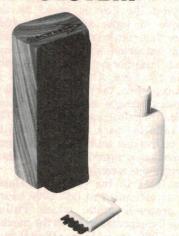
For more information, ring the number below. Any donation of \$2 or over is tax deductible and bequests, endowments and legacies are exempt from State & Federal Estate duties.

Remember, as someone has so rightly pointed out – the life you could help to save could be your own.

The Australian Kidney Foundation, 1 York St., Sydney. Phone 27 1436

A NEW STANDARD OF RECORD CARE

DISCWASHER D4 SYSTEM



NEW D4 FLUID

Inherently more active against record contamination. Inherently safe for record vinyl. Preferentially absorptive formula carries all contamination off the record.

NEW D4 FABRIC

Unique directional fibers preferentially remove fluid and contamination. D4 fabric results in clearly better cleaning, better drying and ultimately residue-free surfaces.

UNMATCHED VALUE

The Discwasher D4 System is enhanced by the durability and aesthetics of the hand-finished walnut handle. Included in the D4 System are the DC-1 Pad Cleaner and new instructions.



AVAILABLE AT LEADING
HI-FI DEALERS AND RECORD STORES.

of errors possible; should any difference occur between the record and read signals they are detected and rerecorded.

Warping of the disc can cause vertical excursion of the information surface by as much as 1 mm. Any misfocus is sensed optically, and the error is minimised by a servo-mechanism focusing system.

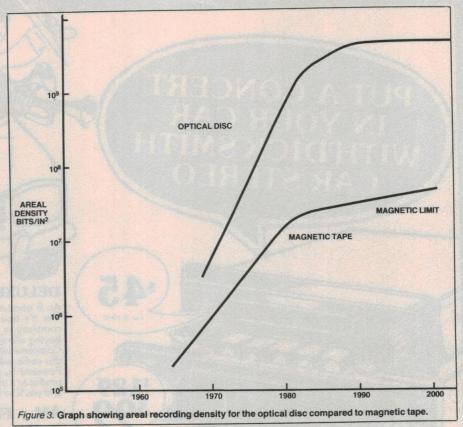
A radial tracking mirror is used to follow the recording tracks during playback. The read spot must follow the tracks to within 0.1×10^{-6} m. Since disc eccentricity on remounting can be as large as 50×10^{-6} m, the radial mirror must be controlled to reduce this error to within the 0.1μ m limits. Mistracking is sensed optically, and the signal controls the radial mirror to reduce the error.

Comparison with magnetic tapes

When a comparison is made between the best commercially available magnetic storage devices and the optical disc system the results are interesting (see Table 1). Using the optical disc, the cost per bit of information stored is considerably cheaper, storage life is greater, access time is reduced, and the hardware cost for information stored lowered. Magnetic tape's main advantage is that it can be erased and rerecorded — the laser optical disc system is permanent and would not be suitable for this type of application.

Briefly, then, the laser optical disc's main advantages are low cost, efficient storage and improved archival properties. The volumetric storage efficiency of the optical disc is also better—approximately one hundred times better than that of magnetic tape's 6250 bits per inch.

The packing density of magnetic re-



cording (see Figure 3) has an ultimate limit somewhere below 10⁸ bits per in², whereas optical recording on metal films has now progressed to this level, and densities above 10⁹ bits per in² are expected as a result of current research.

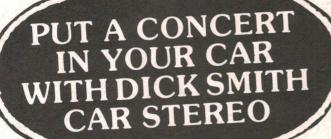
The consumer video disc system maps roughly six bits of information on each pit on the disc. The recording format chosen for the digital disc conservatively assigns only one bit to a recorded pit. The present research and development is aimed at increasing the storage capacity of the disc to about 10¹¹ bits. Endeavours will be made to produce

smaller pits, smaller track spacing, more efficient data encoding and an increase in the number of encoded bits per recorded pit.

Amplifiers and speakers have reached a stage where it is difficult to improve on them in any great degree. But for any class of consumer, in the home or in the radio studio, the record and turntable system has until now been a major weakness. With the advent of the laser optical system, tomorrow should bring a near-perfect and highly durable system to satisfy even the most fastidious music lover.

Table 1. Ch	naracteristics	of magn	etic tape	and	optical discs.	
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Commercial units only **Hardware** Access User **Archival Media cost Device** cost in capacity time in life in per bit in **US dollars Mbytes** ms years cents 6250 bits/in \$28 440 91 45 000 1-2 2.2x10-6 tape IBM 3420-8 **IBM 3850** \$2 400 000 462 500 16 000 5x10-5 1-2 System **Philips** \$10 000 2500 Up to ten years 100-500 $5x10^{-8}$ **Optical Disc** Philips Juke \$200 000 Up to ten years 3000 25x10⁶ $5x10^{-9}$ Box **Philips Optical** \$200 000 125 000 Up to ten years 50-100 1.5x10⁻⁸ Disc Pack







An 8 transistor radio with push button tuning for only \$45? Yes it's true. A similar radio to this one was sold by this company in 1976 for a massive \$79 — our direct import buying allows us to beat inflation!

Complete with all bits and pieces, including a large speaker, this radio will fit most cars in the standard cut-out aperture. Powered by your battery (12V DC negative earth) this radio produces a massive 5 watts output. All you need is an antenna and you'll be listening to your favourite stations in no time at all.

AM + FM STEREO + CASSETTE

The FET front end plus its small size are only two of the outstanding features of this AM/FM cassette stereo. Measuring only 120(d)x180(w)x44(h)mm it will fit into most facia cutouts, the small size has been acomplished by placing the tuner dial in the cassette flap! Indicator lights tell you when you have FM stereo and when you have a tape playing. Ideal for any 12V DC negative earth vehicle. Use with any of the speakers shown below

AUTO REVERSE STEREO

No more troubles with turning the tape over at end of playthis unit automatically plays the other side. If you wish to fast forward or reverse the tape then it is easy - the controls lock down and then pop out at the end of the tape. Ideal for under dash installation the unit works from 12V DC negative earth. The 4 IC and 2 diode construction boasts a healthy 8 watts maximum output - enough power for even the most avid audiophile. Can be used with any of the speakers shown below.



A massive 280 gram magnet, plus 4 ohm impedance and a rating of 20 watts – ideal for those high power sys-tems. Excellent bass and

For top quality sound from the in-built tweeter and bass unit. Handles 20 watts into 4 ohms



Rated at 4 ohms impedance and 8

watts power. Mount on your rear shelf or can be removed from box for

flush mounting.

Cat. A-6980

All speakers are sold and priced as

Cat. A-6985

Flush Mount

Speakers

130mm diameter speakers with a rating of 4 ohms and 10 watts maximum power. Soft padded speaker grille for that touch of luxury. Mount on rear shelf or in doors.

ALL NEW EQUALISER/AMPLIFIER



Boost up your power to 25 watts per channel and at the same time be able to control 7 different frequencies! The slider controls will enable you to set-up the sound that you like thereby compensating for any deficiencies in the cars acoustics. One pair or two pairs of speakers may be used and if using two pairs there is a fader between the front and rear speakers. Use with 12V DC negative earth.

SEE THE OTHER DICK SMITH ADS IN THIS MAGAZINE FOR STORE ADDRESSES & PHONE NUMBERS



The moving coil replacement from Stanton Magnetics... the revolutionary 980LZS!

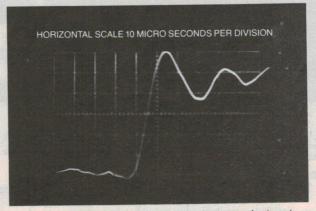


Now from the company to whom the professionals look for setting standards in audio equipment comes a spectacular new cartridge concept. A low impedance pickup that offers all the advantages of a moving magnet cartridge without the disadvantages of the moving coil pickup. At the same time it offers exceedingly fast rise time - less than 10 micro seconds-resulting in dramatic new crispness in sound reproduction - a new "openness" surpassing that of even the best of moving coil designs. The 980LZS incorporates very low dynamic tip mass (0.2 mg.) with extremely high compliance for superb tracking. It tracks the most demanding of the new so called "test" digitally mastered and direct cut recordings with ease and smoothness at 1 gram +1/2 ...

The 980LZS features the famous Stereohedron™ stylus and a lightweight samarium cobalt super magnet. The output can be connected either into the moving coil input of a modern receiver's preamps or can be used with a prepreamp, whose output is fed into the conventional phono input.

For "moving coil" audiophiles the 980LZS offers a new standard of consistency and reliability while maintaining all the sound characteristics even the most critical moving coil advocates demand. For moving magnet advocates the 980LZS provides one more level of sound experience while maintaining all the great sound characteristics of cleanliness and frequency response long associated with fine moving magnet assemblies

From Stanton... The Choice of The Professionals.



Actual unretouched oscilloscope photograph showing rise time of 980LZS using CBS STR112 record.



IMPORTERS AND EXPORTERS OF AUDIO EQUIPMENT

SOUNDEX EM

WA Head Office: 156 Railway Parade, Leaderville, 6007. Phone (09) 381-2930. NSW Office: 7 Jordan Road, Wahroonga, 2076. Phone (02) 487-2543.

The second of th

Sanyo Plus 75 receiver — high-powered sound!

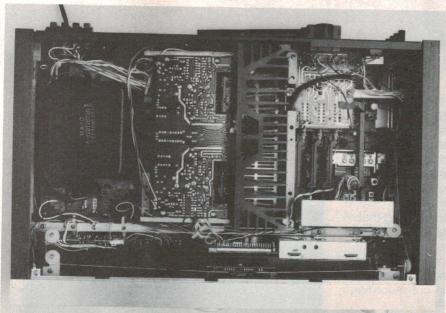
Sanyo's new Plus 75 AM/FM stereo receiver is aimed at the higher end of the market, and according to Louis Challis its biggest 'plus' is that you can play it as loud as if the rock band were in the room with you.

Sanyo have changed their Australian image in the last two years from one of medium to bottom-of-the-line products to a new image of total capability. The Plus series of products has been designed to fill the gap at the top end of the spectrum with a range of quality products designed to compete effective-

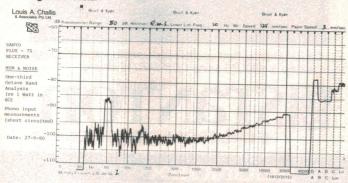
ly and commercially with the other best known Japanese brands.

Features

The Plus 75 AM/FM Stereo Receiver is an excellent example of a well-designed receiver offering the frills, gimmicks and performance that the well-heeled



Internal view of the receiver. Note the fully encapsulated power transformer at the left and the heatsink dividing the chassis.



Louis A. Challis

audio faddist has been looking for.

The receiver is in keeping with the basic styling that Sanyo have been using for the last four years, with the brushed satin aluminium escutcheon divided into two halves; the top half features a perspex insert escutcheon behind which is a long slide rule-type dial. The top section of this features FM and AM frequency designations whilst the centre section features a blue digital display indicating AM or FM.

The lower section of the upper escutcheon features two seven-segment green LEDs to indicate power output, whilst more LEDs separately indicate the selection of AM, FM, phono, auxiliary, FM stereo and FM quartz-locked tuning. A five-segment array of red LEDs displays the incoming signal sensitivity.

The bottom half of the escutcheon is arranged with two levels of controls — of which there are so many that they tend to be a little crowded. A 'busy' appearance is presented, and the whole effect is a little overpowering. The main controls are in the lowest row and consist of push buttons for speakers A and/or B; bass, mid-range and treble controls; a volume control with mechanical indents; a balance control; push buttons for loudness; 20 dB muting; stereo/mono; FM muting and



AM/FM phono or auxiliary. The last control on the right-hand side is the tuning control, which is smooth and well counterbalanced.

The minor controls are located logically above the respective major controls. At the left-hand side is a power display range switch which allows the power display to be increased in sensitivity by ten to one so that the maximum setting is 15 watts instead of 150 watts. This would be the position in which most people would be expected to use the receiver. Above the bass control are three push buttons for corner frequencies of 100 Hz, 200 Hz and 400 Hz for the bass boost or cut. Above the midrange control is a tone control defeat button, whilst above the treble control are switches for selecting corner frequencies of 2.5 kHz, 5 kHz and 10 kHz for the treble boost and cut. Above the loudness control are subsonic and high frequency filters. Above the muting mode and FM muting switches are monitor switches for selecting source or tape for recorders 1 and 2 and dubbing on and off, respectively. Above the mode switch is a quartz-locked frequency tuning selector and a moving magnet or moving coil cartridge input selector switch.

At the rear of the receiver are four terminals for connecting AM and FM external aerials, and a 50 μ s and 75 μ s de-emphasis switch to suit Australian and American conditions. Inputs are provided for one cartridge, one

auxiliary, and two tape recorders. Linked inputs and outputs are provided between the preamplifier out and the main amplifier in, whilst connection of the speakers is achieved through four pairs of well-designed, silver-plated, spring-loaded terminals. The AM antenna is a clever ball and socket jointed fixture providing 60° arc movement in the horizontal plane.

The inside of the amplifier is welldesigned, the most significant features being the subdivision of the circuitry with a main power supply stage on the left including a fully encapsulated power transformer. Two well-designed power amplifier output stages are located adjacent to it, flanked in the centre by a very large and unusual heatsink. This incorporates a thermal cutout circuit which in the event of excessive temperatures switches off the output stage. To the right of this is the AM/FM tuner stage incorporating a tuned RF stage for FM and separate shielded cans for the digital tuning stage and for the SQ quartz-locked tuning stage. The digital display which traverses the slide rule dial at the front of the unit incorporates a flat ribbon cable to carry the signals through to the dial, which, whilst unusual, is nonetheless very practical. Four additional printed circuits are located behind the power level, signal level and preamplifier tuning stages at the front of the receiver.

The unit features a strong steel

chassis and simulated veneer plywood sides in what is a relatively complex but well-designed system. The power supply fuses are located inside the unit, and although the manufacturer does not specifically state "no user serviceable parts inside", those words could well have been presented on the rear panel.

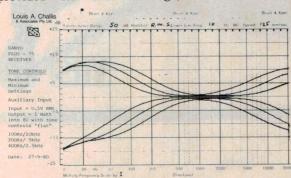
On test

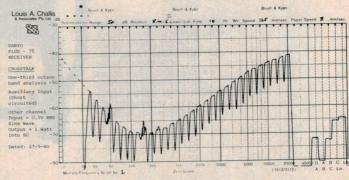
The test performance of the receiver stage provided very good results. With the tone controls defeated the frequency response is 10 Hz to 100 kHz; with the tone controls centred this extends from 9 Hz to 54 kHz. The moving magnet cartridge sensitivity is $620~\mu V$.

At rated power of 75 watts into both channels the distortions are typically .014% at 100 Hz, .008% at 1 kHz and .023% at 6.3 kHz. At 1 watt level these figures are still excellent at .02%, .007% and .009%. The transient intermodulation distortion is less than 0.1%.

Hum and noise levels are particularly good, being $-86 \, \mathrm{dB(A)}$ for the auxiliary input, $-86 \, \mathrm{dB(A)}$ for the moving magnet phono input and $-76 \, \mathrm{dB(A)}$ for the moving coil input. The dynamic head room at rated output is $1.5 \, \mathrm{dB}$, whilst the performance on the transient overload recovery test is excellent.

The most unusual feature of this amplifier is the provision of variable corner frequencies in the tone control circuits, which is one feature I consider well worthwhile and a positive bonus. The addition of the mid-range control is advantageous and in general terms





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HITACHI VERS

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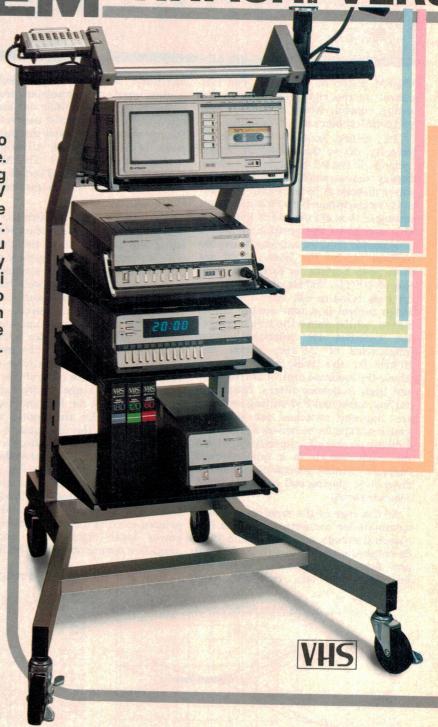
VT-7000 Portable Video Recorder

Still picture & frame advance
 Full remote control
 VT-TU70 Video Tuner/Timer

Soft-touch tuning
 10 day/1 program preset with daily repeat recording

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The VT-7000 Portable Video System. Let your nearest Hitachi dealer show you the fun.



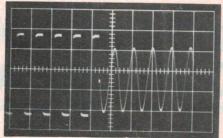
review

adds to the flexibility of the unit. Overall, the audio section of this receiver provides impeccable performance.

The FM performance on mono features a 21 dBf sensitivity for 26 dB signal-to-noise ratio, but surprisingly features a 26 dBf stereo sensitivity for 46 dB signal-to-noise ratio. The measured ultimate signal-to-noise ratio is 75 dB on mono and 70 dB on stereo. The frequency response in the FM mode is particularly flat, extending from 17 Hz to 17 kHz at -3 dB. The total harmonic distortion on stereo at 1 kHz is +.3%, whilst the spurious response rejection and image rejection ratios are both better than 70 dB.

Although of generally conventional design, the FM stage offers exceptionally good reception with either a simple folded dipole antenna or with a commercial external stacked Yagi array, correctly aligned to receive the incoming signals. The incorporation of the RF stage certainly provided very good performance, bettered by very few receivers.

By contrast the AM response at -6 dB bandwidth is 40 Hz to 1.8 kHz. This must rate as one of the narrowest selectivities I have seen and is very open to criticism because of the good and



Transient overload recovery test (IHF-A-202) 1 ms/div.

10 dB overload re rated power into 8 ohms — both channels driven. Overload duration: 20 ms; repetition rate: 512 ms.

useful AM broadcast quality in this country. The AM sensitivity is $300~\mu\text{V/m}$ and ranks as only a moderate performance, suitable for local reception only, not for long-range reception.

In practical use in my home, with two sets of loudspeakers connected (a set of Fisher monitor speakers in parallel with a set of Quads and subwoofer), the system was able to reproduce rock music with healthy outputs in excess of 110 dB peak with no sign of significant distortion but with plenty of movement of the windows and doors. The Plus 75 is readily capable of producing music powers of up to 75 watts on a continuous basis without thermal distress and

without significant distortion. This is a kind of power level which ten years ago might have been considered unnecessarily excessive, but perhaps the general acceptance of rock music and rock music power levels has changed our style of music and consequent needs in equipment.

The Sanyo Plus 75 is a receiver which can form the basis of a home music centre, background music system for a restaurant or school, monitor system for a radio station, or even meet the requirements of people who like to listen to their music at 'realistic' sound levels

SANYO PLUS 75 RECEIVER

Dimensions: 480 mm wide x 355 mm deep x

Weight: 135 mm high

Manufactured by: Sanyo Electric Co Ltd in Osaka, Japan

Price: \$490 Distributed by: Sanyo Australia

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FREQUENCY RESPONSE:			Tone Controls Defeate
(-3dB re 1 Watt, 0.5V Input to Aux)	Left 10Hz Right 10Hz	to 53kHz to 52kHz	
	Tone Control	s Centred	Turnover Frequencies
	Left 9.0Hz Left 8.5Hz Right 8.0Hz Right 8.0Hz	to 56kHz to 55kHz	100Hz/10kHz 400Hz/25kHz 100Hz/10kHz 400Hz/2.5kHz
SENSITIVITY:		Left	<u> Ki ght</u>
(for 1 Watt in 8Ω)	ALK	13.5mV	14.0 mV
	TUNER	-mV	-mV
	TAPE	14.5mV	1.5mV
	PHONO M/M	620 μν	900thn
	PHU O MYC	58 μν	58 μν
	OVERLUAD MYM	180mV	180mW
	OVERLOAD MYNI	20mV	19mV
NPUT IMPEDANCE:		Left	Right
	ALK	47 kΩ	48 k Ω
	TUNER		
	TAPE	47kΩ	48kΩ
	PHONO MI/M	47kΩ	47kΩ
	PHONO MYC	100kΩ	100kΩ

HAIN PAUC DISTRATIO				
HARMONIC DISTORTION:				
(A) (At Rated power o into 8 Ω =	f 75 Watts 26.5 Volt	s)		
		100Hz	IkHz	6.3krlz
	2nd	-78.5	-83.6	-78.9d3
	3rd	-83.7	-86.4	82.4dB
	4th	-90.2	-96.7	-76.7dB
	5th	-97.5	-99.3	-dB
	THD	0.014%	0.008%	0.023%%
(B) (At 1 Watt into 8 Ω	The second			
	1.00	100Hz	<u>IkHz</u>	6.3kHz
	2nd	-76.2	-83.2	-83.2dd
	3rd	-78.8		-90.1dB
	4th	-88.1		-86.4dB
	5th	-87.3	The state of the s	-dB
	THD	0.02	0.007	0.009%
TRANSIENT INTERNODULATION DIS	TORTION: 0.	1%		
(3.15kHz square wave and 15kHz sine wave mixed 4:1)				
NOISE & HUM LEVELS:				
re I Watt into 8 Ω)	ALK	-83 dB (Lin)	-86 ds(A)	
(with volume control	PHIND	/.) -79du (Lin)	-86 dis(A)	
set for I Watt output with,	Prov M	/C -63d3(Lin)	-76db(A)	
0.5V input	(Aux)			
SmV input	(Phono w/M)			
0.5mV input	(Phono AVC)			
WAXIMM CUTPUT POWER AT CLIPPING POINT:				
(IHF -A - 202) (20mS burst repeated at 500mS				

Not Just Speaker Wire



Conventional speaker wire limits the performance of your sound system by decreasing power output, restricting dynamic range, and reducing clarity and definition. You can significantly improve the performance of your audio system by switching from your present speaker wire to Monster Cable.

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Wider dynamic range.

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SOURCE review

Marantz' 'Esotec' SM1000 stereo amp

Marantz have recently been marketing a range of amplifiers, record players, cassette decks and receivers which have been designed to be the ultimate in their class. The majority of these units have "mind blowing" characteristics including massive weights, unbelievable output powers and gimmicks calculated to attract the well heeled audiophile for whom money is no object.

WITH THE RELEASE of their "Esotec Series" of products Marantz have finally put their act together and offered a series of quasi-compatible components whose general technical features, and most particularly their individual costs, truly justify the title "esoteric".

Of all of the components listed in their latest brochure, the SM1000 Power Amplifier is the biggest. This amplifier is also the heaviest that we have ever reviewed and at 43 kg is one of the heaviest components that we have ever handled in the laboratory.

Perfection?

Marantz claim that in developing their Esotec Series of products they have striven to achieve the ideal power amplifier and naturally claim that they have been successful.

They examined the characteristics of even and odd order harmonics in terms of the total harmonic distortion and make the claim that:-

- Even order harmonic distortion is produced by a system whose transfer mechanisms are asymmetric in nature.
- 2. Odd-order harmonic distortion is produced by a system whose transfer mechanism is symmetric in nature.
- 3. Symmetric transfer functions generate intermodulation pro-

ducts that *lie above* the two or more trequencies that are intermodulating.

 Asymmetric transfer functions generate intermodulation products that lie below the two or more frequencies that are intermodulating.

Since intermodulation products are non-harmonic they are far more offensive to the ear than simple harmonic distortion. Herein lies the rub. Should the designer reduce the odd order harmonic distortion and favour the even order, steady state distortion? Were the mechanisms of high frequency THD adequately investigated? These are the kind of questions we had in mind when we reviewed the amp.

Features

The SM1000 Amplifier is a particularly attractive piece of hardware. The front escutcheon is constructed from goldenhued brushed satin aluminium with two very large illuminated VU type meters set in the front panel. These are calibrated in "power into 8 ohms" over the range .01 to 400 watts and from +3 to -50 dB re 400 watts into 8 ohms. The bottom of the front panel features a hinged, drop down cover behind which are two pushbutton switches for selecting speaker group 1 or 2, two individual

Louis Challis

gain controls for the left and right speaker group 1 or 2, two individual gain controls for the left and right channels, and interlocked pushbutton switches for selecting ac or dc input

The rear of the amplifier features three, rather than the normal two, sets of universal output speaker terminals. The first set is direct-connected whilst the second and third sets are switch controllable from the front panel. The primary input connections are by means of either a pair of gold plated coaxial sockets or a pair of XLR recessed locking sockets to accept professional "Canon type" plugs.

The side panels, front panel and chassis construction is built "like the proverbial battle ship" and the top panel is slotted for improved ventilation. On the two sides of the unit there are expanded mesh grilles for an air intake and discharge respectively for a cooling fan system which forms part of the power output stage. The top panel features a small separate removable cover to provide access to an internal mesh pre-filter assembly.

The inside of the amplifier is, as the manufacturers claim, built to exacting professional standards and features the electronic circuitry constructed in a series of compartmentalised enclosures that stretch from the front panel to the rear, utilising all the available space.

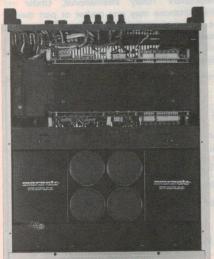
In the foremost compartment are the preamplifier stages and the meter amplifier circuitry, which provides each meter with a direct reading 53 dB range. The meters are sensibly



The controls hidden behind the drop-down front cover.

calibrated in 1-2-5 steps from .01 watts to 200 watts and then provide two additional steps of 300 and 400 watts into 8 ohms. Immediately behind the metering circuitry and preamplifier stage are two large individual power transformers on opposite sides of the chassis. These flank four very large power supply capacitors of 23 000 μF each. Straddling the centre of the amplifier is additional circuitry incorporating the protection controls for the output stage. These control modules simultaneously act as an electrostatic screen between the power transformer stage and the output stage.

At the rear of the amplifier is the power output stage incorporated within what Marantz call their "heat dissipation tunnel". This makes use of a technique developed by NASA for transistor cooling. This technique uses a series of transistors each fitted with its own heat sink for improved thermal dissipation. Each transistor has a "heat dissipator" with twelve long finger-like





controls the maximum temperature variations from case to fins at less than 8°C compared with up to 35°C measurable on more conventional heatsink assemblies. The fan speed is sensibly thermostatically controlled so that it draws less air at low thermal outputs and more air with increasing load. By utilising an outer rotor type motor, the fan noise is effectively minimised (but only at low speeds). The internal circuitry is laid out on large, well-cooled printed circuit cards, the majority of which incorporate plug and socket connections to ease factory construction, and where spare cards or extender boards are available, general servicing as well.

The internal wiring is conventional heavy duty, and laced to MIL type specs. In the fully assembled form, although designed for rack mounting, it should be noted that a residential type racking system would never support the weight, unless placed at the bottom of the rack with additional end support.

The objective testing of the unit supported the claims of the manufacturer as the frequency response was flat to within 0.1 dB from 10 Hz to 20 kHz. In the dc mode the frequency response extends from dc to beyond 150 kHz. The power output claims are modest for the unit is readily capable of producing 625 watts into an 8 ohm load with both channels driven and with a 4 ohm load in excess of 700 watts into each channel. At the rated 400 watt level into 8 ohms the total harmonic distortion characteristics are relatively low, being only .01% at 100 Hz, .02% at 1 kHz, and .02% at 6.3 kHz.

At the 1 watt level, and at virtually all powers below 50 watts, the distortions are particularly low, being only 0.008% at 100 Hz, 0.05% at 1 kHz and 0.009% at 6.3 kHz. Because of the claims made in some of Marantz' literature we measured the intermodulation distortion in strict accordance with the C.C.I.R. Two-Tone Test Method.



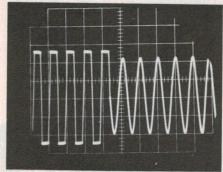


5000 review

These distortions were also related to power working level and at the 100 watt level the difference frequency component (f₁-f₂) was -82.5 dB, at 200 watts -74.6 dB, at 300 watts -73.5 dB and at 400 watts -73.3 dB. This performance is also exemplary and more than good enough for either the well-off audiophile or a professional. The signal to noise ratio is -87 dB unweighted and -92 dB(A), which is good but not the best we have seen.

In practical use, after manhandling into the appointed location, the amplifier proved to be very easy to use. The unit incorporates a six second time delay so that power is not applied to the output circuitry in such a way to cause clicks and pops, nor result in inadvertent damage to expensive speakers. With direct reading of power output on the meters one knows exactly what the amplifier is doing and what dynamic headroom one has before approaching the clipping point of 625 watts output into 8 ohms.

Connected to various sets of high powered speakers like the JBL L110s, Fisher Monitor Speakers, or National Technics SB 10s, or all three systems in parallel to handle the power output, one soon finds what this amplifier is all about. This amplifier is able to handle such loads without complaint like a professional trouper. We made effective



10 dB overload re rated power into 8 ohms — both channels driven. Overload duration: 20 ms; repetition rate: 512 ms. 1 ms/div.

use of the amplifier in a laboratory study to test a series of six loudspeakers, literally to destruction, to determine their power output ratings. It performed admirably in a situation which would have distressed lesser units.

To the ear

In subjective testing we coupled the amplifier to the Esotec Tt1000 Series Turntable, a Carver C4000 Control and an AIWA AD6900 Cassette Deck. With this set-up it provided superlative performance and at power levels which were truly awe inspiring. Sound pressure levels in excess of 110 decibels fall easily within its scope even in rooms with volumes exceeding 500 cubic

metres.

Listening to records like Telarc's 'Peer Gynt Suite' (TEL10048) and Stravinsky's 'Rite of Spring' (TEL10054) provided me with the opportunity to hear what "high fidelity" is really all about, without detracting from source material or adding any audible colouration.

Well?

The Marantz SM1000 amplifier has the capability to provide superlative performance at home, in a laboratory, in a studio or in a rock band with the ease and true panache of a professional. At a selling price of \$4999.00 it is to be expected that many readers will lose interest — but not all — for cost, like quality, is one form of beauty that is seen in the eyes of the beholder.

MARANTZ MODEL SM1000 AMPLIFIER

Dimensions: 483 mm wide by 178 mm high by
550 mm deep.
Weight: 43 kg Price: \$4999
Manufactured in Japan for Marantz, California U.S.A.
Distributed by: Marantz (Aust.) Pty Ltd,

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P.O. Box 604, Brookvale NSW 2100.

S.N. 09 P010	003	ANCE OF MAI	RANTZ MC	DDEL SM1000					
FREQUENCY RESPO	NSE:				(B) (At 1 Watt into	00			
(-3dB re 1 Watt, 0.5V Input to Aux)	DC	150kHz			(B) (AC I WALL INTO	811) <u>100Hz</u>	1kHz	6.3kHz
SENSITIVITY:		H A UKK SA	. 18/19/0			2nd	-83.2	-88.5	-85.6d
(for 1 Watt in 8)	AUX	Let 1/10	mV	Right		3rd	-88.7	-90.0	-83.9di
INPUT IMPEDANCE:	11 11			148mV		4th	-96.2	_	-90.88
MATO I IMI EBANCE.	AUX	<u>Lei</u>		Right		5th			
OUTPUT IMPEDANC	The state of the s	oilliohms (@ 1ki		27k					-dB
C.C.I.R. FI/F2 TES		illioniis (@ 1ki	12)			THD	0.008	0.005	0.009%
$f_1 = 16kHz f_2 = 15kHz$		f ₁ -	fa						
100 Watt Output			5dB		TRANSTENT INTERMODILIANT	ON DIG	mormron.		
200 Watt" 300 Watt"			6dB		TRANSIENT INTERMODULATI	ON DIE	TORTION:	very low less tha	n 0.1%
400 Watt "			5dB		(3.15kHz square wave and	đ			
$f_1 = 21$ kHz $f_2 = 20$ kHz		-/3.	3dB		15kHz sine wave mixed 4	:1)			
100 Watt Output		-73.	// dB						
200 Watt "		-75.			NOISE & HUM LEVELS:				
300 Watt"		-73.3dB			re 1 Watt into 8Ω		AUX	07 10 (5.	
400 Watt " 10 Watt "	-73.6dB					AOA	-87 dB (Lin)	-92 dB(2	
HARMONIC DISTORTION:		-85.	2dB		(with volume control set for 1 Watt output wi	A-1-			
	400				0.5V input (Aux)	tn,			
A) (At Rated power of into 8Ω =	400 Wat		1kHz	6.3kHz	MAXIMUM OUTPUT POWER AT				
	2nd	-79.3	-73.2	-71.5dB	CLIPPING POINT:				
	3rd	-81.7	-86.9	-71.5dB -88.6dB	(1HF-A-202)				
	4th	-93.3	-84.3	-80.8dB	(20ms burst repeated at intervals)	500ms			
	5th		-97.3	-dB	Intervals)			200 V P-P	
	THD.	0.01	0.02	0.028%		**	room =	625 Watts 1.9 dB (re 4	

NOISE REDUCTION SYSTEM

Nakamichi

Obviously a noise reduction system that carries the Nakamichi logo has to offer some real benefits over existing systems.

Features like:-

- Professional two-band design affords 20 dB noise reduction throughout the entire audio band necessary if side effects such as breathing, pumping and sonic coloration are to be eliminated.
- Ultra-fast attack and release times in each band assures accurate transient reproduction and minimal distortion — necessary for faithful reproduction of music.
- Optimum compander ratio for each band provides an additional 5 dB headroom affording a dynamic range improvement of 25 dB!
- 50 dB range peak responding meters assure correct record level monitoring.
- Subsonic and MPX filters eliminate externally induced errors.
- Suitable for any cassette or reel-to-reel recorder.

High-Com II follows Nakamichi's commitment to excellence in the fine art of recording.

SPECIFICATIONS:

Compression ratio Encoding 1:2, Decoding 2:1

Distortion less than 0.1 %

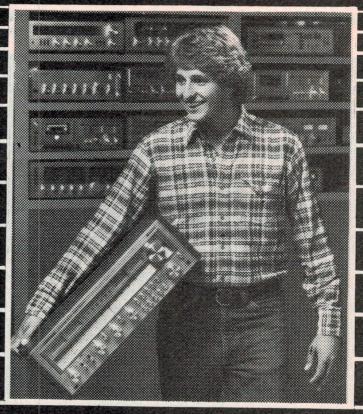
..... 482(w) x 82(h) x 270(d) mm (fitted for 19" rack mount)

High-Com is the trademark of AEG-TELEFUNKEN

For complete information on Nakamichi's High-Com II, see your nearest dealer or write to Convoy International Pty Ltd, 4 Dowling Street, Woolloomooloo 2011.



In recent years over 500,000 music lovers chose a Realistic® receiver over Pioneer® Kenwood® and Technics®



We make it easy for you. Tandy is a retailer as well as a manufacturer. When the store is also the factory, you're apt to get less fiction and more fact. Also, Tandy has more companyowned and operated service stations than almost anyone we κnow of in the audio business.

We've stood the test of time. Could it be we sell so much Realistic because we've been around since 1921—long before those other brands were even a twinkle in daddy's eye? Although hi-fi is typically a younger person's product, maybe the kids prefer to bet their hard-earned bucks on the outfit with the greyest hair.

We have the edge on technology. Then there's this to consider: The same company that puts Realistic out front also builds and markets the world's most popular personal computer, the TRS-80.1M It seems to us that a company smart enough to design equipment as innovative and complex as a

computer is a logical one to choose to build your stereo receiver!

We're close to you. Need another good reason to buy Realistic? Let's take availability. Instead of tens or hundreds of places to find it, we offer over 7,300—wherever you go in Australia, USA, Canada, Belgium, England. France, Germany. Holland, and Japan as well as 31 other countries. Being more available doesn't necessarily make us more lovable. But it goes a long way towards assuring you of being able to get in touch with us when you need advice on adding a tape deck or replacing a lost instruction manual.

We've challenged the rest and won your trust. Incidentally, we're not picking on Kenwood. Pioneer and Technics. The same half-million plus folks who chose Realistic also probably had a crack at buying Sansui." Marantz. Fisher." The important thing is ... they didn't!



Mail Order Department P.O. Box 229, Rydalmere, N.S.W. 2116.





Using ETI PCB Artwork

This method can be used to make negatives of ETI artwork from October negatives of ETI artwork from October 1977 on, provided the reverse of the page is printed in blue. The film used is Scotchcal 8007, which is UV sensitive and can be used under normal subdued light.

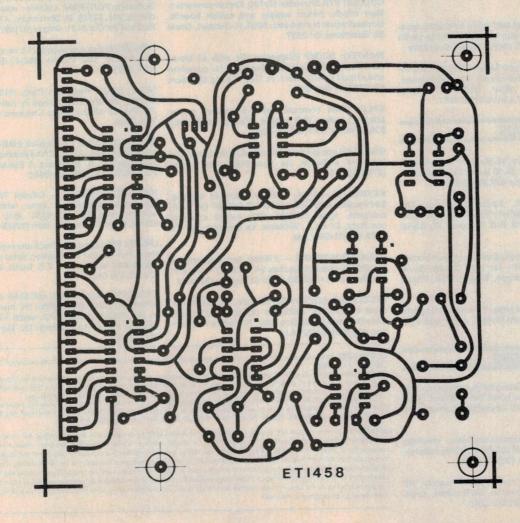
Cut a piece of film a little larger than the pc board and expose it to UV light through the magazine page. The non-emulsion side should be in contact with the page. This surface can be detected by picking

This surface can be detected by picking the film up by one corner — it will curl towards the emulsion side. Exposures of about 20 minutes are normally necessary.

The film can now be developed by placing it emulsion side up on a table, pouring some Scotchcal 8500 developer on the surface and rubbing it with a clean tissue.

Further information on Scotchcal and pcb manufacture can be found in the September and December 1977 issues of ETI.

Please note that occasionally lack of space may prohibit the printing of blue type behind all pcbs. In this case the reader must resort to more conventional photographic techniques for pcb manufacture.



WIREWART

Where readers can advertise — For Sale/Wanted/Swap/Join.

 We'll publish up to 24 words (maximum) totally free of charge for you, your club or your association. Copy must be with us by the 1st of the month preceding the month of issue. Please - please - print or type adverts clearly, otherwise it may not turn out as you intended! Every effort will be made to publish all adverts received however, no responsibility for so doing is accepted or implied. Private adverts only will be accepted. We reserve the right to refuse adverts considered unsuitable.

• Conditions: Name and address plus phone number (if required) must be included within the 24 words. Reasonable abbreviations, such as 25 W RMS or 240 Vac, count as one word. Adverts must relate to electronics, audio, communications, computing etcgeneral adverts cannot be accepted.

Send your advert to: ETI Mini-Mart, Modern Magazines 15 Boundary St, **Rushcutters Bay NSW 2011**

AUDIO

FOR SALE: 80 watt amplifier, \$179. Stereo graphic equaliser, 10 knobs per channel, \$169. Both new. (02)30-1839.

HAFLER DH 200 power amp, \$580; DH101 preamp, \$295; both as new cond. P. Prava, 4 Bandera Ave, Wagga NSW 2650. Ph (069) 31-1253.

BOOMERANG TAPE RECORDING CLUB: A club of tape recorder enthusiasts from all over Australia. Write to us for details: BTRC, P.O. Box 155, Carlingford 2118.

FOR SALE: JH manual belt drive turntable, complete with tonearm, mag. cartridge, stylus plinth and perspex cover. Ph. (049) 61-4854. Best offer.

JOIN the Australian Tape Makers Society, the international tapespondence club. Tapespondence, printed magazine, other benefits. Details, Secretary, P.O. Box 77, Umina NSW 2257.

MISCELLANEOUS

SELL: 11 Mini speakers, \$1.50 each; 28 electronic mags — ETI, EA, EE, \$2.00 each. Price includes p&p. K. Howell, P.O. Box 405, Renmark S.A.

COMPUTER DATING: Absolutely free! The ultimate in compatibility! Send stamped self-addressed envelope to 24A Simpson St, Bondi 2026. (Sydney only).

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FOR SALE: Quantity of older TV tuners and parts for tuner repairs, mainly Philips, Astor and Pye. Phone (02) 456-1211.

COMMUNICATIONS

VINTAGE VALVES — Going back a long time, \$10 ea. Can't guarantee ac types but filament types tested for continuity. R. Lockerbie, Cameron St, Merimbula 2548

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FERRIS SSB 5000 CB radio, power supply, RF signaliser, base power-mike, SWR meter, coax, etc, \$260 ono. Phone (02) 522-5270.

SPECTRUM ANALYSER for sale, Tektronix portable, 1-500 MHz range, \$2200. Ph. (08) 381-7419 a.h.

COMPUTERS

S100, 16K static RAM board, ETI-642, 450 nS, see running, \$225. Alan Peek, phone (02) 89-1450.

SORCERER 32K Mk 11. Light pen, TRS80 emulator, debug, assembler, disassembler. \$1370 o.n.o. Phone (02)427-5143 (ah).

COLOUR VTR Shibaden SV700, Xenon converted. New heads, power supply and colour boards. Unused since purchased, \$300. R. Stewart, Grant St, Goornong Vic 3557.

WANTED: SC/MP Evaluation Kit, with all literature. Need not be working but must be complete and circuit board intact. N. Guy, 7 King St, Boort Vic 3537. Phone 215.

SALE: ZX80 computer with programs, power adapter, video VHF output. Rarely used. Only \$260. Phone Tom (02)799-4091.

OSI-C2-4P complete with 8K RAM, printer port and loads of software. As new \$600 ono. Phone (07)286-2665.

KEYBOARD, HP-35, 106-key, numeric keypad, 32-function (LIST etc.), \$50. NS-SC/MP kit, built, manuals, spare SC/MP, \$80. 4-digit solenoid counters, \$4 ea. L. Williams, 6a Emerson Gr, SA 5073. (08)31-0467 a.h.

DREAM SOFTWARE — Listing and instructions for 6800 Invaders (requires 2K system) — \$5, and Car Rally (1K) - \$3.50 Janie St, Aspley Qld 4034.

EXCELLENT SOFTWARE for sale. Programs for most computers. Extra-low prices. Best quality diskettes and cassettes only. For more information ring (03)80-4221 a.h.

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WANTED TO BUY: Sorcerer 16K or System 80 16K, used but in good working order. Mario Tito, 207 Lauren St, Urangan Qld 4658. Phone (071)28-9527 a.h., (071)28-9133 b.h.

ZX80, including 55 programs on tape, improved patch cords, home-made power supply, cost \$350 sell \$250 ono. Phone (071)71-5939.

FOR SALE: Superboard II, 8K RAM, power supply, and many programs included, \$330. Tim Turner, 41 Glebe St, Glebe NSW 2037. (02) 692-0889.

TELETYPE ASR33, mint condition, complete with RS232 and 20 mA interfaces, plus full set of maintenance manuals, \$550. I.E. data entry terminals complete with self-contained 12-inch VDU and detachable full keyboard with auxiliary hex pad and cursor control keys. Manuals and circuit diagrams available, \$150 ea. Memorex 8" floppy disk drive with control electronics and 240 volt power supply, all mounted in 19-inch rack-mounting case with exhaust fan and provisions for second drive. Controller card for PDP8 system included. Ma included,\$250 ono. Phone 487-2972. Manuals

S100 8K STATIC RAM, 2102, base 2, inc. 450 ns, fully socketed, 4K selectable, documentation works! \$110. Ring Joe (02)30-1990 a.h. 11/51 Birriga Rd, Bellevue Hill NSW 2023.

SELL: COMPUCOLOR II, 32K, extended keyboard. Software FORTRAN; utilities: editor, assembler, debug, etc, \$2750. M. Donaldson, 47 Birdwood Rd, Holland Pk Qld 4121. Phone (07)397-5667.

DREAM 6800 power supply (1A version), fully built and tested, \$30. Phone (062)47-8548, Canberra ACT 2601.

SELL: Digital Research Corp. S100 static RAM board, fully stocked, burned in, runs 4 MHz, \$230. VK7FM, 23 McGuinness Crescent, Lenah Valley Tas. 7008.

MOTOROLA 6802 DZ kit plus EME-1 VDU board. power supplies, full ASCII keyboard and manuals \$220.Contact N. Bradicica, 8 Elaine St, Regents Park NSW 2143. (02)644-8949.

PRINTER/TERMINAL — Olivetti TE 318, RS232, 150 Baud, upper/lower case, with stand. See working with TRS-80, \$350. Also U/S unit as above, good i/face, \$75. Stan (02)88-4453 a.h.

DREAM 6800 Chip 8 instructions manual and software. For further information send stamped selfaddressed envelope to T.S. Huett, P.O. Box 520, Woodridge Qld 4114.

S100 CPU: Bought \$330, sell \$250, excellent condition. SD-SBC 100 2.5 MHz, 2K mon, 1K RAM, up to 8K PROM, parallel I/O, serial I/O, and more. Andrew Heard, 18 Elsworth Dv, Banksia Pk. (05) 264-1055.

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ONE OF THE HACKS who writes for Dregs was driving along one of Sydney's main thoroughfares the other week when he spied a billboard graffiti team at work. Traffic noticeably slowed, some drivers blew their horns, others shouted encouragement and generally, those going past took notice.

It seems billboard graffiti is no longer the spasmodic expression of consumer frustration or political activism. It's now a highly organised attack against specific targets. The bestknown team involved is 'Billboard Utilising Graffitists Against Unhealthy Promotions' and they sign their work B.U.G.A. U.P. an acronym which, to the billboard lessees, seems more than appropriate! The incident of seeing the team at work set this writer thinking what will they do next? Tossing the question amongst the lunchtime communal salad gave rise to the thought that B.U.G.A. U.P. were not optimising their efforts. The audience they reached via billboard graffiti was inevitably limited. About the most powerful advertising media available were undoubtedly TV and radio. With a slight change in name, technology and tactics, B.U.G.A. U.P. would become 'Broadcast Utilising Graffitists Against Unhealthy Promotions'.

Speculation on the activities of this hypothetical group produces some interesting (if not amusing) possibilities: radio and TV ads suddenly blotted out and replaced by a typical B.U.G.A. U.P. anti message, advertising jingles suddenly re-arranged mid-tune etc,

The problem arises, however, of how B.U.G.A. U.P. could go about such a campaign. Well, it seems there are two possibilities: infiltration and direct assault. A B.U.G.A. U.P. member (or members) could infiltrate certain advertising agencies and secretly 'doctor' radio station tapes and TV video tapes. Hmmm — bound to be



discovered as the infiltrator's profile is a little high. Same problem in infiltrating radio or TV stations. A more subtle method would be to infiltrate Telecom (maybe there's no need? ...) and 'patch' B.U.G.A. U.P. messages into the appropriate audio/video lines between studios and transmitters. This one isn't new and dates back to WWII, we're told.

So much for infiltration. Piracy is the second avenue and is as old as broadcasting itself. The idea is to have a rival, but mobile, transmitter and to broadcast B.U.G.A. U.P. messages virtually 'over the top' of transmissions during the time an advert is running. Tch, tch. One can imagine scenes of the B.U.G.A. U.P. broadcast van charging down the back streets of Blacktown hotly pursued by a Department of Communications van, police etc — a scenario straight out of the Keystone Cops!

No doubt B.U.G.A. U.P. has entertained such aspirations, but are stumped by the sheer scale of the technology and organisation, let alone the cost. But ... pocket computers were 'pie in the sky' a scant few years ago. Perhaps technological sophistication will reach the 'urban guerillas' yet.

DO WE'RE STILL getting feedback from the 'What's in a name?' article we published a few months ago in Dregs. This month B.W. Hopkins of Puckapunyal, Victoria, tells us of the Officer Commanding the Army School of Catering in 1980, whose name was Major Tucker. He also once knew a July issue — stay tuned!

Sergeant whose surname was Major, who (you guessed it) became promoted to Sergeant-Major Major. Not content with this, his brother was Major Major!

And one that we recently came across in the office here ... how about an ethnologist called Dr. Coon (nasty, nasty...).

Pun pulchritudes

At last! — some non-computing puns. Well, we do get the occasional one, but this time around we received a whole spate. Anyway, the computer puns were generally dismal - like John Macklin's entry: "On a clear disk you can seek forever". Trevor Bartlett's winning entry published in the February issue brought out a few puns in the (resistors, resistance) same vein. but not of the same quality, including another from Trevor himself.

Amongst the best of the noncomputing puns was one from Michael Clarke of Browns Plains in Qld: "Who's a cute little blonde capacitor with a set of electrodes enough to polarise a man's electrolyte - Farad Fawcett-Majors!" We have too many ladies on staff to let that one through, so this month's prize goes to lan Steele of Ringwood in Victoria with: "What do you get if you cross a positive air ion with a negative air ion — an air heir!" Clearly inspired by our April issue lead

Penultimate puns to appear in the

UNTILWE DEVELOPED THE STEREO GROOVE, HI-FI WAS PRETTY HO-HUM!



The world of hi-fi owes a lot to the original and continuing innovation of JVC. Few companies, if any, have done as much to help turn records and record-players into the virtual musical instruments they are today . . . or to lead the way in developing so many *firsts* in the more recent concepts of sound amplifiers, cassette decks and computer-designed speaker

systems. Hi-fi, as we know it today, had its beginnings in 1956, with JVC's development of the 45°/45° groove for stereo records. The fact that this system still remains as the world standard is, in itself, outstanding testimony to the technology of JVC. The development revolutionised not only the record-*making* industry, in which we've been involved since 1930; it also paved the way for enormous advancement in the design and engineering of record-playing equipment. Now, hi-fi has expanded to



R-S77. Super-A FM/AM Stereo receiver

embrace a wealth of highly-sophisticated electronic equipment; and it's not surprising that JVC has continued to play a leading role in so much of its development.



HR-3660 EA. VHS Colour Video Cassette recorder

THAT WASN'T OUR ONLY FIRST, EITHER.

We also pioneered Japan's television industry, introducing their first TV receiver just over 40 years ago. A more recent innovation is VHS, the home video recording system now gaining world-wide acceptance as *the* system for such equipment. In the course of staying ahead, we've introduced a number of world *firsts* of radical importance: the Quartz Lock turntable is one of them.

THE QUARTZ LOCK TURNTABLE. MANY TIMES MORE ACCURATE.

It stands to reason that if your equipment is at the top end of the range, then your turntable must be capable of comparable performances. Only Quartz Lock ensures this, tying the speed of the turntable to the unvarying pulse of the atom, and providing a level of accuracy far in excess of conventional turntables.



MORE MILESTONES IN HI-FI.

To match the superb quality of Quartz Lock, we produced the S.E.A. graphic equalizer system. Then we refined it to such a degree it even compensates for the effect your furniture has on sound when it leaves the speakers! To expand the capabilities of tape, we designed ANRS and



SEA-80. Stereo Graphic Equalizer

Super ANRS — automatic noise reduction systems which not only reduce distortion and 'hiss' but actually extend the dynamic range of the tape. Similarly, with speakers: at JVC we employ computers in their design to help provide the ultimate in sound reproduction.

AND NOW, SUPER-A.

In its own way, as significant a hi-fi development as the stereo groove. Imagine an amplifier which combines the *best* features of the two recognised amplifier classes (A and B) . . . an amp which combines the *efficiency* of one with the *low distortion* of the other. Some engineers said it couldn't be done; but not those at JVC. Enter the Super-A amplifier the *latest* JVC *first!*





THE FUTURE.

It's already with us. For instance, we were so far ahead in the new metal tape technology that our cassette decks were metal-compatible before the tapes were generally available. And now there's the JVC Electro-Dynamic Servo Tonearm, damping tonearm resonance by means of a purely electronic system and two 'thinking' linear motors. Who was it who dubbed JVC, 'the innovators'?



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 DID YOU WIN? The results are on Teletext.
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